



# SPEC® CFP2006 Result

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Dell Inc.

**SPECfp<sub>®</sub>\_rate2006 = 1490**

PowerEdge M830 (Intel Xeon E5-4640 v4, 2.10 GHz)

**SPECfp\_rate\_base2006 = 1450**

CPU2006 license: 55

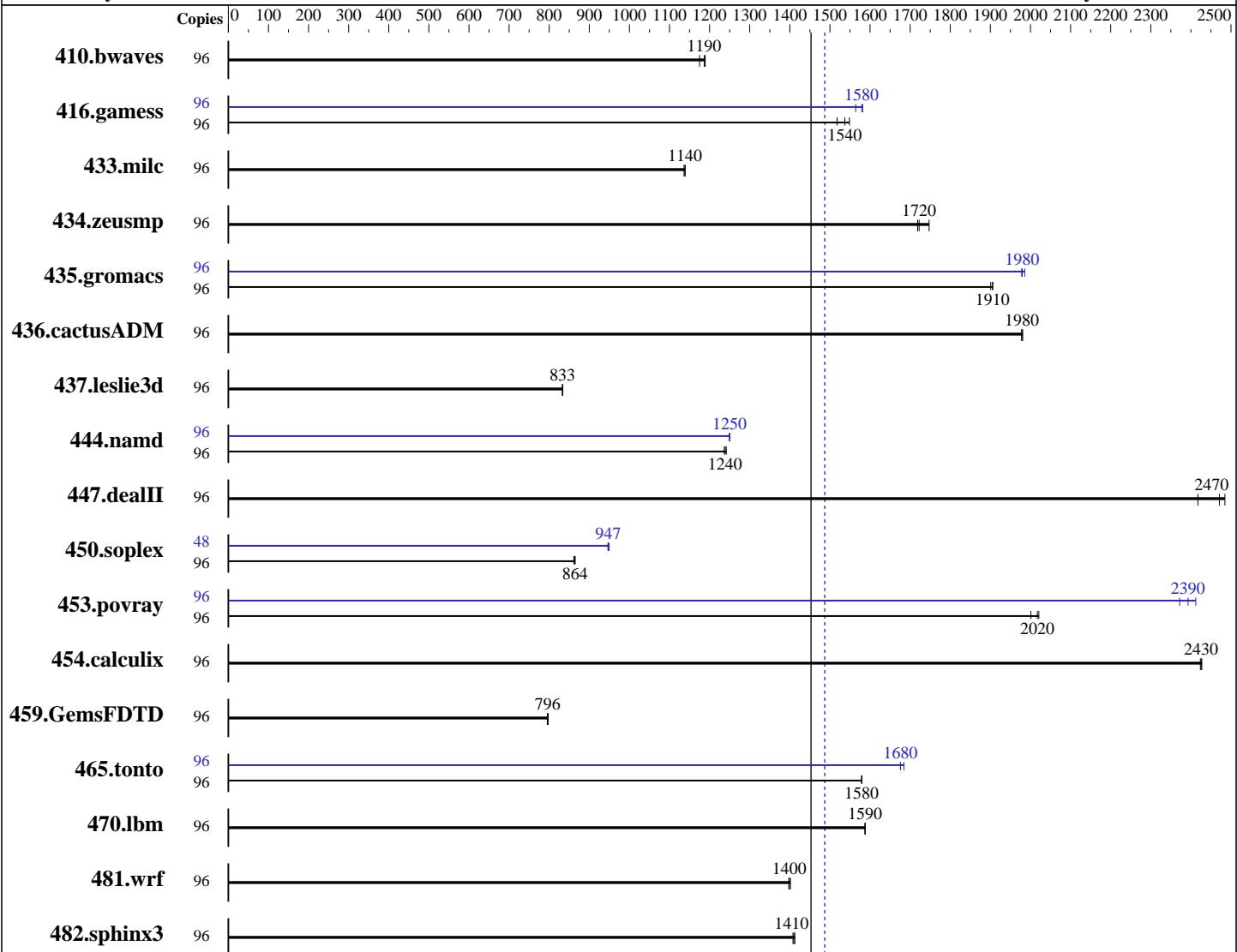
Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Apr-2016

Hardware Availability: Jun-2016

Software Availability: Mar-2016



**SPECfp\_rate\_base2006 = 1450**

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## Hardware

CPU Name: Intel Xeon E5-4640 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.60 GHz  
 CPU MHz: 2100  
 FPU: Integrated  
 CPU(s) enabled: 48 cores, 4 chips, 12 cores/chip, 2 threads/core  
 CPU(s) orderable: 2,4 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

## Software

Operating System: SUSE Linux Enterprise Server 12 SP1 3.12.49-11-default  
 Compiler: C/C++: Version 16.0.2.181 of Intel C++ Studio XE for Linux;  
 Auto Parallel: No  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit

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L3 Cache: 30 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 512 GB (32 x 16 GB 2Rx8 PC4-2400T-R, running at 2133 MHz)  
 Disk Subsystem: 1 x 800 GB SATA SSD  
 Other Hardware: None

Other Software: None

## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	96	1110	1170	<b>1099</b>	<b>1190</b>	1097	1190	96	1110	1170	<b>1099</b>	<b>1190</b>	1097	1190	1097	1190
416.gamess	96	<b>1223</b>	<b>1540</b>	1214	1550	1238	1520	96	1202	1560	<b>1190</b>	<b>1580</b>	1188	1580	1188	1580
433.milc	96	<b>774</b>	<b>1140</b>	776	1140	774	1140	96	<b>774</b>	<b>1140</b>	776	1140	774	1140	774	1140
434.zeusmp	96	500	1750	<b>507</b>	<b>1720</b>	508	1720	96	500	1750	<b>507</b>	<b>1720</b>	508	1720	508	1720
435.gromacs	96	360	1910	361	1900	<b>360</b>	<b>1910</b>	96	347	1980	345	1990	<b>346</b>	<b>1980</b>	345	1980
436.cactusADM	96	<b>580</b>	<b>1980</b>	579	1980	580	1980	96	<b>580</b>	<b>1980</b>	579	1980	580	1980	580	1980
437.leslie3d	96	1084	833	1082	834	<b>1083</b>	<b>833</b>	96	1084	833	1082	834	<b>1083</b>	<b>833</b>	1083	<b>833</b>
444.namd	96	623	1240	<b>622</b>	<b>1240</b>	620	1240	96	616	1250	<b>616</b>	<b>1250</b>	616	1250	616	1250
447.dealII	96	454	2420	442	2480	<b>444</b>	<b>2470</b>	96	454	2420	442	2480	<b>444</b>	<b>2470</b>	442	2480
450.soplex	96	<b>926</b>	<b>864</b>	926	864	929	862	48	<b>423</b>	<b>947</b>	423	946	422	950	423	946
453.povray	96	255	2000	253	2020	<b>253</b>	<b>2020</b>	96	212	2410	<b>213</b>	<b>2390</b>	215	2370	215	2370
454.calculix	96	<b>327</b>	<b>2430</b>	326	2430	327	2420	96	<b>327</b>	<b>2430</b>	326	2430	327	2420	326	2430
459.GemsFDTD	96	1281	795	<b>1280</b>	<b>796</b>	1277	797	96	1281	795	<b>1280</b>	<b>796</b>	1277	797	1277	797
465.tonto	96	598	1580	598	1580	<b>598</b>	<b>1580</b>	96	561	1680	564	1680	<b>564</b>	<b>1680</b>	564	1680
470.lbm	96	<b>831</b>	<b>1590</b>	831	1590	831	1590	96	<b>831</b>	<b>1590</b>	831	1590	831	1590	831	1590
481.wrf	96	765	1400	<b>767</b>	<b>1400</b>	767	1400	96	765	1400	<b>767</b>	<b>1400</b>	767	1400	767	1400
482.sphinx3	96	<b>1327</b>	<b>1410</b>	1329	1410	1325	1410	96	<b>1327</b>	<b>1410</b>	1329	1410	1325	1410	1325	1410

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS settings:

Snoop Mode set to Cluster on Die

Virtualization Technology disabled

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## Platform Notes (Continued)

System Profile set to custom  
CPU Performance set to Hardware P States  
C States set to Autonomous  
C1E disabled  
Energy Efficient Turbo disabled  
Uncore Frequency set to Dynamic  
Energy Efficiency Policy set to Balanced Performance  
Memory Patrol Scrub disabled  
Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6914  
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
running on linux-9j51 Sat Apr 30 06:55:33 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) CPU E5-4640 v4 @ 2.10GHz  
 4 "physical id"s (chips)  
 96 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
 cpu cores : 12  
 siblings : 24  
 physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13  
 physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13  
 physical 2: cores 0 1 2 3 4 5 8 9 10 11 12 13  
 physical 3: cores 0 1 2 3 4 5 8 9 10 11 12 13  
cache size : 15360 KB

From /proc/meminfo  
MemTotal: 529325756 kB  
HugePages\_Total: 0  
Hugepagesize: 2048 kB

/usr/bin/lsb\_release -d  
SUSE Linux Enterprise Server 12 SP1

From /etc/\*release\* /etc/\*version\*  
SuSE-release:  
 SUSE Linux Enterprise Server 12 (x86\_64)  
VERSION = 12  
PATCHLEVEL = 1  
# This file is deprecated and will be removed in a future service pack or release.  
# Please check /etc/os-release for details about this release.  
os-release:  
 NAME="SLES"  
VERSION="12-SP1"  
VERSION\_ID="12.1"  
PRETTY\_NAME="SUSE Linux Enterprise Server 12 SP1"

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## Platform Notes (Continued)

```
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"
```

```
uname -a:
Linux linux-9j51 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Apr 29 18:02
```

```
SPEC is set to: /root/cpu2006-1.2
Filesystem      Type  Size  Used   Avail Use% Mounted on
/dev/sda2        xfs   225G  8.8G  217G   4% /
Additional information from dmidecode:
```

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```
BIOS Dell Inc. 2.0.1 03/31/2016
```

Memory:

```
16x 00AD00B300AD HMA82GR7MFR8N-UH 16 GB 2 rank 2400 MHz, configured at 2133
MHz
16x 00CE00B300CE M393A2K43BB1-CRC 16 GB 2 rank 2400 MHz, configured at 2133
MHz
16x Not Specified Not Specified
```

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64:/root/cpu2006-1.2/sh"

Binaries compiled on a system with 1x Intel Core i7-4790K CPU + 32GB memory using RedHat EL 7.2 glibc 2.17

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1> /proc/sys/vm/drop_caches
```

runspec command invoked through numactl i.e.:

```
numactl --interleave=all runspec <etc>
```

## Base Compiler Invocation

C benchmarks:

```
icc -m64
```

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## Base Compiler Invocation (Continued)

C++ benchmarks:

icpc -m64

Fortran benchmarks:

fort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3



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## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -D\_FILE\_OFFSET\_BITS=64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

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## Peak Optimization Flags (Continued)

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -unroll14 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll12  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll14 -auto  
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.20151006.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.20151006.xml>

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