SPEC® CINT2006 Result
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Lenovo Group Limited

Lenovo System x3850 X6
(2.00 GHz, Intel Xeon E7-4809 v3)

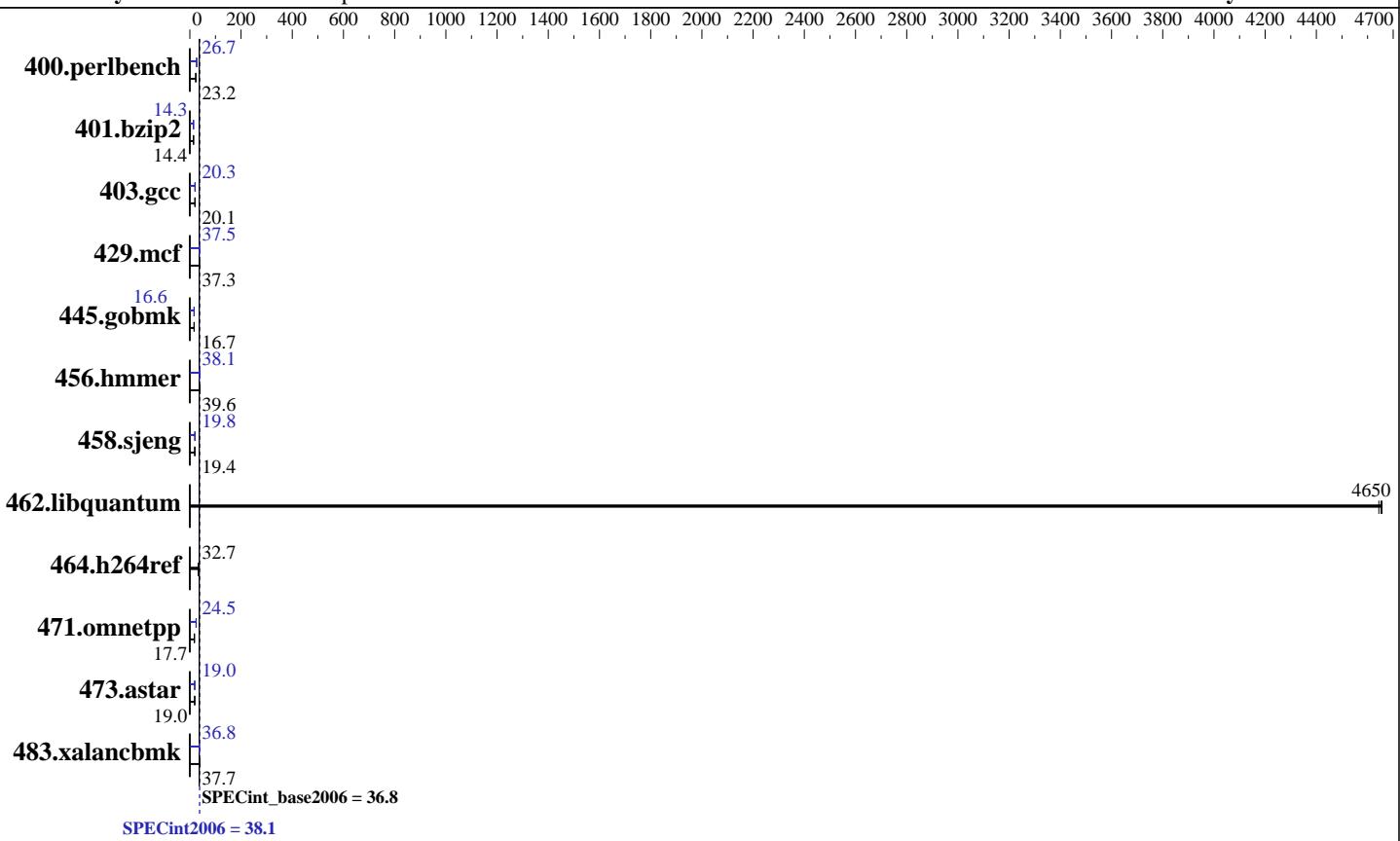SPECint®2006 = 38.1

SPECint_base2006 = 36.8

CPU2006 license: 9017
Test sponsor: Lenovo Group Limited
Tested by: Lenovo Group Limited

Test date: May-2016
Hardware Availability: Jul-2015
Software Availability: Dec-2015

Benchmark	Base	Peak
400.perlbench	23.2	26.7
401.bzip2	14.4	14.3
403.gcc	20.1	20.3
429.mcf	37.3	37.5
445.gobmk	16.7	16.6
456.hmmer	39.6	38.1
458.sjeng	19.4	19.8
462.libquantum	4650	
464.h264ref	32.7	
471.omnetpp	17.7	24.5
473.astar	19.0	19.0
483.xalancbmk	37.7	36.8

SPECint_base2006 = 36.8
SPECint2006 = 38.1

Hardware
CPU Name: Intel Xeon E7-4809 v3
CPU Characteristics:
CPU MHz: 2000
FPU: Integrated
CPU(s) enabled: 32 cores, 4 chips, 8 cores/chip
CPU(s) orderable: 2,4 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 20 MB I+D on chip per chip
Other Cache: None
Memory: 512 GB (32 x 16 GB 2Rx4 PC4-2133P-R, running at 1333 MHz)
Disk Subsystem: 1 x 800 GB SATA SSD
Other Hardware: None

Software
Operating System: SUSE Linux Enterprise Server 12 SP1 (x86_64) Kernel 3.12.49-11-default
Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux
Auto Parallel: Yes
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V10.0

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	424	23.0	418	23.4	**420**	**23.2**	366	26.7	367	26.6	**366**	**26.7**
401.bzip2	674	14.3	**672**	**14.4**	671	14.4	**676**	**14.3**	675	14.3	676	14.3
403.gcc	**400**	**20.1**	400	20.1	399	20.2	393	20.5	400	20.1	**396**	**20.3**
429.mcf	248	36.7	242	37.7	**245**	**37.3**	**243**	**37.5**	244	37.4	243	37.5
445.gobmk	**629**	**16.7**	628	16.7	629	16.7	**631**	**16.6**	631	16.6	631	16.6
456.hmmer	236	39.6	235	39.6	**235**	**39.6**	246	37.9	**245**	**38.1**	244	38.2
458.sjeng	625	19.4	625	19.4	**625**	**19.4**	**612**	**19.8**	612	19.8	611	19.8
462.libquantum	4.46	4640	**4.45**	**4650**	4.45	4660	4.46	4640	**4.45**	**4650**	4.45	4660
464.h264ref	**678**	**32.7**	676	32.7	680	32.6	**678**	**32.7**	676	32.7	680	32.6
471.omnetpp	354	17.6	349	17.9	**353**	**17.7**	255	24.5	256	24.4	**255**	**24.5**
473.astar	371	18.9	368	19.1	**369**	**19.0**	372	18.8	365	19.2	**370**	**19.0**
483.xalancbmk	182	37.9	**183**	**37.7**	184	37.5	**187**	**36.8**	189	36.6	187	36.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

```
BIOS Configuration:
 Operating Mode set to "Maximum Performance"
 Hyper-Threading set to Disabled
Sysinfo program /home/cpu2006-1.2-ic15.0/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on Draco-SLES12SP1 Mon May 30 02:05:00 2016

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E7-4809 v3 @ 2.00GHz
      4 "physical id"s (chips)
      32 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 8
```

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Platform Notes (Continued)

```
      siblings  : 8
      physical 0: cores 0 1 2 3 4 5 6 7
      physical 1: cores 0 1 2 3 4 5 6 7
      physical 2: cores 0 1 2 3 4 5 6 7
      physical 3: cores 0 1 2 3 4 5 6 7
   cache size : 20480 KB

From /proc/meminfo
   MemTotal:       529167244 kB
   HugePages_Total:        0
   Hugepagesize:        2048 kB

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 12 (x86_64)
      VERSION = 12
      PATCHLEVEL = 1
      # This file is deprecated and will be removed in a future service pack or
      release.
      # Please check /etc/os-release for details about this release.
   os-release:
      NAME="SLES"
      VERSION="12-SP1"
      VERSION_ID="12.1"
      PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
      ID="sles"
      ANSI_COLOR="0;32"
      CPE_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:
   Linux Draco-SLES12SP1 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
   (8d714a0) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 May 30 01:45

SPEC is set to: /home/cpu2006-1.2-ic15.0
   Filesystem      Type   Size  Used Avail Use% Mounted on
   /dev/sdc4       xfs    701G   21G  680G   3% /home
Additional information from dmidecode:

   Warning: Use caution when you interpret this section. The 'dmidecode' program
   reads system data which is "intended to allow hardware to be accurately
   determined", but the intent may not be met, as there are frequent changes to
   hardware, firmware, and the "DMTF SMBIOS" standard.

   BIOS IBM -[A9E131VUS-3.00]- 05/16/2016
   Memory:
   64x NO DIMM Unknown
   32x Samsung M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz, configured at 1333 MHz

(End of data from sysinfo program)
```

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General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2006-1.2-ic15.0/libs/32:/home/cpu2006-1.2-ic15.0/libs/64:/home/cpu2006-1.2-ic15.0/sh"
OMP_NUM_THREADS = "32"

 Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB
 memory using RedHat EL 7.0
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:
    icc -m64

C++ benchmarks:
    icpc -m64

Base Portability Flags

```
      400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
         401.bzip2: -DSPEC_CPU_LP64
           403.gcc: -DSPEC_CPU_LP64
           429.mcf: -DSPEC_CPU_LP64
         445.gobmk: -DSPEC_CPU_LP64
         456.hmmer: -DSPEC_CPU_LP64
          458.sjeng: -DSPEC_CPU_LP64
      462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
        464.h264ref: -DSPEC_CPU_LP64
        471.omnetpp: -DSPEC_CPU_LP64
          473.astar: -DSPEC_CPU_LP64
      483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
    -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:
    -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
    -Wl,-z,muldefs -L/sh -lsmartheap64

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Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc  -m64`

400.perlbench: `icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32`

445.gobmk: `icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32`

C++ benchmarks (except as noted below):
`icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32`

473.astar: `icpc -m64`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
403.gcc: `-DSPEC_CPU_LP64`
429.mcf: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
464.h264ref: `-DSPEC_CPU_LP64`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-opt-prefetch -ansi-alias`

401.bzip2: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32`
`-opt-prefetch -ansi-alias`

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Peak Optimization Flags (Continued)

403.gcc: `-xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc`
`-opt-malloc-options=3 -auto-ilp32`

429.mcf: `-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel`
`-opt-prefetch -auto-p32`

445.gobmk: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)`
`-ansi-alias`

456.hmmer: `-xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32`
`-ansi-alias`

458.sjeng: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-unroll4`

462.libquantum: `basepeak = yes`

464.h264ref: `basepeak = yes`

C++ benchmarks:

471.omnetpp: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-opt-ra-region-strategy=block          -ansi-alias`
`-Wl,-z,muldefs -L/sh -lsmartheap`

473.astar: `-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch`
`-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64`

483.xalancbmk: `-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch`
`-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-BDW-B.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-BDW-B.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jun 30 14:07:36 2016 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 14 June 2016.