



SPEC® CFP2006 Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL160 Gen9

(2.00 GHz, Intel Xeon E5-2660 v4)

SPECfp®2006 =

114

SPECfp_base2006 =

108

CPU2006 license: 3

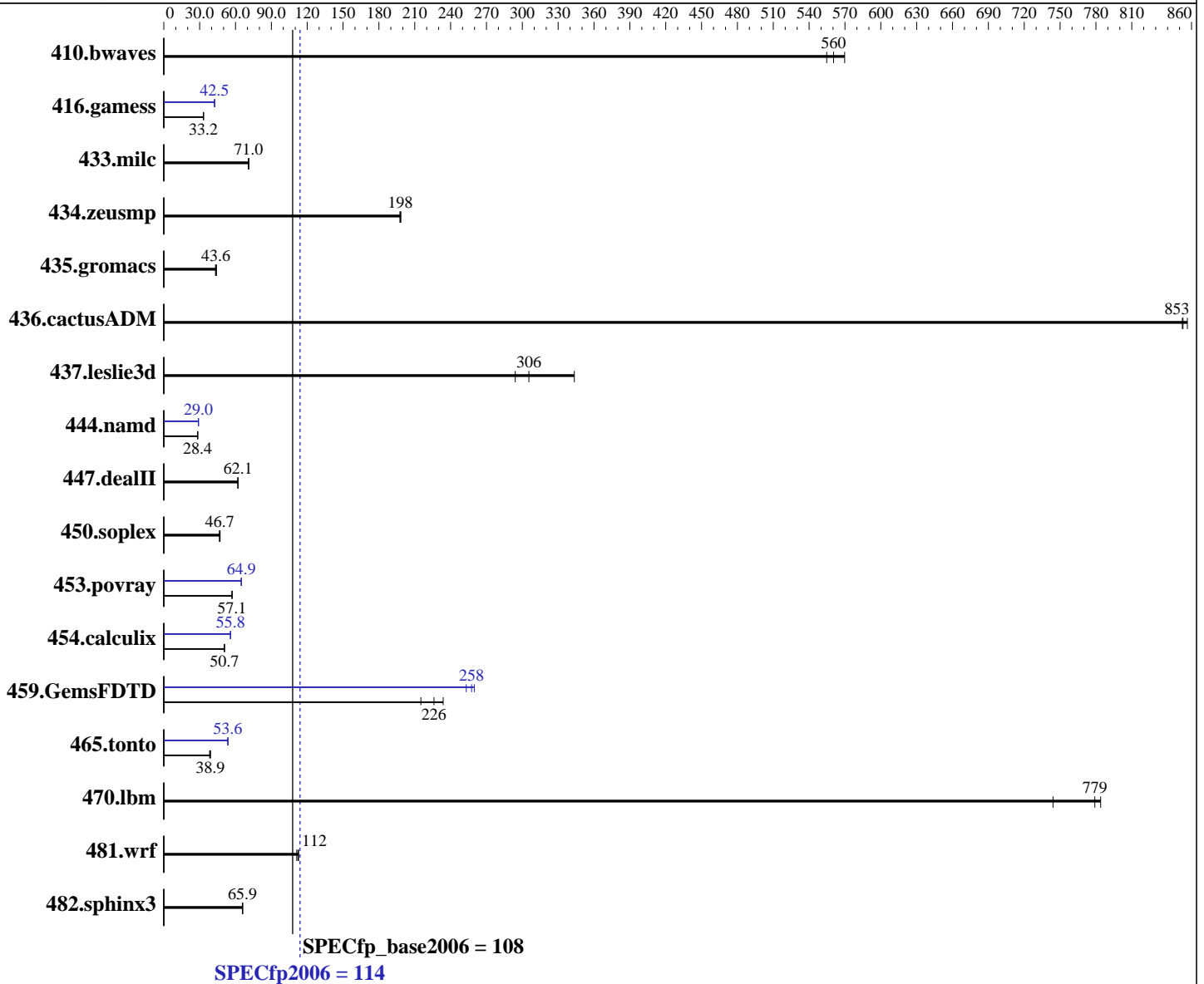
Test sponsor: HPE

Tested by: HPE

Test date: May-2016

Hardware Availability: Mar-2016

Software Availability: Nov-2015



Hardware

CPU Name: Intel Xeon E5-2660 v4
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 28 cores, 2 chips, 14 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server release 7.2 (Maipo)
 Kernel 3.10.0-327.el7.x86_64
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: xfs

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L3 Cache: 35 MB I+D on chip per chip
Other Cache: None
Memory: 512 GB (16 x 32 GB 2Rx4 PC4-2400T-R, running at 2133 MHz)
Disk Subsystem: 2 x 400 GB SAS SSD, RAID 1
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<u>24.2</u>	<u>560</u>	24.5	555	23.9	570	<u>24.2</u>	<u>560</u>	24.5	555	23.9	570
416.gamess	<u>589</u>	<u>33.2</u>	589	33.3	591	33.1	<u>460</u>	<u>42.5</u>	461	42.4	460	42.5
433.milc	129	71.2	<u>129</u>	<u>71.0</u>	130	70.8	129	71.2	<u>129</u>	<u>71.0</u>	130	70.8
434.zeusmp	46.1	198	45.9	198	<u>46.0</u>	<u>198</u>	46.1	198	45.9	198	<u>46.0</u>	<u>198</u>
435.gromacs	<u>164</u>	<u>43.6</u>	165	43.3	162	44.1	<u>164</u>	<u>43.6</u>	165	43.3	162	44.1
436.cactusADM	14.0	857	14.0	852	<u>14.0</u>	<u>853</u>	14.0	857	14.0	852	<u>14.0</u>	<u>853</u>
437.leslie3d	27.4	344	<u>30.8</u>	<u>306</u>	32.0	294	27.4	344	<u>30.8</u>	<u>306</u>	32.0	294
444.namd	<u>283</u>	<u>28.4</u>	283	28.3	283	28.4	277	29.0	<u>277</u>	<u>29.0</u>	277	29.0
447.dealII	185	62.0	<u>184</u>	<u>62.1</u>	184	62.1	185	62.0	<u>184</u>	<u>62.1</u>	184	62.1
450.soplex	177	47.1	179	46.6	<u>178</u>	<u>46.7</u>	177	47.1	179	46.6	<u>178</u>	<u>46.7</u>
453.povray	93.3	57.0	<u>93.2</u>	<u>57.1</u>	93.0	57.2	<u>81.9</u>	<u>64.9</u>	82.2	64.7	81.9	65.0
454.calculix	163	50.7	163	50.8	<u>163</u>	<u>50.7</u>	148	55.8	148	55.9	<u>148</u>	<u>55.8</u>
459.GemsFDTD	<u>46.9</u>	<u>226</u>	45.4	234	49.3	215	<u>41.2</u>	<u>258</u>	41.9	253	40.8	260
465.tonto	252	39.1	<u>253</u>	<u>38.9</u>	257	38.3	184	53.5	183	53.7	<u>183</u>	<u>53.6</u>
470.lbm	17.5	784	<u>17.6</u>	<u>779</u>	18.5	744	17.5	784	<u>17.6</u>	<u>779</u>	18.5	744
481.wrf	<u>99.5</u>	<u>112</u>	99.0	113	100	111	<u>99.5</u>	<u>112</u>	99.0	113	100	111
482.sphinx3	<u>296</u>	<u>65.9</u>	296	65.9	295	66.0	<u>296</u>	<u>65.9</u>	296	65.9	295	66.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Platform Notes

BIOS Configuration:

HP Power Profile set to Custom

HP Power Regulator to HP Static High Performance Mode

Minimum Processor Idle Power Core C-State set to C1E State

Minimum Processor Idle Power Package C-State set to No Package State

Collaborative Power Control set to Disabled

Thermal Configuration set to Maximum Cooling

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Platform Notes (Continued)

Processor Power and Utilization Monitoring set to Disabled
 Memory Refresh Rate set to 1x Refresh
 Intel Hyperthreading set to Enabled
 QPI Snoop Configuration set to Home Snoop
 Sysinfo program /cpu2006-HP/config/sysinfo.rev6914
 \$Rev: 6914 \$ \$Date:: 2014-06-25 # \$ e3fbb8667b5a285932ceab81e28219e1
 running on DL160Gen9 Thu May 12 12:13:55 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name      : Intel(R) Xeon(R) CPU E5-2660 v4@ 2.00GHz
 2 "physical id"s (chips)
 56 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores    : 14
  siblings    : 28
  physical 0   : cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
  physical 1   : cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
cache size     : 35840 KB
```

```
From /proc/meminfo
MemTotal:      527932428 kB
HugePages_Total: 0
Hugepagesize:  2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.2 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.2"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server
```

```
uname -a:
Linux DL160Gen9 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29 EDT 2015
x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 May 10 11:27

SPEC is set to: /cpu2006-HP

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
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Platform Notes (Continued)

/dev/sda4 xfs 368G 115G 253G 32% /
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP U20 02/22/2016

Memory:

16x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz, configured at 2133 MHz

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 512 GB and the dmidecode description should have one line reading as:
16x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz, configured at 2133 MHz

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"

OMP_NUM_THREADS = "28"

LD_LIBRARY_PATH = "/cpu2006-HP/libs/32:/cpu2006-HP/libs/64:/cpu2006-HP/sh"

Binaries compiled on a system with 1x Intel Xeon E5-2660 v4 CPU + 128GB memory using RedHat EL 7.2

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64

416.gamess: -DSPEC_CPU_LP64

433.milc: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

```

434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deallI: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias -fp-model fast=2
-qopt-prefetch-issue-excl-hint

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias
-fp-model fast=2
-qopt-prefetch-issue-excl-hint

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-fp-model fast=2
-qopt-prefetch-issue-excl-hint

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias -fp-model fast=2
-qopt-prefetch-issue-excl-hint

```

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

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Peak Compiler Invocation (Continued)

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

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Peak Optimization Flags (Continued)

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Compiler-Flags-Intel-V1.2-HSW-revF.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/HP-Compiler-Flags-Intel-V1.2-HSW-revF.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>

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