



SPEC® CINT2006 Result

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Huawei

SPECint_rate2006 = Not Run

Huawei RH8100 V3 (Intel Xeon E7-8890 v4)

SPECint_rate_base2006 = 7070

CPU2006 license: 3175

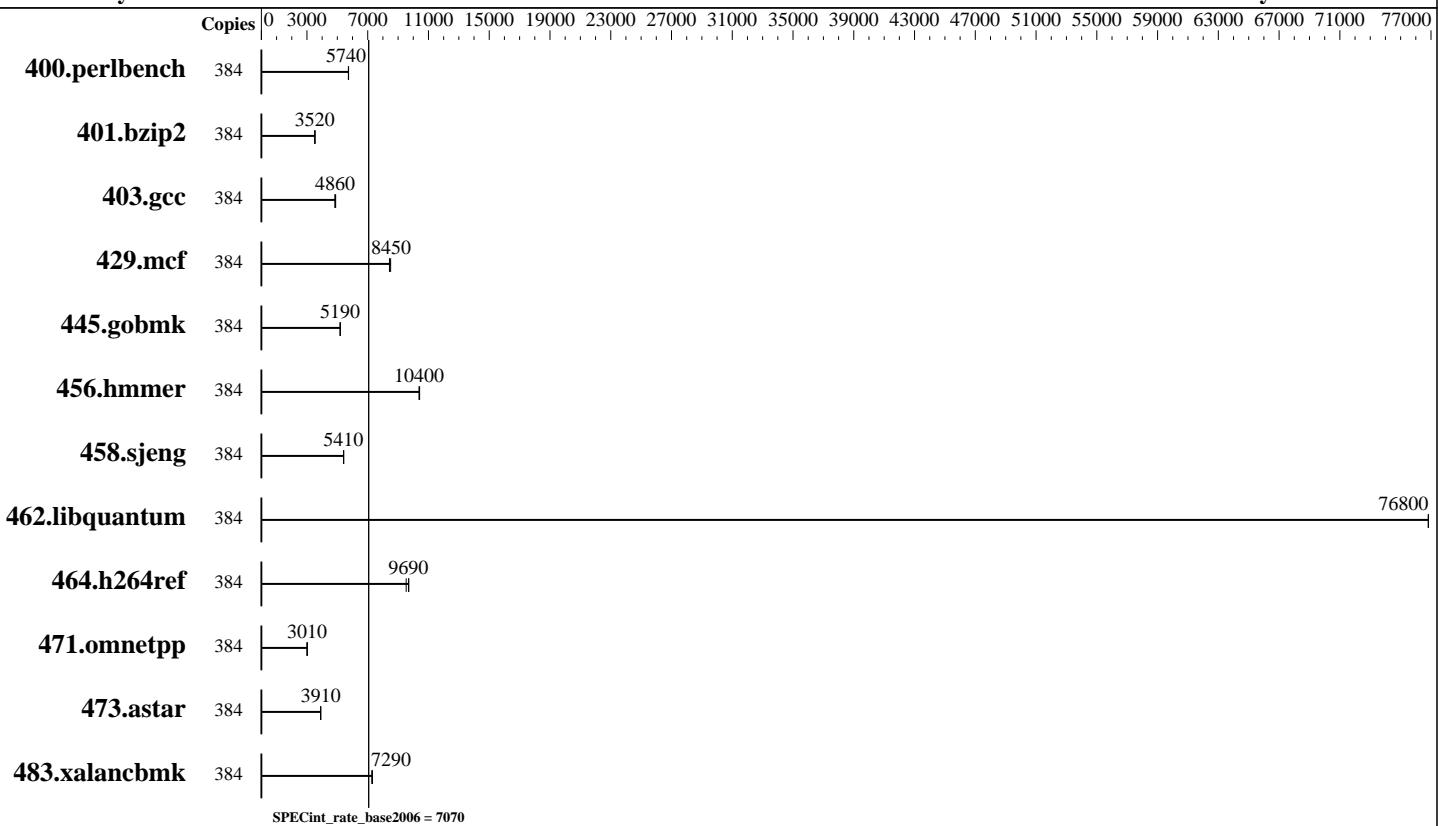
Test date: May-2016

Test sponsor: Huawei

Hardware Availability: Jun-2016

Tested by: Huawei

Software Availability: Oct-2015



Hardware

CPU Name: Intel Xeon E7-8890 v4
CPU Characteristics: Intel Turbo Boost Technology up to 3.40 GHz
CPU MHz: 2200
FPU: Integrated
CPU(s) enabled: 192 cores, 8 chips, 24 cores/chip, 2 threads/core
CPU(s) orderable: 4,8 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 60 MB I+D on chip per chip
Other Cache: None
Memory: 1 TB (64 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)
Disk Subsystem: 2 x 600GB SAS, 10K RPM
Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 7.2 (Maipo)
Compiler: 3.10.0-327.el7.x86_64
C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux
Auto Parallel: No
File System: tmpfs
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V10.2



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	384	655	5730	654	5740	654	5740							
401.bzip2	384	1052	3520	1053	3520	1051	3530							
403.gcc	384	632	4890	638	4840	636	4860							
429.mcf	384	415	8430	412	8510	415	8450							
445.gobmk	384	776	5190	776	5190	776	5190							
456.hammer	384	344	10400	345	10400	345	10400							
458.sjeng	384	858	5410	858	5420	859	5410							
462.libquantum	384	104	76800	104	76800	104	76800							
464.h264ref	384	877	9690	876	9700	891	9530							
471.omnetpp	384	797	3010	797	3010	796	3020							
473.astar	384	692	3890	690	3910	690	3910							
483.xalancbmk	384	364	7280	362	7310	363	7290							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Turbo mode set with:

cpupower -c all frequency-set -g performance

Tmpfs filesystem can be set with:

mkdir /home/shm

mount -t tmpfs -o size=2000g,rw tmpfs /home/shm

Process tuning setting:

echo 50000 > /proc/sys/kernel/sched_cfs_bandwidth_slice_us

echo 240000000 > /proc/sys/kernel/sched_latency_ns

echo 5000000 > /proc/sys/kernel/sched_migration_cost_ns

echo 100000000 > /proc/sys/kernel/sched_min_granularity_ns

echo 150000000 > /proc/sys/kernel/sched_wakeup_granularity_ns

Platform Notes

BIOS configuration:

Set Power Efficiency Mode to Performance

Set Lock_step to disabled

Baseboard Management Controller used to adjust the fan speed to 100%

Set C-State to C0/C1

Sysinfo program /home/shm/spec/config/sysinfo.rev6914

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Platform Notes (Continued)

\$Rev: 6914 \$ \$Date::: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
running on localhost.localdomain Wed May 11 21:44:43 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E7-8890 v4 @ 2.20GHz
8 "physical id"s (chips)
384 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 24
siblings : 48
physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26
27 28 29
physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26
27 28 29
physical 2: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26
27 28 29
physical 3: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26
27 28 29
physical 4: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26
27 28 29
physical 5: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26
27 28 29
physical 6: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26
27 28 29
physical 7: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26
27 28 29
cache size : 61440 KB

From /proc/meminfo
MemTotal: 1056704636 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.2 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.2"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server
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Platform Notes (Continued)

```
uname -a:  
Linux localhost.localdomain 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29  
EDT 2015 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 May 10 23:35
```

```
SPEC is set to: /home/shm/spec  
Filesystem      Type  Size  Used Avail Use% Mounted on  
tmpfs          tmpfs  2.0T  5.3G  2.0T   1% /home/shm
```

Additional information from dmidecode:

```
Warning: Use caution when you interpret this section. The 'dmidecode' program  
reads system data which is "intended to allow hardware to be accurately  
determined", but the intent may not be met, as there are frequent changes to  
hardware, firmware, and the "DMTF SMBIOS" standard.
```

```
BIOS American Megatrends Inc. 5.11 02/05/2016
```

Memory:

```
128x NO DIMM NO DIMM  
64x Samsung M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz, configured at 1600 MHz
```

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of
memory is 1 TB and the dmidecode description should have two lines reading as:

```
128x NO DIMM NO DIMM  
64x Samsung M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz, configured at 1600 MHz
```

General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/home/shm/spec/libs/32:/home/shm/spec/libs/64:/home/shm/spec/sh"
```

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1> /proc/sys/vm/drop_caches
```

runspec command invoked through numactl i.e.:

```
numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:

```
icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
```

C++ benchmarks:

```
icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
```



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Base Portability Flags

```
400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
 401.bzip2: -D_FILE_OFFSET_BITS=64
 403.gcc: -D_FILE_OFFSET_BITS=64
 429.mcf: -D_FILE_OFFSET_BITS=64
 445.gobmk: -D_FILE_OFFSET_BITS=64
 456.hammer: -D_FILE_OFFSET_BITS=64
 458.sjeng: -D_FILE_OFFSET_BITS=64
 462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
 464.h264ref: -D_FILE_OFFSET_BITS=64
 471.omnetpp: -D_FILE_OFFSET_BITS=64
 473.astar: -D_FILE_OFFSET_BITS=64
 483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.2-BDW-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.2-BDW-RevG.xml>



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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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