



# SPEC® CFP2006 Result

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## NEC Corporation

**SPECfp®2006 = 112**

Express5800/R120g-1M (Intel Xeon E5-2650 v4)

**SPECfp\_base2006 = 107**

CPU2006 license: 9006

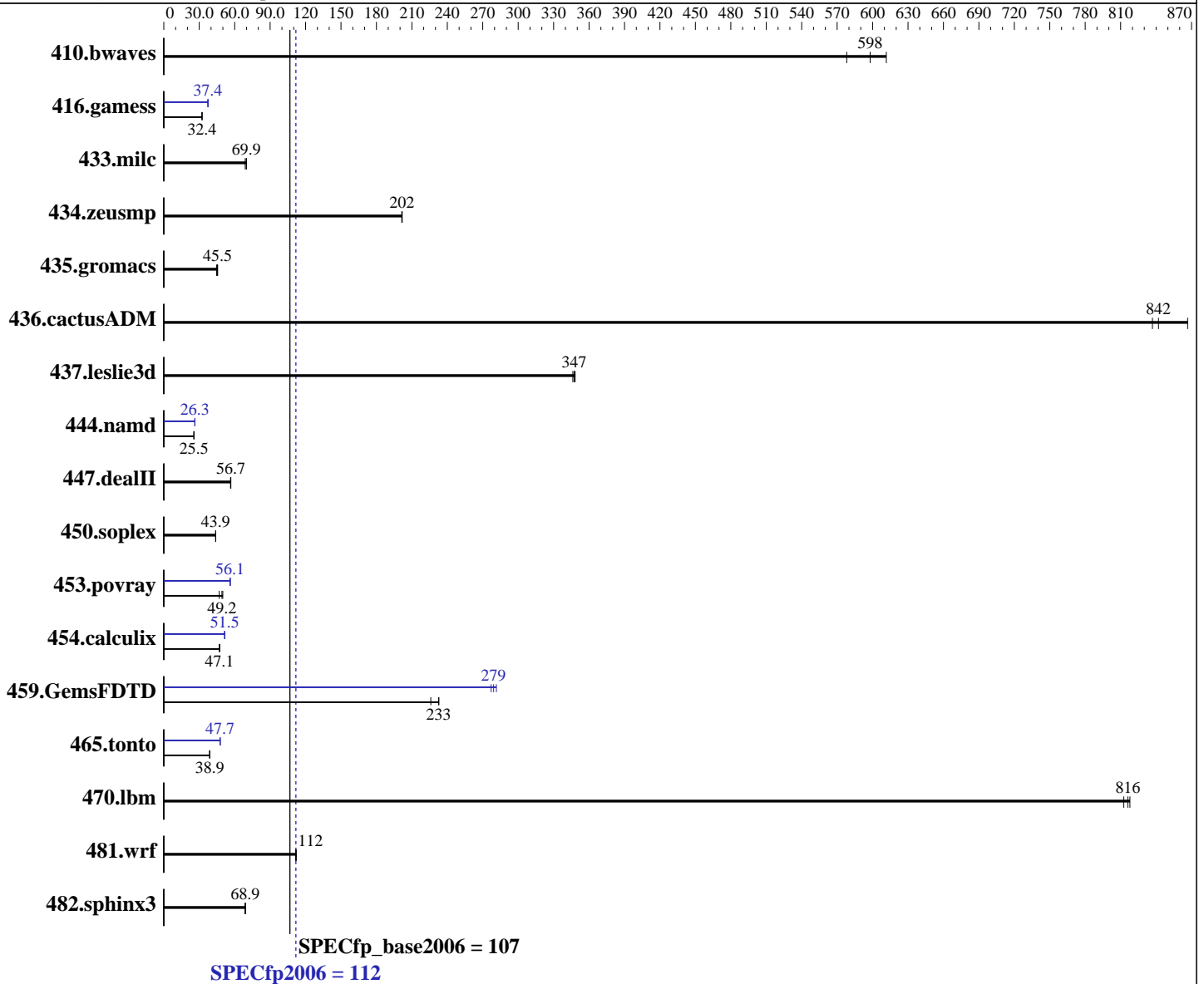
Test date: May-2016

Test sponsor: NEC Corporation

Hardware Availability: Apr-2016

Tested by: NEC Corporation

Software Availability: Jan-2016



**Hardware**

CPU Name: Intel Xeon E5-2650 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.90 GHz  
 CPU MHz: 2200  
 FPU: Integrated  
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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**Software**

Operating System: Red Hat Enterprise Linux Server release 7.2 (Maipo)  
 Kernel 3.10.0-327.4.5.el7.x86\_64  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;  
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4

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L3 Cache: 30 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)  
 Disk Subsystem: 1 x 1 TB SATA, 7200 RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	22.2	612	23.5	578	<u>22.7</u>	<u>598</u>	22.2	612	23.5	578	<u>22.7</u>	<u>598</u>
416.gamess	606	32.3	<u>604</u>	<u>32.4</u>	604	32.4	<u>524</u>	<u>37.4</u>	524	37.3	523	37.4
433.milc	131	69.9	<u>131</u>	<u>69.9</u>	133	68.9	131	69.9	<u>131</u>	<u>69.9</u>	133	68.9
434.zeusmp	45.2	202	45.1	202	<u>45.1</u>	<u>202</u>	45.2	202	45.1	202	<u>45.1</u>	<u>202</u>
435.gromacs	160	44.7	<u>157</u>	<u>45.5</u>	157	45.5	160	44.7	<u>157</u>	<u>45.5</u>	157	45.5
436.cactusADM	14.3	837	13.8	867	<u>14.2</u>	<u>842</u>	14.3	837	13.8	867	<u>14.2</u>	<u>842</u>
437.leslie3d	27.1	346	<u>27.1</u>	<u>347</u>	27.0	348	27.1	346	<u>27.1</u>	<u>347</u>	27.0	348
444.namd	314	25.5	315	25.5	<u>314</u>	<u>25.5</u>	305	26.3	305	26.3	<u>305</u>	<u>26.3</u>
447.dealII	<u>202</u>	<u>56.7</u>	202	56.8	203	56.5	<u>202</u>	<u>56.7</u>	202	56.8	203	56.5
450.soplex	190	44.0	190	43.9	<u>190</u>	<u>43.9</u>	190	44.0	190	43.9	<u>190</u>	<u>43.9</u>
453.povray	107	49.9	<u>108</u>	<u>49.2</u>	113	46.9	<u>94.8</u>	<u>56.1</u>	95.0	56.0	94.3	56.4
454.calculix	175	47.1	174	47.3	<u>175</u>	<u>47.1</u>	160	51.6	160	51.5	<u>160</u>	<u>51.5</u>
459.GemsFDTD	45.6	233	46.9	226	<u>45.6</u>	<u>233</u>	38.3	277	<u>38.0</u>	<u>279</u>	37.7	282
465.tonto	252	39.0	254	38.8	<u>253</u>	<u>38.9</u>	206	47.7	206	47.7	<u>206</u>	<u>47.7</u>
470.lbm	16.8	818	16.9	813	<u>16.8</u>	<u>816</u>	16.8	818	16.9	813	<u>16.8</u>	<u>816</u>
481.wrf	<u>99.7</u>	<u>112</u>	99.7	112	100	112	<u>99.7</u>	<u>112</u>	99.7	112	100	112
482.sphinx3	283	68.9	282	69.1	<u>283</u>	<u>68.9</u>	283	68.9	282	69.1	<u>283</u>	<u>68.9</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Settings:  
 Power Management Policy: Custom  
 Energy Performance: Performance  
 Patrol Scrub: Disabled  
 Snoop Mode: Home Snoop with Directory  
 Hyper-Threading: Disabled



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## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact"

LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

OMP\_NUM\_THREADS = "24"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

The Express5800/R120g-1M (Intel Xeon E5-2650 v4) and the Express5800/R120g-2M (Intel Xeon E5-2650 v4) models are electronically equivalent. The results have been measured on the Express5800/R120g-2M (Intel Xeon E5-2650 v4) model.

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.lelie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.deallI: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX

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## Base Portability Flags (Continued)

482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch  
-ansi-alias

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

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## Peak Optimization Flags (Continued)

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

### C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4  
-ansi-alias

### Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

### Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

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## Peak Optimization Flags (Continued)

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-120g-RevC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-120g-RevC.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.

Report generated on Wed Jun 1 19:10:55 2016 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 1 June 2016.