



SPEC® CINT2006 Result

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NEC Corporation

SPECint®2006 = 72.2

Express5800/R120g-2M (Intel Xeon E5-2690 v4)

SPECint_base2006 = 69.1

CPU2006 license: 9006

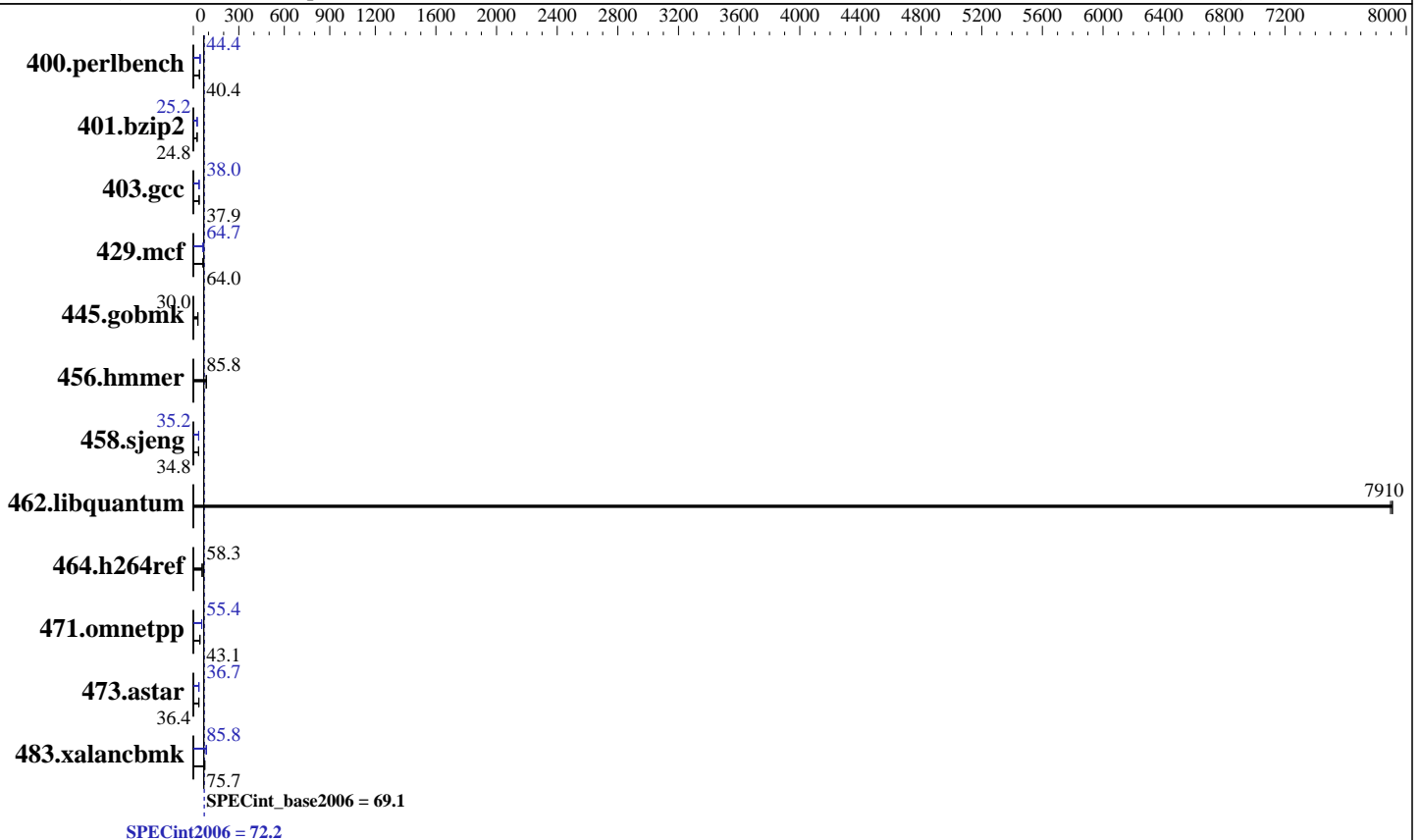
Test date: May-2016

Test sponsor: NEC Corporation

Hardware Availability: Apr-2016

Tested by: NEC Corporation

Software Availability: Jan-2016



Hardware

CPU Name: Intel Xeon E5-2690 v4
 CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz
 CPU MHz: 2600
 FPU: Integrated
 CPU(s) enabled: 28 cores, 2 chips, 14 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 35 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)
 Disk Subsystem: 1 x 1 TB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 7.2 (Maipo)
 Kernel 3.10.0-327.4.5.el7.x86_64
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.2



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	239	40.8	242	40.3	<u>242</u>	<u>40.4</u>	<u>220</u>	<u>44.4</u>	220	44.3	220	44.4
401.bzip2	391	24.7	389	24.8	<u>390</u>	<u>24.8</u>	381	25.3	383	25.2	<u>382</u>	<u>25.2</u>
403.gcc	213	37.9	<u>213</u>	<u>37.9</u>	213	37.8	<u>212</u>	<u>38.0</u>	213	37.7	212	38.0
429.mcf	<u>142</u>	<u>64.0</u>	143	63.8	141	64.8	142	64.2	<u>141</u>	<u>64.7</u>	141	64.7
445.gobmk	350	30.0	350	29.9	<u>350</u>	<u>30.0</u>	350	30.0	350	29.9	<u>350</u>	<u>30.0</u>
456.hammer	<u>109</u>	<u>85.8</u>	109	85.6	108	86.1	<u>109</u>	<u>85.8</u>	109	85.6	108	86.1
458.sjeng	<u>348</u>	<u>34.8</u>	348	34.8	348	34.8	343	35.2	<u>343</u>	<u>35.2</u>	344	35.1
462.libquantum	<u>2.62</u>	<u>7910</u>	2.62	7910	2.62	7900	<u>2.62</u>	<u>7910</u>	2.62	7910	2.62	7900
464.h264ref	380	58.3	<u>380</u>	<u>58.3</u>	380	58.3	380	58.3	<u>380</u>	<u>58.3</u>	380	58.3
471.omnetpp	<u>145</u>	<u>43.1</u>	144	43.5	145	43.1	<u>113</u>	<u>55.4</u>	113	55.2	112	55.6
473.astar	195	36.1	<u>193</u>	<u>36.4</u>	192	36.5	191	36.7	192	36.5	<u>192</u>	<u>36.7</u>
483.xalancbmk	90.9	75.9	<u>91.1</u>	<u>75.7</u>	96.0	71.9	80.3	85.9	<u>80.4</u>	<u>85.8</u>	80.5	85.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Settings:
 Power Management Policy: Custom
 Energy Performance: Performance
 Patrol Scrub: Disabled
 Snoop Mode: Home Snoop with Directory
 Hyper-Threading: Disabled

General Notes

Environment variables set by runspec before the start of the run:
 KMP_AFFINITY = "granularity=fine,compact"
 LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"
 OMP_NUM_THREADS = "28"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

The Express5800/R120g-1M (Intel Xeon E5-2690 v4) and

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General Notes (Continued)

the Express5800/R120g-2M (Intel Xeon E5-2690 v4) models are electronically equivalent. The results have been measured on the Express5800/R120g-2M (Intel Xeon E5-2690 v4) model.

Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca



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Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

C++ benchmarks (except as noted below):

icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch
-ansi-alias
401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div
-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32
-opt-prefetch -ansi-alias
403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32
429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel
-opt-prefetch -auto-p32

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Peak Optimization Flags (Continued)

445.gobmk: basepeak = yes

456.hmmmer: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll14

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-120g-RevC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-120g-RevC.xml>



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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Wed Jun 1 19:09:13 2016 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 1 June 2016.