



# SPEC<sup>®</sup> CFP2006 Result

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## NEC Corporation

**SPECfp<sup>®</sup>2006 = 96.1**

Express5800/R120g-1M (Intel Xeon E5-2623 v4)

**SPECfp\_base2006 = 91.9**

CPU2006 license: 9006

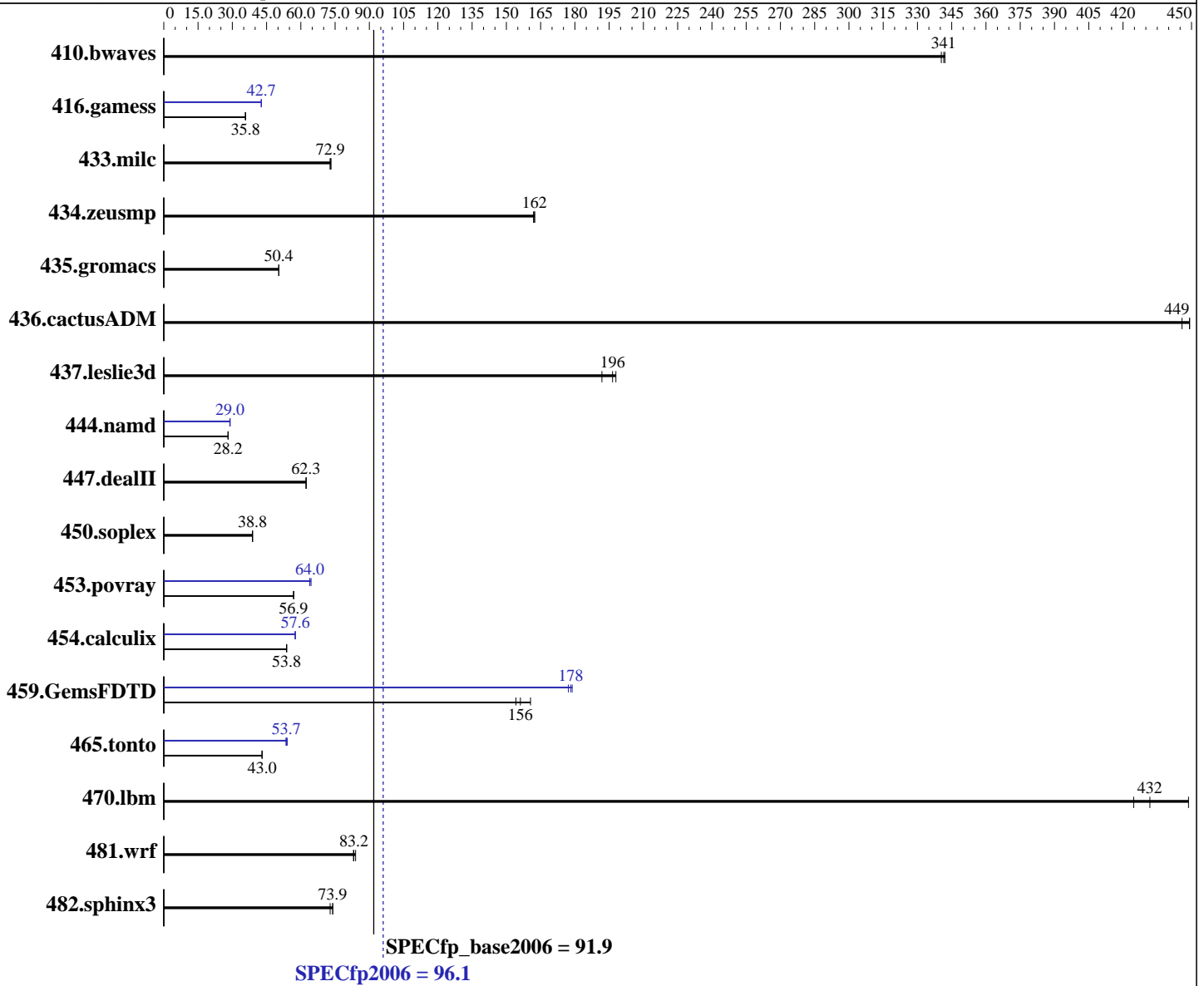
Test date: Apr-2016

Test sponsor: NEC Corporation

Hardware Availability: Apr-2016

Tested by: NEC Corporation

Software Availability: Jan-2016



### Hardware

CPU Name: Intel Xeon E5-2623 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz  
 CPU MHz: 2600  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 7.2 (Maipo)  
 Kernel 3.10.0-327.4.5.el7.x86\_64  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;  
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4

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L3 Cache: 10 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R, running at 2133 MHz)  
 Disk Subsystem: 1 x 1 TB SATA, 7200 RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	39.7	342	<b>39.8</b>	<b>341</b>	39.9	340	39.7	342	<b>39.8</b>	<b>341</b>	39.9	340
416.gamess	546	35.8	<b>547</b>	<b>35.8</b>	549	35.7	459	42.6	458	42.8	<b>459</b>	<b>42.7</b>
433.milc	<b>126</b>	<b>72.9</b>	125	73.2	126	72.8	<b>126</b>	<b>72.9</b>	125	73.2	126	72.8
434.zeusmp	56.2	162	56.0	162	<b>56.1</b>	<b>162</b>	56.2	162	56.0	162	<b>56.1</b>	<b>162</b>
435.gromacs	142	50.4	<b>142</b>	<b>50.4</b>	142	50.4	142	50.4	<b>142</b>	<b>50.4</b>	142	50.4
436.cactusADM	26.6	449	26.8	446	<b>26.6</b>	<b>449</b>	26.6	449	26.8	446	<b>26.6</b>	<b>449</b>
437.leslie3d	<b>47.8</b>	<b>196</b>	49.0	192	47.5	198	<b>47.8</b>	<b>196</b>	49.0	192	47.5	198
444.namd	285	28.1	<b>285</b>	<b>28.2</b>	285	28.2	276	29.0	277	28.9	<b>276</b>	<b>29.0</b>
447.dealII	184	62.3	183	62.4	<b>184</b>	<b>62.3</b>	184	62.3	183	62.4	<b>184</b>	<b>62.3</b>
450.soplex	<b>215</b>	<b>38.8</b>	214	38.9	215	38.8	<b>215</b>	<b>38.8</b>	214	38.9	215	38.8
453.povray	93.4	56.9	93.7	56.8	<b>93.6</b>	<b>56.9</b>	82.5	64.5	83.3	63.9	<b>83.1</b>	<b>64.0</b>
454.calculix	<b>153</b>	<b>53.8</b>	153	53.8	153	53.7	143	57.6	<b>143</b>	<b>57.6</b>	144	57.5
459.GemsFDTD	66.1	161	<b>68.0</b>	<b>156</b>	68.8	154	<b>59.5</b>	<b>178</b>	59.9	177	59.3	179
465.tonto	228	43.1	229	43.0	<b>229</b>	<b>43.0</b>	182	54.0	184	53.5	<b>183</b>	<b>53.7</b>
470.lbm	<b>31.8</b>	<b>432</b>	30.6	449	32.4	425	<b>31.8</b>	<b>432</b>	30.6	449	32.4	425
481.wrf	135	83.0	133	84.0	<b>134</b>	<b>83.2</b>	135	83.0	133	84.0	<b>134</b>	<b>83.2</b>
482.sphinx3	<b>264</b>	<b>73.9</b>	263	74.0	268	72.8	<b>264</b>	<b>73.9</b>	263	74.0	268	72.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Settings:  
 Power Management Policy: Custom  
 Energy Performance: Performance  
 Patrol Scrub: Disabled  
 Snoop Mode: Home Snoop with Directory  
 Hyper-Threading: Disabled



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## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact"

LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

OMP\_NUM\_THREADS = "8"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

The Express5800/R120g-1M (Intel Xeon E5-2623 v4) and the Express5800/R120g-2M (Intel Xeon E5-2623 v4) models are electronically equivalent. The results have been measured on the Express5800/R120g-2M (Intel Xeon E5-2623 v4) model.

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.lelie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.deallI: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX

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## Base Portability Flags (Continued)

482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch  
-ansi-alias

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

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## Peak Optimization Flags (Continued)

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

### C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4  
-ansi-alias

### Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

### Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

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## Peak Optimization Flags (Continued)

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-120g-RevC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-120g-RevC.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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