



# SPEC® CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise  
(Test Sponsor: HPE)

ProLiant BL460c Gen9  
(2.20 GHz, Intel Xeon E5-2650 v4)

**SPECfp®2006 = 110**

**SPECfp\_base2006 = 105**

CPU2006 license: 3

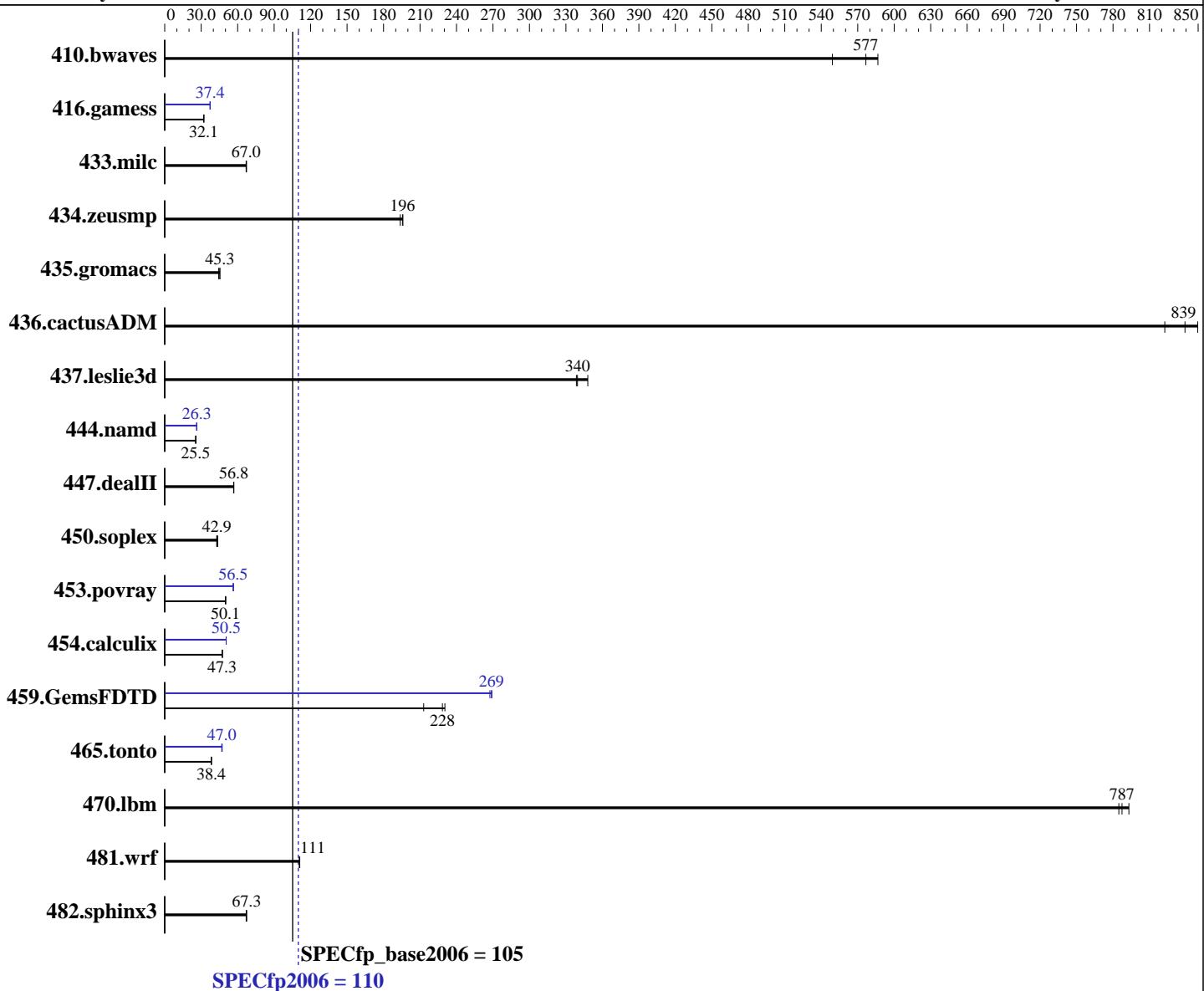
Test sponsor: HPE

Tested by: HPE

Test date: Mar-2016

Hardware Availability: Mar-2016

Software Availability: Dec-2015



## Hardware

CPU Name: Intel Xeon E5-2650 v4  
CPU Characteristics: Intel Turbo Boost Technology up to 2.90 GHz  
CPU MHz: 2200  
FPU: Integrated  
CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip, 2 threads/core  
CPU(s) orderable: 1,2 chip  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core

## Software

Operating System: SuSE Linux Enterprise 12 (x86\_64) SP1  
Kernel 3.12.49-11-default  
Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;  
Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux  
Auto Parallel: Yes  
File System: xfs  
System State: Run level 5 (multi-user, w/GUI)

Continued on next page

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise  
(Test Sponsor: HPE)

ProLiant BL460c Gen9  
(2.20 GHz, Intel Xeon E5-2650 v4)

**SPECfp2006 = 110**

**SPECfp\_base2006 = 105**

**CPU2006 license:** 3

**Test sponsor:** HPE

**Tested by:** HPE

**Test date:** Mar-2016

**Hardware Availability:** Mar-2016

**Software Availability:** Dec-2015

L3 Cache: 30 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (8 x 32 GB 2Rx4 PC4-2400T-R)  
Disk Subsystem: 2 x 500 GB SAS 7.2 K HDD, RAID 1  
Other Hardware: None

Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	23.2	587	24.7	549	<b>23.6</b>	<b>577</b>	23.2	587	24.7	549	<b>23.6</b>	<b>577</b>
416.gamess	607	32.2	610	32.1	<b>609</b>	<b>32.1</b>	525	37.3	524	37.4	<b>524</b>	<b>37.4</b>
433.milc	137	67.0	136	67.3	<b>137</b>	<b>67.0</b>	137	67.0	136	67.3	<b>137</b>	<b>67.0</b>
434.zeusmp	46.4	196	<b>46.5</b>	<b>196</b>	47.0	194	46.4	196	<b>46.5</b>	<b>196</b>	47.0	194
435.gromacs	161	44.3	157	45.4	<b>158</b>	<b>45.3</b>	161	44.3	157	45.4	<b>158</b>	<b>45.3</b>
436.cactusADM	14.5	823	<b>14.2</b>	<b>839</b>	14.1	850	14.5	823	<b>14.2</b>	<b>839</b>	14.1	850
437.leslie3d	27.0	348	<b>27.7</b>	<b>340</b>	27.8	339	27.0	348	<b>27.7</b>	<b>340</b>	27.8	339
444.namd	314	25.6	314	25.5	<b>314</b>	<b>25.5</b>	305	26.3	305	26.3	<b>305</b>	<b>26.3</b>
447.dealII	201	56.8	202	56.7	<b>201</b>	<b>56.8</b>	201	56.8	202	56.7	<b>201</b>	<b>56.8</b>
450.soplex	<b>194</b>	<b>42.9</b>	191	43.6	195	42.8	<b>194</b>	<b>42.9</b>	191	43.6	195	42.8
453.povray	106	50.2	<b>106</b>	<b>50.1</b>	106	50.0	94.5	56.3	<b>94.2</b>	<b>56.5</b>	94.2	56.5
454.calculix	174	47.5	174	47.3	<b>174</b>	<b>47.3</b>	163	50.5	<b>163</b>	<b>50.5</b>	163	50.6
459.GemsFDTD	49.8	213	46.0	230	<b>46.4</b>	<b>228</b>	<b>39.5</b>	<b>269</b>	39.4	269	39.7	267
465.tonto	256	38.5	<b>257</b>	<b>38.4</b>	257	38.3	<b>209</b>	<b>47.0</b>	208	47.3	209	47.0
470.lbm	17.3	793	<b>17.4</b>	<b>787</b>	17.5	785	17.3	793	<b>17.4</b>	<b>787</b>	17.5	785
481.wrf	101	111	<b>101</b>	<b>111</b>	101	111	101	111	<b>101</b>	<b>111</b>	101	111
482.sphinx3	289	67.3	<b>290</b>	<b>67.3</b>	291	67.0	<b>289</b>	<b>67.3</b>	<b>290</b>	<b>67.3</b>	291	67.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

## Platform Notes

BIOS Configuration:

Intel Hyperthreading Option set to Enabled

Power Profile set to Custom

Power Regulator set to Static High Performance Mode

Minimum Processor Idle Power Core C-State set to C1E State

Minimum Processor Idle Power Package C-State set to No Package State

Collaborative Power Control set to Disabled

QPI Snoop Configuration set to Home Snoop

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant BL460c Gen9

(2.20 GHz, Intel Xeon E5-2650 v4)

**SPECfp2006 =**

**110**

**SPECfp\_base2006 =**

**105**

**CPU2006 license:** 3

**Test sponsor:** HPE

**Tested by:** HPE

**Test date:** Mar-2016

**Hardware Availability:** Mar-2016

**Software Availability:** Dec-2015

## Platform Notes (Continued)

Thermal Configuration set to Maximum Cooling

Processor Power and Utilization Monitoring set to Disabled

Memory Refresh Rate set to 1x Refresh

Sysinfo program /home/cpuv1.5/cpu2006/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date::: 2014-06-25 #\\$ e3fbb8667b5a285932ceab81e28219e1

running on bl460c2-gen9-b Mon Apr 18 09:51:55 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2650 v4@ 2.20GHz
        2 "physical id"s (chips)
        48 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
        cpu cores : 12
        siblings : 24
        physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
        physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
cache size : 30720 KB
```

```
From /proc/meminfo
MemTotal:      264326032 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP1
```

```
From /etc/*release* /etc/*version*
SuSE-release:
        SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
        NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"
```

```
uname -a:
Linux bl460c2-gen9-b 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux
Continued on next page
```



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant BL460c Gen9

(2.20 GHz, Intel Xeon E5-2650 v4)

**SPECfp2006 =**

**110**

**SPECfp\_base2006 =**

**105**

**CPU2006 license:** 3

**Test sponsor:** HPE

**Tested by:** HPE

**Test date:**

Mar-2016

**Hardware Availability:** Mar-2016

**Software Availability:** Dec-2015

## Platform Notes (Continued)

run-level 5 Apr 18 09:12

SPEC is set to: /home/cpuv1.5/cpu2006

Filesystem Type Size Used Avail Use% Mounted on  
/dev/sda4 xfs 424G 75G 349G 18% /home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP I36 02/22/2016

Memory:

8x UNKNOWN NOT AVAILABLE

8x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 256 GB and the dmidecode description should have one line reading as:

8x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact,1,0"

LD\_LIBRARY\_PATH = "/home/cpuv1.5/cpu2006/libs/32:/home/cpuv1.5/cpu2006/libs/64:/home/cpuv1.5/cpu2006/sh"

OMP\_NUM\_THREADS = "24"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant BL460c Gen9

(2.20 GHz, Intel Xeon E5-2650 v4)

**SPECfp2006 =**

**110**

**SPECfp\_base2006 =**

**105**

**CPU2006 license:** 3

**Test sponsor:** HPE

**Tested by:** HPE

**Test date:**

Mar-2016

**Hardware Availability:** Mar-2016

**Software Availability:** Dec-2015

## Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias
```

## Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant BL460c Gen9

(2.20 GHz, Intel Xeon E5-2650 v4)

**SPECfp2006 =**

**110**

**SPECfp\_base2006 =**

**105**

**CPU2006 license:** 3

**Test sponsor:** HPE

**Tested by:** HPE

**Test date:**

Mar-2016

**Hardware Availability:** Mar-2016

**Software Availability:** Dec-2015

## Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll14  
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant BL460c Gen9

(2.20 GHz, Intel Xeon E5-2650 v4)

**SPECfp2006 =**

**110**

**SPECfp\_base2006 =**

**105**

**CPU2006 license:** 3

**Test sponsor:** HPE

**Tested by:** HPE

**Test date:**

Mar-2016

**Hardware Availability:** Mar-2016

**Software Availability:** Dec-2015

## Peak Optimization Flags (Continued)

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll12  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>  
<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>  
<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.

Report generated on Tue May 17 16:51:12 2016 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 17 May 2016.