SPEC® CFP2006 Result
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Cisco Systems

Cisco UCS B200 M4 (Intel Xeon E5-2690 v4, 2.60 GHz)

$SPECfp^{®}\_rate2006 = 967$
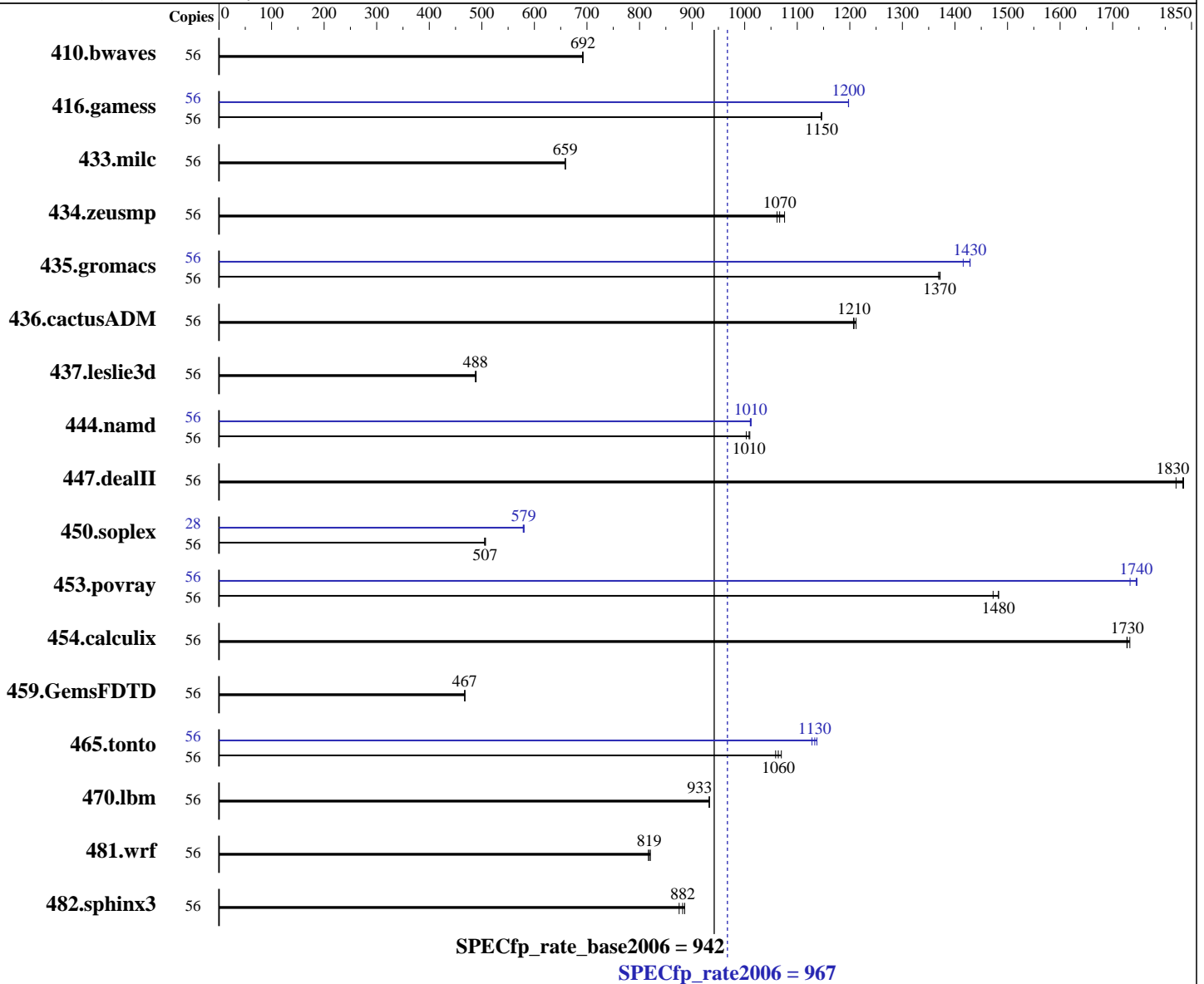
SPECfp_rate_base2006 = 942

CPU2006 license: 9019

Test sponsor: Cisco Systems

Tested by: Cisco Systems

Test date: Apr-2016

Hardware Availability: Apr-2016

Software Availability: Dec-2015

SPECfp_rate_base2006 = 942

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Hardware

CPU Name: Intel Xeon E5-2690 v4

CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz

CPU MHz: 2600

FPU: Integrated

CPU(s) enabled: 28 cores, 2 chips, 14 cores/chip, 2 threads/core

CPU(s) orderable: 1,2 chips

Primary Cache: 32 KB I + 32 KB D on chip per core

Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 12 SP1 (x86_64) 3.12.49-11-default

Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux

Auto Parallel: No

File System: xfs

System State: Run level 3 (multi-user)

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L3 Cache:	35 MB I+D on chip per chip	Base Pointers:	32/64-bit
Other Cache:	None	Peak Pointers:	32/64-bit
Memory:	256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)	Other Software:	None
Disk Subsystem:	1 x 600 GB SAS, 10K RPM		
Other Hardware:	None		

Results Table

Benchmark	Copies	Base Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Peak Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	56	1099	692	**1099**	**692**	1100	692	56	1099	692	**1099**	**692**	1100	692
416.gamess	56	**957**	**1150**	957	1150	957	1150	56	**916**	**1200**	915	1200	916	1200
433.milc	56	780	659	780	659	**780**	**659**	56	780	659	780	659	**780**	**659**
434.zeusmp	56	474	1080	480	1060	**478**	**1070**	56	474	1080	480	1060	**478**	**1070**
435.gromacs	56	**292**	**1370**	292	1370	291	1370	56	280	1430	**280**	**1430**	282	1420
436.cactusADM	56	554	1210	552	1210	**554**	**1210**	56	554	1210	552	1210	**554**	**1210**
437.leslie3d	56	1079	488	1076	489	**1079**	**488**	56	1079	488	1076	489	**1079**	**488**
444.namd	56	445	1010	**445**	**1010**	448	1000	56	444	1010	444	1010	**444**	**1010**
447.dealII	56	352	1820	**349**	**1830**	349	1830	56	352	1820	**349**	**1830**	349	1830
450.soplex	56	925	505	**922**	**507**	921	507	28	402	580	404	579	**403**	**579**
453.povray	56	**201**	**1480**	201	1480	202	1470	56	172	1730	171	1750	**171**	**1740**
454.calculix	56	267	1730	**267**	**1730**	267	1730	56	267	1730	**267**	**1730**	267	1730
459.GemsFDTD	56	1270	468	1271	467	**1271**	**467**	56	1270	468	1271	467	**1271**	**467**
465.tonto	56	520	1060	**518**	**1060**	515	1070	56	489	1130	484	1140	**486**	**1130**
470.lbm	56	**825**	**933**	825	933	825	933	56	**825**	**933**	825	933	825	933
481.wrf	56	762	821	766	817	**764**	**819**	56	762	821	766	817	**764**	**819**
482.sphinx3	56	1232	886	**1237**	**882**	1247	875	56	1232	886	**1237**	**882**	1247	875

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Settings:
CPU performance set to Enterprise
Power Technology set to Performance

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Platform Notes (Continued)

```
Energy Performance BIAS setting set to Balanced Performance
Memory RAS configuration set to Maximum Performance
Memory Power Saving Mode set to Disabled
QPI Snoop Mode set to Cluster-on-Die
 Sysinfo program /opt/cpu2006-1.2/config/sysinfo.rev6914
 $Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
 running on linux-wg2b Thu Apr  7 21:38:26 2016

 This section contains SUT (System Under Test) info as seen by
 some common utilities.  To remove or add to this section, see:
   http://www.spec.org/cpu2006/Docs/config.html#sysinfo

 From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-2690 v4@ 2.60GHz
       2 "physical id"s (chips)
       56 "processors"
    cores, siblings (Caution: counting these is hw and system dependent.  The
    following excerpts from /proc/cpuinfo might not be reliable.  Use with
    caution.)
       cpu cores : 14
       siblings  : 28
       physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
       physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
    cache size : 17920 KB

 From /proc/meminfo
    MemTotal:       264402608 kB
    HugePages_Total:        0
    Hugepagesize:        2048 kB

 From /etc/*release* /etc/*version*
    SuSE-release:
       SUSE Linux Enterprise Server 12 (x86_64)
       VERSION = 12
       PATCHLEVEL = 1
       # This file is deprecated and will be removed in a future service pack or
       release.
       # Please check /etc/os-release for details about this release.
    os-release:
       NAME="SLES"
       VERSION="12-SP1"
       VERSION_ID="12.1"
       PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
       ID="sles"
       ANSI_COLOR="0;32"
       CPE_NAME="cpe:/o:suse:sles:12:sp1"

 uname -a:
    Linux linux-wg2b 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
    (8d714a0) x86_64 x86_64 x86_64 GNU/Linux

 run-level 3 Apr 7 10:55
```

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Cisco Systems

Cisco UCS B200 M4 (Intel Xeon E5-2690 v4, 2.60 GHz)

SPECfp_rate2006 =   967

SPECfp_rate_base2006 =   942

CPU2006 license: 9019		**Test date:**	Apr-2016
Test sponsor:	Cisco Systems	**Hardware Availability:**	Apr-2016
Tested by:	Cisco Systems	**Software Availability:**	Dec-2015

Platform Notes (Continued)

```
  SPEC is set to: /opt/cpu2006-1.2
     Filesystem      Type  Size  Used Avail Use% Mounted on
     /dev/sda1       xfs   559G   11G  549G   2% /
  Additional information from dmidecode:

     Warning: Use caution when you interpret this section. The 'dmidecode' program
     reads system data which is "intended to allow hardware to be accurately
     determined", but the intent may not be met, as there are frequent changes to
     hardware, firmware, and the "DMTF SMBIOS" standard.

  BIOS Cisco Systems, Inc. B200M4.3.1.2a.0.022620161405 02/26/2016
  Memory:
   8x 0xCE00 M393A2G40DB1-CRC 16 GB 2 rank 2400 MHz
   8x 0xCE00 M393A2G40EB1-CRC 16 GB 2 rank 2400 MHz
   8x NO DIMM NO DIMM

  (End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/opt/cpu2006-1.2/libs/32:/opt/cpu2006-1.2/libs/64:/opt/cpu2006-1.2/sh"

 Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
 memory using RedHat EL 7.1
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>        /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
```
icc    -m64
```

C++ benchmarks:
```
icpc   -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc    -m64 ifort -m64
```

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CPU2006 license: 9019

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Tested by: Cisco Systems

Test date: Apr-2016

Hardware Availability: Apr-2016

Software Availability: Dec-2015

Base Portability Flags

```
     410.bwaves: -DSPEC_CPU_LP64
     416.gamess: -DSPEC_CPU_LP64
       433.milc: -DSPEC_CPU_LP64
     434.zeusmp: -DSPEC_CPU_LP64
    435.gromacs: -DSPEC_CPU_LP64  -nofor_main
  436.cactusADM: -DSPEC_CPU_LP64  -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
      444.namd: -DSPEC_CPU_LP64
      447.dealII: -DSPEC_CPU_LP64
     450.soplex: -DSPEC_CPU_LP64
     453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64  -nofor_main
  459.GemsFDTD: -DSPEC_CPU_LP64
      465.tonto: -DSPEC_CPU_LP64
       470.lbm: -DSPEC_CPU_LP64
        481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
    482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Peak Compiler Invocation

C benchmarks:

```
icc   -m64
```

C++ benchmarks (except as noted below):

```
icpc  -m64
```

```
450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
```

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Peak Compiler Invocation (Continued)

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Peak Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
   435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
      444.namd: -DSPEC_CPU_LP64
     447.dealII: -DSPEC_CPU_LP64
     450.soplex: -D_FILE_OFFSET_BITS=64
     453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
  459.GemsFDTD: -DSPEC_CPU_LP64
      465.tonto: -DSPEC_CPU_LP64
       470.lbm: -DSPEC_CPU_LP64
       481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
    482.sphinx3: -DSPEC_CPU_LP64
```

Peak Optimization Flags

C benchmarks:

```
    433.milc: basepeak = yes

    470.lbm: basepeak = yes

    482.sphinx3: basepeak = yes
```

C++ benchmarks:

```
    444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
             -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
             -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
             -prof-use(pass 2) -fno-alias -auto-ilp32

    447.dealII: basepeak = yes
```

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Peak Optimization Flags (Continued)

450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revD.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revD.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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