



# SPEC® CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Supermicro

Supermicro X11SSL-nF motherboard  
(X11SSL-nF, Intel Core i3-6100TE)

**SPECint®\_rate2006 = 107**

**SPECint\_rate\_base2006 = 103**

CPU2006 license: 001176

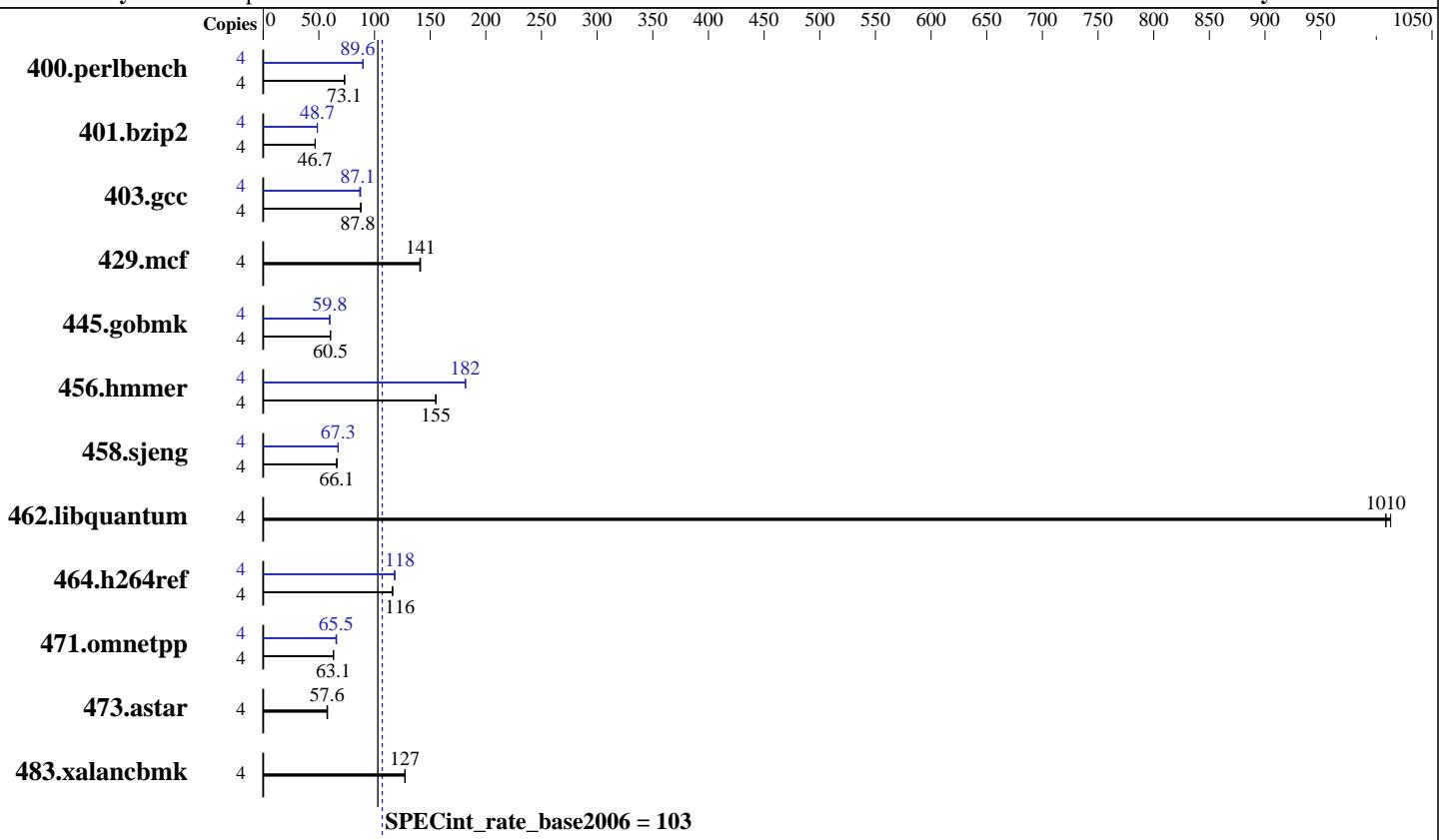
Test sponsor: Supermicro

Tested by: Supermicro

**Test date:** Mar-2016

**Hardware Availability:** Oct-2015

**Software Availability:** Mar-2015



<b>Hardware</b>		<b>Software</b>	
CPU Name:	Intel Core i3-6100TE	Operating System:	Red Hat Enterprise Linux Server release 7.1, Kernel 3.10.0-229.el7.x86_64
CPU Characteristics:		Compiler:	C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux
CPU MHz:	2700	Auto Parallel:	No
FPU:	Integrated	File System:	xfs
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip, 2 threads/core	System State:	Run level 3 (multi-user)
CPU(s) orderable:	1 chip	Base Pointers:	32-bit
Primary Cache:	32 KB I + 32 KB D on chip per core	Peak Pointers:	32/64-bit
Secondary Cache:	256 KB I+D on chip per core	Other Software:	Microquill SmartHeap V10.0
L3 Cache:	4 MB I+D on chip per chip		
Other Cache:	None		
Memory:	64 GB (4 x 16 GB 2Rx8 PC4-2133P-E)		
Disk Subsystem:	1 x 1000 GB SATA III, 7200 RPM		
Other Hardware:	None		



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Supermicro

Supermicro X11SSL-nF motherboard  
(X11SSL-nF, Intel Core i3-6100TE)

**SPECint\_rate2006 = 107**

**SPECint\_rate\_base2006 = 103**

CPU2006 license: 001176

Test date: Mar-2016

Test sponsor: Supermicro

Hardware Availability: Oct-2015

Tested by: Supermicro

Software Availability: Mar-2015

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	536	73.0	<b>535</b>	<b>73.1</b>	534	73.2	4	436	89.6	<b>436</b>	<b>89.6</b>	435	89.7
401.bzip2	4	828	46.6	826	46.7	<b>826</b>	<b>46.7</b>	4	795	48.6	792	48.7	<b>793</b>	<b>48.7</b>
403.gcc	4	368	87.5	367	87.8	<b>367</b>	<b>87.8</b>	4	371	86.8	368	87.6	<b>370</b>	<b>87.1</b>
429.mcf	4	258	141	259	141	<b>258</b>	<b>141</b>	4	258	141	259	141	<b>258</b>	<b>141</b>
445.gobmk	4	694	60.5	694	60.4	<b>694</b>	<b>60.5</b>	4	702	59.7	<b>702</b>	<b>59.8</b>	702	59.8
456.hmmer	4	241	155	<b>240</b>	<b>155</b>	240	155	4	205	182	206	181	<b>206</b>	<b>182</b>
458.sjeng	4	732	66.1	<b>732</b>	<b>66.1</b>	731	66.2	4	<b>720</b>	<b>67.3</b>	720	67.2	718	67.4
462.libquantum	4	82.2	1010	81.8	1010	<b>82.2</b>	<b>1010</b>	4	82.2	1010	81.8	1010	<b>82.2</b>	<b>1010</b>
464.h264ref	4	764	116	761	116	<b>761</b>	<b>116</b>	4	751	118	<b>749</b>	<b>118</b>	748	118
471.omnetpp	4	<b>396</b>	<b>63.1</b>	395	63.2	397	63.0	4	379	65.9	<b>382</b>	<b>65.5</b>	382	65.5
473.astar	4	<b>488</b>	<b>57.6</b>	490	57.4	485	57.9	4	<b>488</b>	<b>57.6</b>	490	57.4	485	57.9
483.xalancbmk	4	<b>217</b>	<b>127</b>	217	127	216	128	4	<b>217</b>	<b>127</b>	217	127	216	128

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

As tested, the system used a Supermicro CSE-731i-300B chassis. The chassis is configured with 2 PWS-305-PQ redundant power supply, 1 SNK-P0046A4 heatsink, as well as 1 FAN-0108L4 rear cooling fan.

```
Sysinfo program /home/cpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$
e3fbb8667b5a285932ceab81e28219e1
running on localhost.localdomain Thu Mar 24 08:40:50 2016
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Core(TM) i3-6100TE CPU @ 2.70GHz
 1 "physical id"s (chips)
 4 "processors"
```

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with  
Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Supermicro

Supermicro X11SSL-nF motherboard  
(X11SSL-nF, Intel Core i3-6100TE)

SPECint\_rate2006 = 107

SPECint\_rate\_base2006 = 103

CPU2006 license: 001176

Test date: Mar-2016

Test sponsor: Supermicro

Hardware Availability: Oct-2015

Tested by: Supermicro

Software Availability: Mar-2015

## Platform Notes (Continued)

```
caution.)  
    cpu cores : 2  
    siblings   : 4  
    physical 0: cores 0 1  
    cache size : 4096 kB  
  
From /proc/meminfo  
MemTotal:       65631132 kB  
HugePages_Total:        0  
Hugepagesize:     2048 kB  
  
From /etc/*release* /etc/*version*  
os-release:  
  NAME="Red Hat Enterprise Linux Server"  
  VERSION="7.1 (Maipo)"  
  ID="rhel"  
  ID_LIKE="fedora"  
  VERSION_ID="7.1"  
  PRETTY_NAME="Red Hat Enterprise Linux Server 7.1 (Maipo)"  
  ANSI_COLOR="0;31"  
  CPE_NAME="cpe:/o:redhat:enterprise_linux:7.1:GA:server"  
redhat-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)  
system-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)  
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.1:ga:server  
  
uname -a:  
Linux localhost.localdomain 3.10.0-229.el7.x86_64 #1 SMP Thu Jan 29 18:37:38  
EST 2015 x86_64 x86_64 x86_64 GNU/Linux  
  
run-level 3 Mar 24 02:42  
  
SPEC is set to: /home/cpu2006  
Filesystem      Type  Size  Used Avail Use% Mounted on  
/dev/mapper/rhel-home xfs  865G  170G  696G  20% /home  
Additional information from dmidecode:  
  
Warning: Use caution when you interpret this section. The 'dmidecode' program  
reads system data which is "intended to allow hardware to be accurately  
determined", but the intent may not be met, as there are frequent changes to  
hardware, firmware, and the "DMTF SMBIOS" standard.  
  
BIOS American Megatrends Inc. 1.0a 12/25/2015  
Memory:  
 4x Samsung M391A2K43BB1-CPB 16 GB 2 rank 2133 MHz  
  
(End of data from sysinfo program)
```



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Supermicro

Supermicro X11SSL-nF motherboard  
(X11SSL-nF, Intel Core i3-6100TE)

**SPECint\_rate2006 = 107**

**SPECint\_rate\_base2006 = 103**

**CPU2006 license:** 001176

**Test sponsor:** Supermicro

**Tested by:** Supermicro

**Test date:** Mar-2016

**Hardware Availability:** Oct-2015

**Software Availability:** Mar-2015

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

C++ benchmarks:

icpc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Supermicro

Supermicro X11SSL-nF motherboard  
(X11SSL-nF, Intel Core i3-6100TE)

**SPECint\_rate2006 = 107**

**SPECint\_rate\_base2006 = 103**

**CPU2006 license:** 001176

**Test sponsor:** Supermicro

**Tested by:** Supermicro

**Test date:** Mar-2016

**Hardware Availability:** Oct-2015

**Software Availability:** Mar-2015

## Peak Compiler Invocation (Continued)

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

456.hmmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -opt-mem-layout-trans=3

456.hmmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll12 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll14 -auto-ilp32

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Supermicro

Supermicro X11SSL-nF motherboard  
(X11SSL-nF, Intel Core i3-6100TE)

**SPECint\_rate2006 = 107**

**SPECint\_rate\_base2006 = 103**

**CPU2006 license:** 001176

**Test sponsor:** Supermicro

**Tested by:** Supermicro

**Test date:** Mar-2016

**Hardware Availability:** Oct-2015

**Software Availability:** Mar-2015

## Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

```
464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
              -unroll12 -ansi-alias
```

C++ benchmarks:

```
471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
              -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
              -L/sh -lsmartheap
```

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.

Report generated on Thu Jun 30 14:35:25 2016 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 29 June 2016.