



# SPEC® CINT2006 Result

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## NEC Corporation

**SPECint®2006 = 63.4**

Express5800/R120g-2E (Intel Xeon E5-2630 v4)

**SPECint\_base2006 = 60.6**

CPU2006 license: 9006

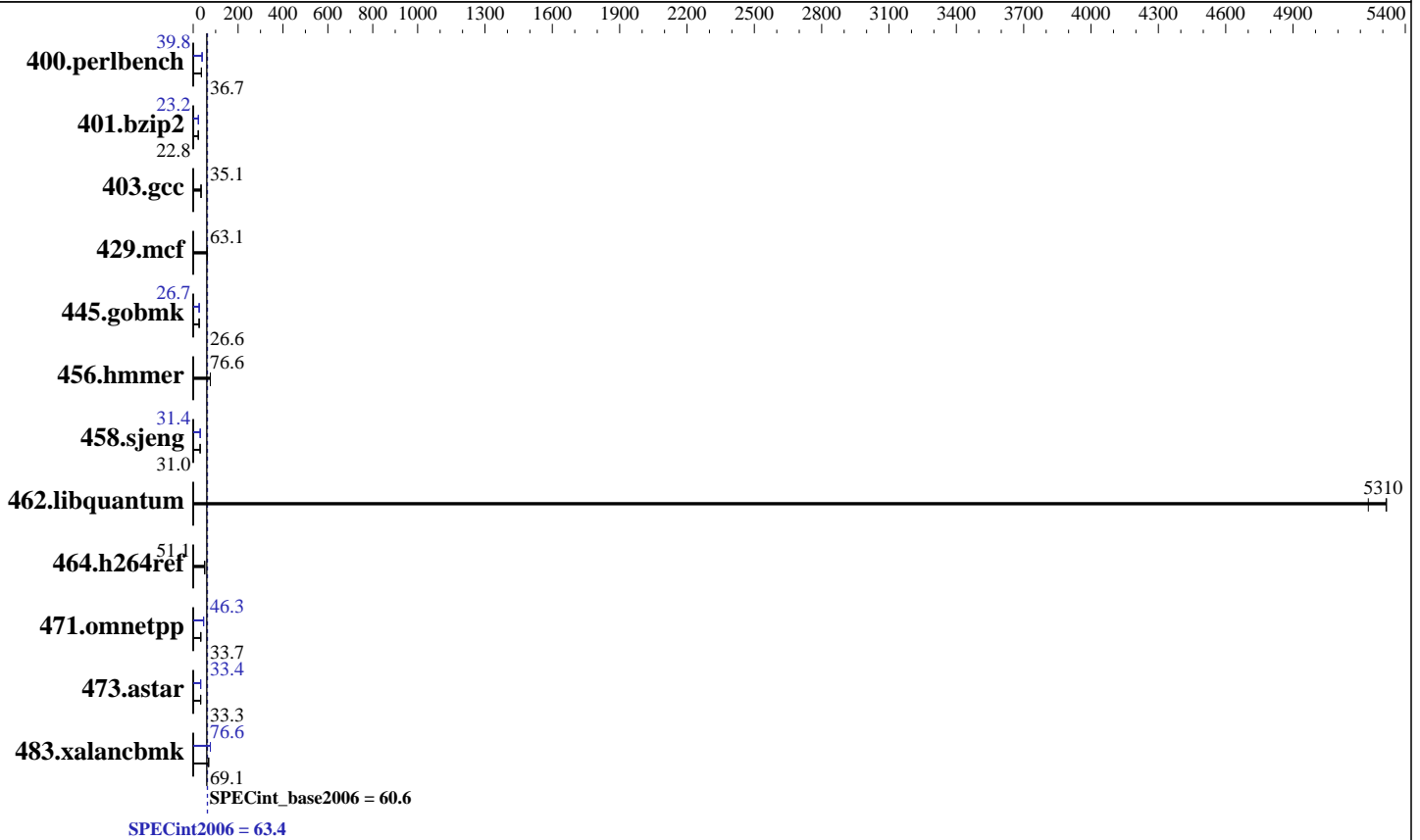
Test date: Mar-2016

Test sponsor: NEC Corporation

Hardware Availability: Apr-2016

Tested by: NEC Corporation

Software Availability: Jan-2016



### Hardware

CPU Name: Intel Xeon E5-2630 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.10 GHz  
 CPU MHz: 2200  
 FPU: Integrated  
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 25 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R, running at 2133 MHz)  
 Disk Subsystem: 1 x 1 TB SATA, 7200 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 7.2 (Maipo)  
 Kernel 3.10.0-327.4.5.el7.x86\_64  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.2



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	266	36.8	<b>266</b>	<b>36.7</b>	266	36.7	245	39.9	<b>246</b>	<b>39.8</b>	246	39.7
401.bzip2	422	22.8	<b>424</b>	<b>22.8</b>	424	22.7	416	23.2	416	23.2	<b>416</b>	<b>23.2</b>
403.gcc	229	35.1	<b>230</b>	<b>35.1</b>	230	35.0	229	35.1	<b>230</b>	<b>35.1</b>	230	35.0
429.mcf	144	63.1	145	63.0	<b>145</b>	<b>63.1</b>	144	63.1	145	63.0	<b>145</b>	<b>63.1</b>
445.gobmk	394	26.6	394	26.6	<b>394</b>	<b>26.6</b>	393	26.7	<b>393</b>	<b>26.7</b>	393	26.7
456.hammer	122	76.4	122	76.6	<b>122</b>	<b>76.6</b>	122	76.4	122	76.6	<b>122</b>	<b>76.6</b>
458.sjeng	390	31.0	390	31.0	<b>390</b>	<b>31.0</b>	386	31.4	<b>385</b>	<b>31.4</b>	385	31.4
462.libquantum	3.96	5240	3.90	5320	<b>3.90</b>	<b>5310</b>	3.96	5240	3.90	5320	<b>3.90</b>	<b>5310</b>
464.h264ref	<b>433</b>	<b>51.1</b>	434	51.0	433	51.1	<b>433</b>	<b>51.1</b>	434	51.0	433	51.1
471.omnetpp	<b>185</b>	<b>33.7</b>	185	33.7	185	33.7	134	46.6	<b>135</b>	<b>46.3</b>	135	46.2
473.astar	211	33.2	<b>211</b>	<b>33.3</b>	211	33.3	211	33.3	<b>210</b>	<b>33.4</b>	210	33.4
483.xalancbmk	<b>99.9</b>	<b>69.1</b>	103	67.2	99.8	69.1	<b>90.1</b>	<b>76.6</b>	90.1	76.6	90.2	76.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Settings:  
 Power Management Policy: Custom  
 Energy Performance: Performance  
 Patrol Scrub: Disabled  
 Snoop Mode: Home Snoop with Directory  
 Hyper-Threading: Disabled

## General Notes

Environment variables set by runspec before the start of the run:  
 KMP\_AFFINITY = "granularity=fine,compact"  
 LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"  
 OMP\_NUM\_THREADS = "20"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1  
 Transparent Huge Pages enabled with:  
 echo always > /sys/kernel/mm/transparent\_hugepage/enabled



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## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
 401.bzip2: -DSPEC\_CPU\_LP64  
 403.gcc: -DSPEC\_CPU\_LP64  
 429.mcf: -DSPEC\_CPU\_LP64  
 445.gobmk: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
 464.h264ref: -DSPEC\_CPU\_LP64  
 471.omnetpp: -DSPEC\_CPU\_LP64  
 473.astar: -DSPEC\_CPU\_LP64  
 483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-Wl,-z,muldefs -L/sh -lsmartheap64

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

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## Peak Compiler Invocation (Continued)

400.perlbench: icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

445.gobmk: icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

C++ benchmarks (except as noted below):

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX\_IA32

401.bzip2: -DSPEC\_CPU\_LP64

403.gcc: -DSPEC\_CPU\_LP64

429.mcf: -DSPEC\_CPU\_LP64

445.gobmk: -D\_FILE\_OFFSET\_BITS=64

456.hmmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

464.h264ref: -DSPEC\_CPU\_LP64

471.omnetpp: -D\_FILE\_OFFSET\_BITS=64

473.astar: -DSPEC\_CPU\_LP64

483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch  
-ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div  
-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32  
-opt-prefetch -ansi-alias

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias

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## Peak Optimization Flags (Continued)

456.hmmcr: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-120g-RevB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-120g-RevB.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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