



SPEC® CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Lenovo Group Limited

SPECint®_rate2006 = 1790

Lenovo ThinkSever RD550
(2.20 GHz, Intel Xeon E5-2699 v4)

SPECint_rate_base2006 = 1730

CPU2006 license: 9017

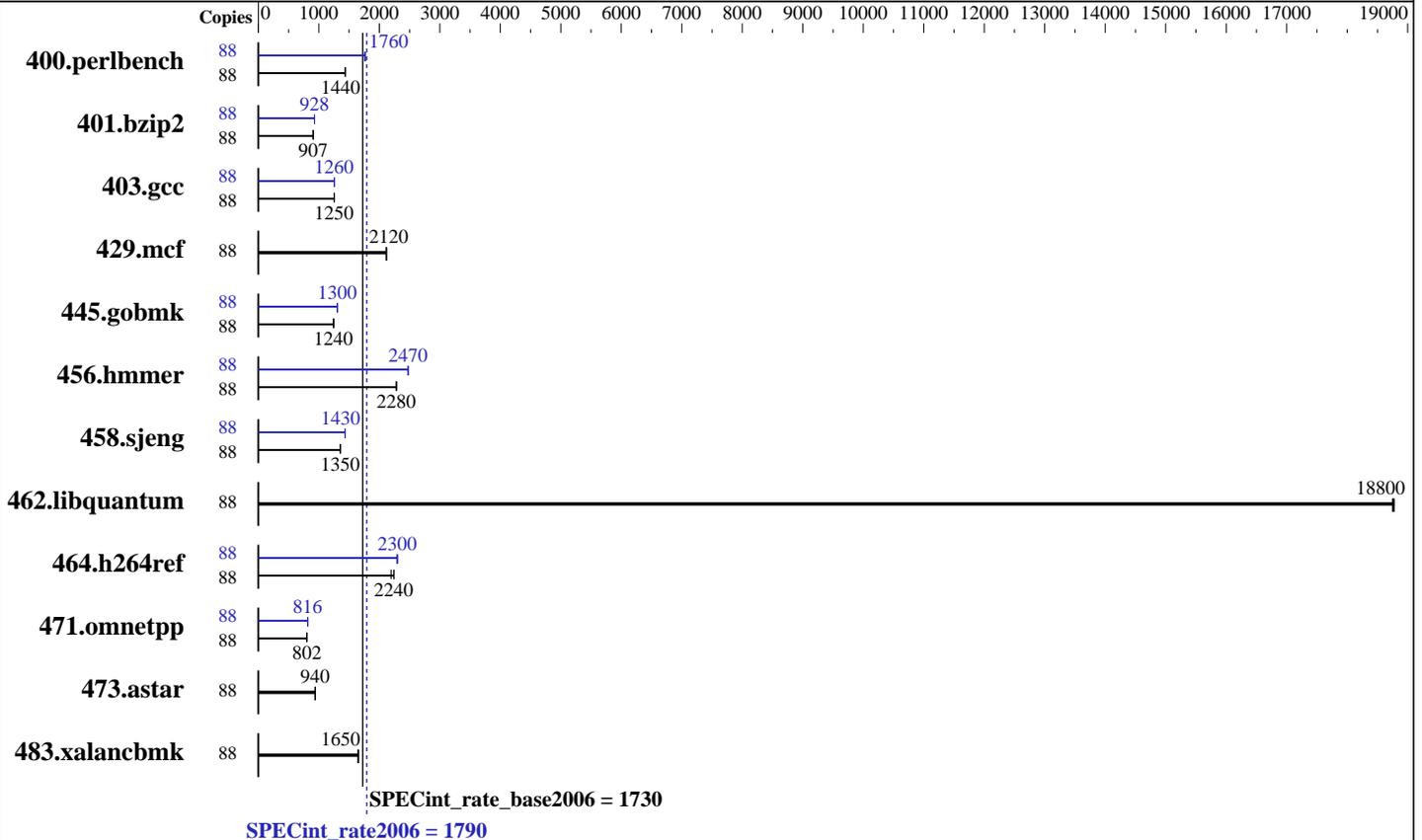
Test date: Feb-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Mar-2016

Tested by: Lenovo Group Limited

Software Availability: Dec-2015



Hardware

CPU Name: Intel Xeon E5-2699 v4
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz
 CPU MHz: 2200
 FPU: Integrated
 CPU(s) enabled: 44 cores, 2 chips, 22 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 55 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)
 Disk Subsystem: 1 x 800 GB SATA SSD
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 12 SP1 (x86_64)
 Kernel 3.12.49-11-default
 Compiler: C/C++; Version 16.0.0.101 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.2



SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Lenovo Group Limited

SPECint_rate2006 = 1790

Lenovo ThinkSever RD550
(2.20 GHz, Intel Xeon E5-2699 v4)

SPECint_rate_base2006 = 1730

CPU2006 license: 9017

Test date: Feb-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Mar-2016

Tested by: Lenovo Group Limited

Software Availability: Dec-2015

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	88	599	1440	<u>599</u>	<u>1440</u>	599	1440	88	487	1770	488	1760	<u>488</u>	<u>1760</u>
401.bzip2	88	938	906	<u>936</u>	<u>907</u>	935	908	88	913	930	<u>915</u>	<u>928</u>	915	928
403.gcc	88	<u>565</u>	<u>1250</u>	564	1260	566	1250	88	565	1250	564	1260	<u>564</u>	<u>1260</u>
429.mcf	88	380	2110	378	2120	<u>379</u>	<u>2120</u>	88	380	2110	378	2120	<u>379</u>	<u>2120</u>
445.gobmk	88	742	1240	<u>742</u>	<u>1240</u>	743	1240	88	<u>708</u>	<u>1300</u>	708	1300	706	1310
456.hammer	88	360	2280	360	2280	<u>360</u>	<u>2280</u>	88	331	2480	<u>332</u>	<u>2470</u>	332	2470
458.sjeng	88	<u>786</u>	<u>1350</u>	786	1350	786	1350	88	<u>743</u>	<u>1430</u>	743	1430	743	1430
462.libquantum	88	97.1	18800	97.2	18700	<u>97.2</u>	<u>18800</u>	88	97.1	18800	97.2	18700	<u>97.2</u>	<u>18800</u>
464.h264ref	88	868	2240	887	2200	<u>870</u>	<u>2240</u>	88	<u>847</u>	<u>2300</u>	851	2290	846	2300
471.omnetpp	88	<u>686</u>	<u>802</u>	686	802	687	801	88	673	817	<u>674</u>	<u>816</u>	674	816
473.astar	88	657	940	<u>657</u>	<u>940</u>	657	941	88	657	940	<u>657</u>	<u>940</u>	657	941
483.xalancbmk	88	<u>368</u>	<u>1650</u>	367	1660	368	1650	88	<u>368</u>	<u>1650</u>	367	1660	368	1650

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches

Platform Notes

BIOS Configuration:
Cluster On Die set to Enabled
Early Snoop set to Disabled
Performance Profile set to Custom
C1E Support set to Disabled
Core C3 set to Disabled
Core C6 set to Disabled
Thermal Profile set to High Fan Speed
Memory Power Savings set to Disabled
Sysinfo program /home/cpu2006-1.2-ic16.0/config/sysinfo.rev6914
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
running on ip10-245-48-75 Sat Jan 1 00:27:39 2000

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Lenovo Group Limited

SPECint_rate2006 = 1790

Lenovo ThinkSever RD550
(2.20 GHz, Intel Xeon E5-2699 v4)

SPECint_rate_base2006 = 1730

CPU2006 license: 9017

Test date: Feb-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Mar-2016

Tested by: Lenovo Group Limited

Software Availability: Dec-2015

Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name      : Intel(R) Xeon(R) CPU E5-2699 v4 @ 2.20GHz
 2 "physical id"s (chips)
 88 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores      : 22
  siblings       : 44
  physical 0:    cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
                    28
  physical 1:    cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
                    28
cache size      : 28160 KB

```

```

From /proc/meminfo
MemTotal:        264548816 kB
HugePages_Total: 0
Hugepagesize:    2048 kB

```

```

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"

```

```

uname -a:
Linux ip10-245-48-75 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Jan 1 00:12

```

SPEC is set to: /home/cpu2006-1.2-ic16.0
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda4        xfs   690G  7.7G  683G   2% /home
Additional information from dmidecode:

```

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Lenovo Group Limited

SPECint_rate2006 = 1790

Lenovo ThinkSever RD550
(2.20 GHz, Intel Xeon E5-2699 v4)

SPECint_rate_base2006 = 1730

CPU2006 license: 9017

Test date: Feb-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Mar-2016

Tested by: Lenovo Group Limited

Software Availability: Dec-2015

Platform Notes (Continued)

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS LENOVO PB2TS335 01/09/2016

Memory:

16x Hynix Semiconductor HMA42GR7AFR4N-UH 16 GB 2 rank 2400 MHz
8x NO DIMM NO DIMM

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/home/cpu2006-1.2-ic16.0/libs/32:/home/cpu2006-1.2-ic16.0/libs/64:/home/cpu2006-1.2-ic16.0/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:

icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Base Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32

401.bzip2: -D_FILE_OFFSET_BITS=64

403.gcc: -D_FILE_OFFSET_BITS=64

429.mcf: -D_FILE_OFFSET_BITS=64

445.gobmk: -D_FILE_OFFSET_BITS=64

456.hmmer: -D_FILE_OFFSET_BITS=64

458.sjeng: -D_FILE_OFFSET_BITS=64

462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

464.h264ref: -D_FILE_OFFSET_BITS=64

471.omnetpp: -D_FILE_OFFSET_BITS=64

Continued on next page

Standard Performance Evaluation Corporation

info@spec.org

http://www.spec.org/

Page 4



SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Lenovo Group Limited

SPECint_rate2006 = 1790

Lenovo ThinkSever RD550
(2.20 GHz, Intel Xeon E5-2699 v4)

SPECint_rate_base2006 = 1730

CPU2006 license: 9017

Test date: Feb-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Mar-2016

Tested by: Lenovo Group Limited

Software Availability: Dec-2015

Base Portability Flags (Continued)

473.astar: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Peak Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Lenovo Group Limited

SPECint_rate2006 = 1790

Lenovo ThinkSever RD550
(2.20 GHz, Intel Xeon E5-2699 v4)

SPECint_rate_base2006 = 1730

CPU2006 license: 9017

Test date: Feb-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Mar-2016

Tested by: Lenovo Group Limited

Software Availability: Dec-2015

Peak Portability Flags (Continued)

445.gobmk: -D_FILE_OFFSET_BITS=64
 456.hmmr: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
 458.sjeng: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
 462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
 464.h264ref: -D_FILE_OFFSET_BITS=64
 471.omnetpp: -D_FILE_OFFSET_BITS=64
 473.astar: -D_FILE_OFFSET_BITS=64
 483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
 -par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
 -par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch
 -auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
 -prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias
 -opt-mem-layout-trans=3

456.hmmr: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
 -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
 -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
 -par-num-threads=1(pass 1) -prof-use(pass 2) -ansi-alias

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Lenovo Group Limited

SPECint_rate2006 = 1790

Lenovo ThinkSever RD550
(2.20 GHz, Intel Xeon E5-2699 v4)

SPECint_rate_base2006 = 1730

CPU2006 license: 9017

Test date: Feb-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Mar-2016

Tested by: Lenovo Group Limited

Software Availability: Dec-2015

Peak Optimization Flags (Continued)

471.omnetpp (continued):

```
-opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/sh -lsmartheap
```

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-BDW-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-BDW-revC.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.

Report generated on Wed Apr 6 10:44:43 2016 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 5 April 2016.