



SPEC® CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Lenovo Group Limited

SPECfp®_rate2006 = 677

Lenovo System x3500 M5
(Intel Xeon E5-2648L v3, 1.80 GHz)

SPECfp_rate_base2006 = 662

CPU2006 license: 9017

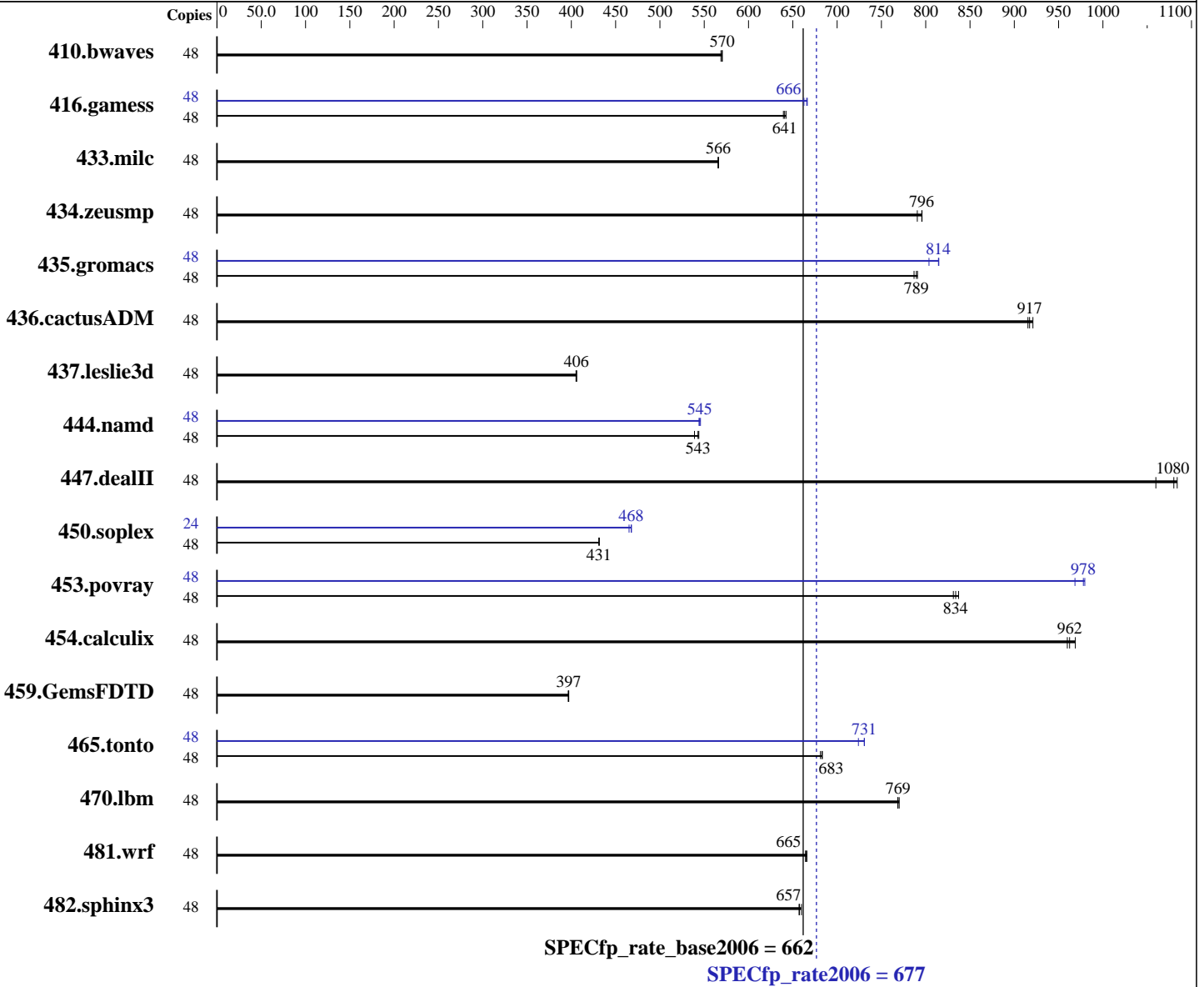
Test sponsor: Lenovo Group Limited

Tested by: Lenovo Group Limited

Test date: Oct-2015

Hardware Availability: Jan-2015

Software Availability: Sep-2014



Hardware

CPU Name: Intel Xeon E5-2648L v3
 CPU Characteristics: Intel Turbo Boost Technology up to 2.50 GHz
 CPU MHz: 1800
 FPU: Integrated
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)
 3.10.0-123.el7.x86_64
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
 Auto Parallel: No
 File System: xfs

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Lenovo Group Limited

SPECfp_rate2006 = **677**

Lenovo System x3500 M5
(Intel Xeon E5-2648L v3, 1.80 GHz)

SPECfp_rate_base2006 = **662**

CPU2006 license: 9017

Test date: Oct-2015

Test sponsor: Lenovo Group Limited

Hardware Availability: Jan-2015

Tested by: Lenovo Group Limited

Software Availability: Sep-2014

L3 Cache: 30 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)
Disk Subsystem: 1 x 960 GB SATA SSD
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	48	1144	570	1147	569	1144	570	48	1144	570	1147	569	1144	570
416.gamess	48	1470	639	1463	642	1467	641	48	1412	666	1410	666	1418	663
433.milc	48	779	566	778	566	779	566	48	779	566	778	566	779	566
434.zeusmp	48	553	791	549	796	549	796	48	553	791	549	796	549	796
435.gromacs	48	433	791	436	787	434	789	48	426	804	421	815	421	814
436.cactusADM	48	627	915	623	921	625	917	48	627	915	623	921	625	917
437.leslie3d	48	1111	406	1112	406	1113	405	48	1111	406	1112	406	1113	405
444.namd	48	708	544	709	543	714	539	48	707	545	705	546	707	544
447.dealII	48	507	1080	508	1080	518	1060	48	507	1080	508	1080	518	1060
450.soplex	48	928	431	929	431	928	432	24	428	468	430	465	428	468
453.povray	48	305	837	306	834	307	831	48	264	968	261	980	261	978
454.calculix	48	409	969	413	960	411	962	48	409	969	413	960	411	962
459.GemsFDTD	48	1283	397	1283	397	1285	396	48	1283	397	1283	397	1285	396
465.tonto	48	693	681	692	683	691	683	48	646	731	652	724	646	731
470.lbm	48	858	769	856	770	858	769	48	858	769	856	770	858	769
481.wrf	48	807	665	805	666	807	664	48	807	665	805	666	807	664
482.sphinx3	48	1418	660	1423	657	1424	657	48	1418	660	1423	657	1424	657

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS setting:
Operating Mode set to "Efficiency-Favor Performance"
Hyper-threading set to "Enable"

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Lenovo Group Limited

SPECfp_rate2006 = 677

Lenovo System x3500 M5
(Intel Xeon E5-2648L v3, 1.80 GHz)

SPECfp_rate_base2006 = 662

CPU2006 license: 9017

Test date: Oct-2015

Test sponsor: Lenovo Group Limited

Hardware Availability: Jan-2015

Tested by: Lenovo Group Limited

Software Availability: Sep-2014

Platform Notes (Continued)

Snoop mode set to "Cluster on Die"
Sysinfo program /home/SPEC_ic16/config/sysinfo.rev6914
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
running on x3500M5 Wed Oct 21 14:23:14 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name      : Intel(R) Xeon(R) CPU E5-2648L v3 @ 1.80GHz
 2 "physical id"s (chips)
 48 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores      : 6
siblings       : 12
physical 0:    cores 0 1 2 3 4 5 8 9 10 11 12 13
physical 1:    cores 0 1 2 3 4 5 8 9 10 11 12 13
cache size     : 15360 KB
```

```
From /proc/meminfo
MemTotal:      263453468 kB
HugePages_Total: 0
Hugepagesize:  2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server
```

```
uname -a:
Linux x3500M5 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014 x86_64
x86_64 x86_64 GNU/Linux
```

```
SPEC is set to: /home/SPEC_ic16
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel-root xfs  927G 188G  739G  21% /
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
Continued on next page



SPEC CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Lenovo Group Limited

SPECfp_rate2006 = 677

Lenovo System x3500 M5
(Intel Xeon E5-2648L v3, 1.80 GHz)

SPECfp_rate_base2006 = 662

CPU2006 license: 9017

Test date: Oct-2015

Test sponsor: Lenovo Group Limited

Hardware Availability: Jan-2015

Tested by: Lenovo Group Limited

Software Availability: Sep-2014

Platform Notes (Continued)

reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS IBM -[TAE105J-1.10]- 04/20/2015

Memory:

6x Hynix HMA42GR7MFR4N-TF 16 GB 2 rank 2133 MHz

10x Hynix HMA42GR7MFR4N-TFT1 16 GB 2 rank 2133 MHz

8x NO DIMM Unknown

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/home/SPEC_ic16/libs/32:/home/SPEC_ic16/libs/64:/home/SPEC_ic16/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64

416.gamess: -DSPEC_CPU_LP64

433.milc: -DSPEC_CPU_LP64

434.zeusmp: -DSPEC_CPU_LP64

Continued on next page

Standard Performance Evaluation Corporation

info@spec.org

http://www.spec.org/

Page 4



SPEC CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Lenovo Group Limited

SPECfp_rate2006 = 677

Lenovo System x3500 M5
(Intel Xeon E5-2648L v3, 1.80 GHz)

SPECfp_rate_base2006 = 662

CPU2006 license: 9017

Test date: Oct-2015

Test sponsor: Lenovo Group Limited

Hardware Availability: Jan-2015

Tested by: Lenovo Group Limited

Software Availability: Sep-2014

Base Portability Flags (Continued)

```

435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks (except as noted below):

```

icpc -m64

```

```

450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

```

Fortran benchmarks:

```

ifort -m64

```

Benchmarks using both Fortran and C:

```

icc -m64 ifort -m64

```



SPEC CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Lenovo Group Limited

SPECfp_rate2006 = 677

Lenovo System x3500 M5
(Intel Xeon E5-2648L v3, 1.80 GHz)

SPECfp_rate_base2006 = 662

CPU2006 license: 9017

Test date: Oct-2015

Test sponsor: Lenovo Group Limited

Hardware Availability: Jan-2015

Tested by: Lenovo Group Limited

Software Availability: Sep-2014

Peak Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -D_FILE_OFFSET_BITS=64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

```

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
         -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
         -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
         -prof-use(pass 2) -fno-alias -auto-ilp32

```

447.dealII: basepeak = yes

```

450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
           -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
           -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
           -prof-use(pass 2) -opt-malloc-options=3

```

```

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
           -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
           -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
           -prof-use(pass 2) -unroll4 -ansi-alias

```

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Lenovo Group Limited

SPECfp_rate2006 = 677

Lenovo System x3500 M5
(Intel Xeon E5-2648L v3, 1.80 GHz)

SPECfp_rate_base2006 = 662

CPU2006 license: 9017

Test date: Oct-2015

Test sponsor: Lenovo Group Limited

Hardware Availability: Jan-2015

Tested by: Lenovo Group Limited

Software Availability: Sep-2014

Peak Optimization Flags (Continued)

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-HSW-D.20150923.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-HSW-D.20150923.xml>



SPEC CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Lenovo Group Limited

Lenovo System x3500 M5
(Intel Xeon E5-2648L v3, 1.80 GHz)

SPECfp_rate2006 = 677

SPECfp_rate_base2006 = 662

CPU2006 license: 9017

Test sponsor: Lenovo Group Limited

Tested by: Lenovo Group Limited

Test date: Oct-2015

Hardware Availability: Jan-2015

Software Availability: Sep-2014

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Tue Dec 1 17:41:04 2015 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 1 December 2015.