



SPEC[®] CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Lenovo Group Limited

SPECint[®]_rate2006 = 225

Lenovo System x3100 M5
(Intel Xeon E3-1280 v3, 4.00 GHz)

SPECint_rate_base2006 = 215

CPU2006 license: 9017

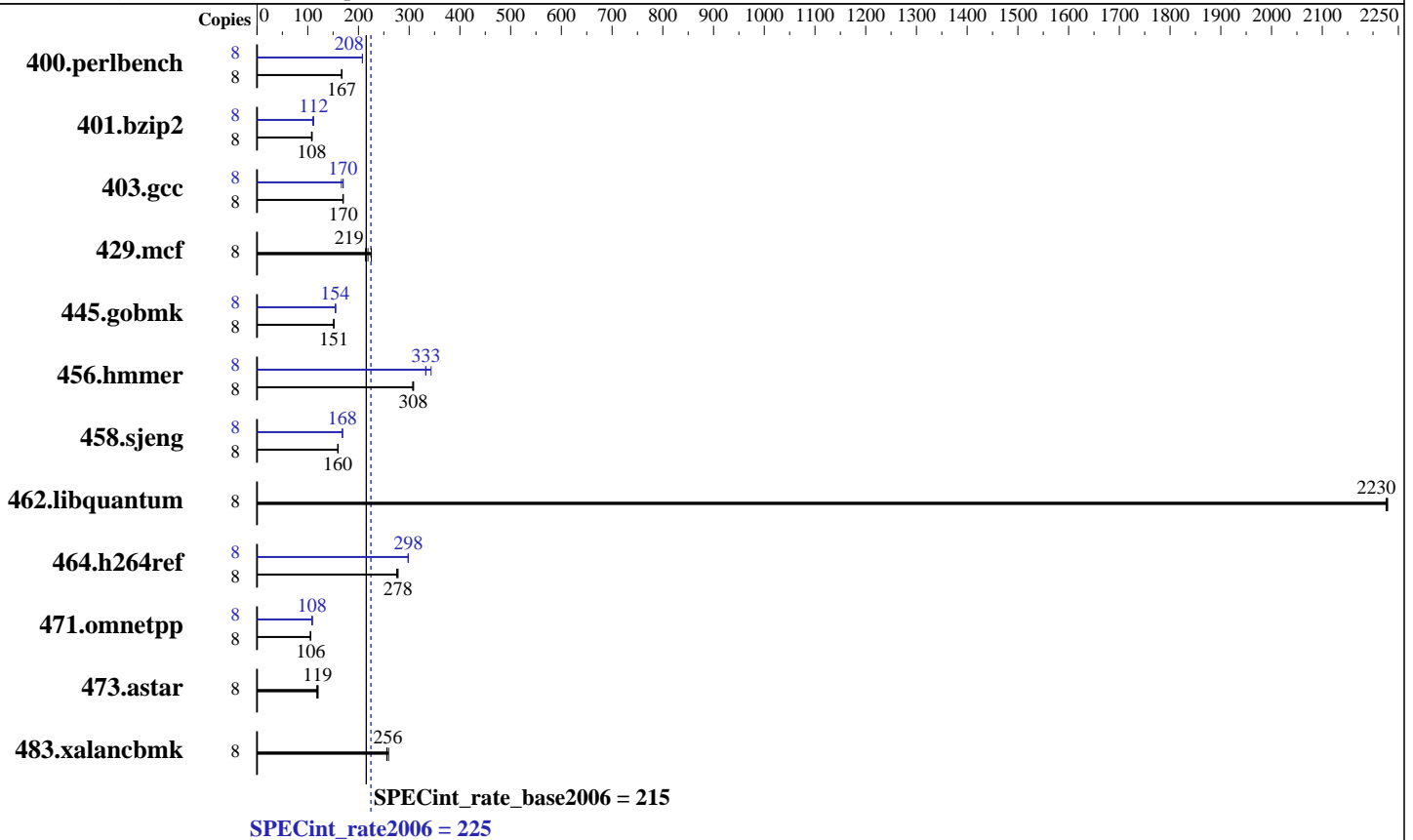
Test date: Sep-2015

Test sponsor: Lenovo Group Limited

Hardware Availability: May-2014

Tested by: Lenovo Group Limited

Software Availability: Jun-2014



Hardware

CPU Name: Intel Xeon E3-1280 v3
 CPU Characteristics: Intel Turbo Boost Technology up to 4.00 GHz
 CPU MHz: 3600
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 16 GB (4 x 4 GB 2Rx8 PC3L-12800E-11, ECC)
 Disk Subsystem: 1 x 500 GB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 7.1 (Maipo)
 3.10.0-229.el7.x86_64
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.2



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Lenovo Group Limited

SPECint_rate2006 = 225

Lenovo System x3100 M5
(Intel Xeon E3-1280 v3, 4.00 GHz)

SPECint_rate_base2006 = 215

CPU2006 license: 9017

Test date: Sep-2015

Test sponsor: Lenovo Group Limited

Hardware Availability: May-2014

Tested by: Lenovo Group Limited

Software Availability: Jun-2014

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	468	167	468	167	468	167	8	376	208	376	208	376	208
401.bzip2	8	718	107	715	108	709	109	8	692	112	704	110	691	112
403.gcc	8	381	169	379	170	379	170	8	379	170	379	170	387	166
429.mcf	8	341	214	334	219	324	226	8	341	214	334	219	324	226
445.gobmk	8	555	151	555	151	553	152	8	540	155	545	154	544	154
456.hammer	8	242	309	243	308	243	307	8	224	333	217	343	224	333
458.sjeng	8	605	160	608	159	607	160	8	575	168	574	169	575	168
462.libquantum	8	74.4	2230	74.5	2230	74.4	2230	8	74.4	2230	74.5	2230	74.4	2230
464.h264ref	8	638	278	643	275	637	278	8	594	298	594	298	594	298
471.omnetpp	8	473	106	473	106	476	105	8	463	108	457	109	461	108
473.astar	8	470	119	466	120	476	118	8	470	119	466	120	476	118
483.xalancbmk	8	216	256	213	259	216	256	8	216	256	213	259	216	256

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS setting:
Hyper-Threading set to Enable
Operating Mode set to "Efficiency-Favor Performance"
Sysinfo program /root/cpu2006_icl6/config/sysinfo.rev6914
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
running on x3100m5.labs.lenovo.com Mon Sep 14 10:52:25 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E3-1280 v3 @ 3.60GHz
1 "physical id"s (chips)
8 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with
Continued on next page



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Lenovo Group Limited

SPECint_rate2006 = 225

Lenovo System x3100 M5
(Intel Xeon E3-1280 v3, 4.00 GHz)

SPECint_rate_base2006 = 215

CPU2006 license: 9017

Test date: Sep-2015

Test sponsor: Lenovo Group Limited

Hardware Availability: May-2014

Tested by: Lenovo Group Limited

Software Availability: Jun-2014

Platform Notes (Continued)

```
caution.)
  cpu cores : 4
  siblings  : 8
  physical 0: cores 0 1 2 3
  cache size : 8192 KB
```

```
From /proc/meminfo
MemTotal:      16100780 kB
HugePages_Total:    0
Hugepagesize:    2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
  NAME="Red Hat Enterprise Linux Server"
  VERSION="7.1 (Maipo)"
  ID="rhel"
  ID_LIKE="fedora"
  VERSION_ID="7.1"
  PRETTY_NAME="Red Hat Enterprise Linux Server 7.1 (Maipo)"
  ANSI_COLOR="0;31"
  CPE_NAME="cpe:/o:redhat:enterprise_linux:7.1:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.1:ga:server
```

```
uname -a:
Linux x3100m5.labs.lenovo.com 3.10.0-229.el7.x86_64 #1 SMP Thu Jan 29
18:37:38 EST 2015 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Sep 14 10:44
```

```
SPEC is set to: /root/cpu2006_ic16
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/mapper/rhel-root xfs      50G   49G  1.7G  97% /
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```
BIOS IBM  -[J9E113LUS-1.05]- 07/06/2014
Memory:
  4x Hynix/Hyundai HMT351U7EFR8A-PB 4 GB 2 rank 1600 MHz
```

(End of data from sysinfo program)



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Lenovo Group Limited

SPECint_rate2006 = 225

Lenovo System x3100 M5
(Intel Xeon E3-1280 v3, 4.00 GHz)

SPECint_rate_base2006 = 215

CPU2006 license: 9017

Test date: Sep-2015

Test sponsor: Lenovo Group Limited

Hardware Availability: May-2014

Tested by: Lenovo Group Limited

Software Availability: Jun-2014

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/root/cpu2006_ic16/libs/32:/root/cpu2006_ic16/libs/64:/root/cpu2006_ic16/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:

icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Base Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -D_FILE_OFFSET_BITS=64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -D_FILE_OFFSET_BITS=64
458.sjeng: -D_FILE_OFFSET_BITS=64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/sh -lsmartheap

Base Other Flags

C benchmarks:

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Lenovo Group Limited

SPECint_rate2006 = 225

Lenovo System x3100 M5
(Intel Xeon E3-1280 v3, 4.00 GHz)

SPECint_rate_base2006 = 215

CPU2006 license: 9017

Test date: Sep-2015

Test sponsor: Lenovo Group Limited

Hardware Availability: May-2014

Tested by: Lenovo Group Limited

Software Availability: Jun-2014

Base Other Flags (Continued)

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Peak Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
458.sjeng: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch
-auto-ilp32 -ansi-alias

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Lenovo Group Limited

SPECint_rate2006 = 225

Lenovo System x3100 M5
(Intel Xeon E3-1280 v3, 4.00 GHz)

SPECint_rate_base2006 = 215

CPU2006 license: 9017

Test date: Sep-2015

Test sponsor: Lenovo Group Limited

Hardware Availability: May-2014

Tested by: Lenovo Group Limited

Software Availability: Jun-2014

Peak Optimization Flags (Continued)

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias

456.hmmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
-auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -ansi-alias
-opt-ra-region-strategy=block -Wl,-z,muldefs
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-HSW-B.20150923.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-HSW-B.20150923.xml>

Standard Performance Evaluation Corporation

info@spec.org

<http://www.spec.org/>

Page 6



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Lenovo Group Limited

SPECint_rate2006 = 225

Lenovo System x3100 M5
(Intel Xeon E3-1280 v3, 4.00 GHz)

SPECint_rate_base2006 = 215

CPU2006 license: 9017

Test date: Sep-2015

Test sponsor: Lenovo Group Limited

Hardware Availability: May-2014

Tested by: Lenovo Group Limited

Software Availability: Jun-2014

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Tue Oct 6 13:12:53 2015 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 6 October 2015.