SPEC® CFP2006 Result
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Hewlett-Packard Company

ProLiant BL660c Gen9
(2.10 GHz, Intel Xeon E5-4650 v3)

$SPECfp^{®}_rate2006 = $ 1450
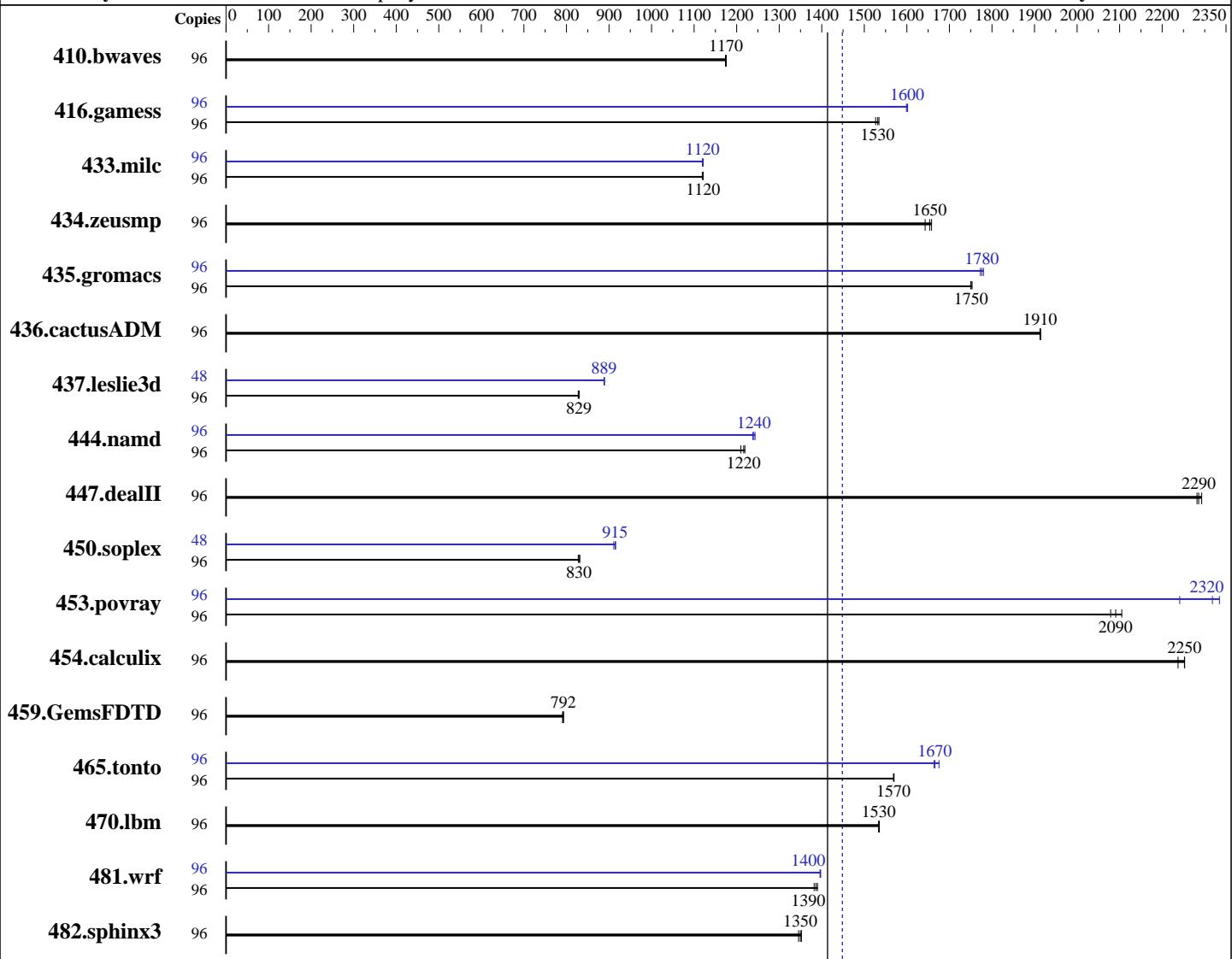
$SPECfp_rate_base2006 = $ 1410

CPU2006 license: 3

Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: May-2015

Hardware Availability: Jun-2015

Software Availability: Oct-2014

SPECfp_rate_base2006 = 1410

SPECfp_rate2006 = 1450

	410.bwaves	96	1170
416.gamess	96 / 96	1600 / 1530
433.milc	96 / 96	1120 / 1120
434.zeusmp	96	1650
435.gromacs	96 / 96	1780 / 1750
436.cactusADM	96	1910
437.leslie3d	48 / 96	889 / 829
444.namd	96 / 96	1240 / 1220
447.dealII	96	2290
450.soplex	48 / 96	915 / 830
453.povray	96 / 96	2320 / 2090
454.calculix	96	2250
459.GemsFDTD	96	792
465.tonto	96 / 96	1670 / 1570
470.lbm	96	1530
481.wrf	96 / 96	1400 / 1390
482.sphinx3	96	1350

Hardware

CPU Name: Intel Xeon E5-4650 v3

CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz

CPU MHz: 2100

FPU: Integrated

CPU(s) enabled: 48 cores, 4 chips, 12 cores/chip, 2 threads/core

CPU(s) orderable: 2,4 chips

Primary Cache: 32 KB I + 32 KB D on chip per core

Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 12 (x86_64) Kernel 3.12.28-4-default

Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux; Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux

Auto Parallel: No

File System: xfs

System State: Run level 3 (multi-user)

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L3 Cache:	30 MB I+D on chip per chip
Other Cache:	None
Memory:	512 GB (32 x 16 GB 2Rx4 PC4-2133P-R)
Disk Subsystem:	1 x 400 GB SAS SSD, RAID 0
Other Hardware:	None

Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	None

Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	96	1110	1180	**1110**	**1170**	1112	1170	96	1110	1180	**1110**	**1170**	1112	1170
416.gamess	96	**1227**	**1530**	1231	1530	1225	1530	96	1176	1600	**1174**	**1600**	1173	1600
433.milc	96	**786**	**1120**	786	1120	788	1120	96	786	1120	787	1120	**787**	**1120**
434.zeusmp	96	**528**	**1650**	532	1640	527	1660	96	**528**	**1650**	532	1640	527	1660
435.gromacs	96	391	1750	392	1750	**391**	**1750**	96	387	1770	**386**	**1780**	385	1780
436.cactusADM	96	**600**	**1910**	599	1910	600	1910	96	**600**	**1910**	599	1910	600	1910
437.leslie3d	96	**1088**	**829**	1090	828	1087	830	48	508	889	507	890	**508**	**889**
444.namd	96	631	1220	**633**	**1220**	636	1210	96	622	1240	619	1240	**621**	**1240**
447.dealII	96	481	2280	479	2290	**480**	**2290**	96	481	2280	479	2290	**480**	**2290**
450.soplex	96	963	831	**965**	**830**	967	828	48	**438**	**915**	439	911	437	916
453.povray	96	246	2080	**244**	**2090**	243	2110	96	228	2240	219	2330	**220**	**2320**
454.calculix	96	354	2240	**352**	**2250**	352	2250	96	354	2240	**352**	**2250**	352	2250
459.GemsFDTD	96	1288	791	1284	793	**1286**	**792**	96	1288	791	1284	793	**1286**	**792**
465.tonto	96	**602**	**1570**	602	1570	602	1570	96	568	1660	**567**	**1670**	564	1680
470.lbm	96	**860**	**1530**	859	1540	860	1530	96	**860**	**1530**	859	1540	860	1530
481.wrf	96	**773**	**1390**	776	1380	771	1390	96	**768**	**1400**	767	1400	768	1400
482.sphinx3	96	**1386**	**1350**	1384	1350	1391	1350	96	**1386**	**1350**	1384	1350	1391	1350

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
 echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>

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CPU2006 license: 3
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Tested by: Hewlett-Packard Company

Test date: May-2015
Hardware Availability: Jun-2015
Software Availability: Oct-2014

Platform Notes

```
BIOS Configuration:
 HP Power Profile set to Custom
 HP Power Regulator to HP Static High Performance Mode
 Minimum Processor Idle Power Core State set to C6 State
 Energy/Performance Bias set to Maximum Performance
 Collaborative Power Control set to Disabled
 Thermal Configuration set to Maximum Cooling
 Processor Power and Utilization Monitoring set to Disabled
 Memory Refresh Rate set to 1x Refresh
Sysinfo program /root/cpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on linux-wzg5 Mon May 18 23:50:32 2015

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-4650 v3 @ 2.10GHz
      4 "physical id"s (chips)
      96 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 12
      siblings  : 24
      physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
      physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
      physical 2: cores 0 1 2 3 4 5 8 9 10 11 12 13
      physical 3: cores 0 1 2 3 4 5 8 9 10 11 12 13
   cache size : 30720 KB

From /proc/meminfo
   MemTotal:      529172948 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 12 (x86_64)
      VERSION = 12
      PATCHLEVEL = 0
      # This file is deprecated and will be removed in a future service pack or
      release.
      # Please check /etc/os-release for details about this release.
   os-release:
      NAME="SLES"
      VERSION="12"
      VERSION_ID="12"
      PRETTY_NAME="SUSE Linux Enterprise Server 12"
      ID="sles"
      ANSI_COLOR="0;32"
```

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Platform Notes (Continued)

```
        CPE_NAME="cpe:/o:suse:sles:12"

  uname -a:
     Linux linux-wzg5 3.12.28-4-default #1 SMP Thu Sep 25 17:02:34 UTC 2014
     (9879bd4) x86_64 x86_64 x86_64 GNU/Linux

  run-level 3 May 18 23:39

  SPEC is set to: /root/cpu2006
     Filesystem      Type  Size  Used Avail Use% Mounted on
     /dev/sdb4       xfs   300G  8.8G  292G   3% /
Additional information from dmidecode:

     Warning: Use caution when you interpret this section. The 'dmidecode' program
     reads system data which is "intended to allow hardware to be accurately
     determined", but the intent may not be met, as there are frequent changes to
     hardware, firmware, and the "DMTF SMBIOS" standard.

   BIOS HP I38 03/05/2015
   Memory:
   32x HP 752369-081 16 GB 2 rank 2133 MHz

  (End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/root/cpu2006/libs/32:/root/cpu2006/libs/64:/root/cpu2006/sh"

 Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB
 memory using RedHat EL 7.1
```

Base Compiler Invocation

C benchmarks:
    icc   -m64

C++ benchmarks:
    icpc -m64

Fortran benchmarks:
    ifort -m64

Benchmarks using both Fortran and C:
    icc   -m64 ifort -m64

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Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
   435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
   437.leslie3d: -DSPEC_CPU_LP64
     444.namd: -DSPEC_CPU_LP64
     447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
   454.calculix: -DSPEC_CPU_LP64 -nofor_main
  459.GemsFDTD: -DSPEC_CPU_LP64
     465.tonto: -DSPEC_CPU_LP64
      470.lbm: -DSPEC_CPU_LP64
       481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
   482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
```
    -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
    -ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:
```
    -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
    -ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:
```
    -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:
```
    -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
    -ansi-alias -opt-mem-layout-trans=3
```

Peak Compiler Invocation

C benchmarks:
```
    icc   -m64
```

C++ benchmarks (except as noted below):
```
    icpc  -m64

   450.soplex: icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

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Peak Compiler Invocation (Continued)

Fortran benchmarks:
        ifort -m64

Benchmarks using both Fortran and C:
        icc   -m64 ifort -m64

Peak Portability Flags

        410.bwaves: -DSPEC_CPU_LP64
        416.gamess: -DSPEC_CPU_LP64
          433.milc: -DSPEC_CPU_LP64
        434.zeusmp: -DSPEC_CPU_LP64
       435.gromacs: -DSPEC_CPU_LP64 -nofor_main
      436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
       437.leslie3d: -DSPEC_CPU_LP64
          444.namd: -DSPEC_CPU_LP64
         447.dealII: -DSPEC_CPU_LP64
         453.povray: -DSPEC_CPU_LP64
       454.calculix: -DSPEC_CPU_LP64 -nofor_main
      459.GemsFDTD: -DSPEC_CPU_LP64
          465.tonto: -DSPEC_CPU_LP64
           470.lbm: -DSPEC_CPU_LP64
           481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
        482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

        433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2)
                  -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
                  -auto-ilp32

         470.lbm: basepeak = yes

        482.sphinx3: basepeak = yes

C++ benchmarks:

        444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2)
                  -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias
                  -auto-ilp32

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Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
-auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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