



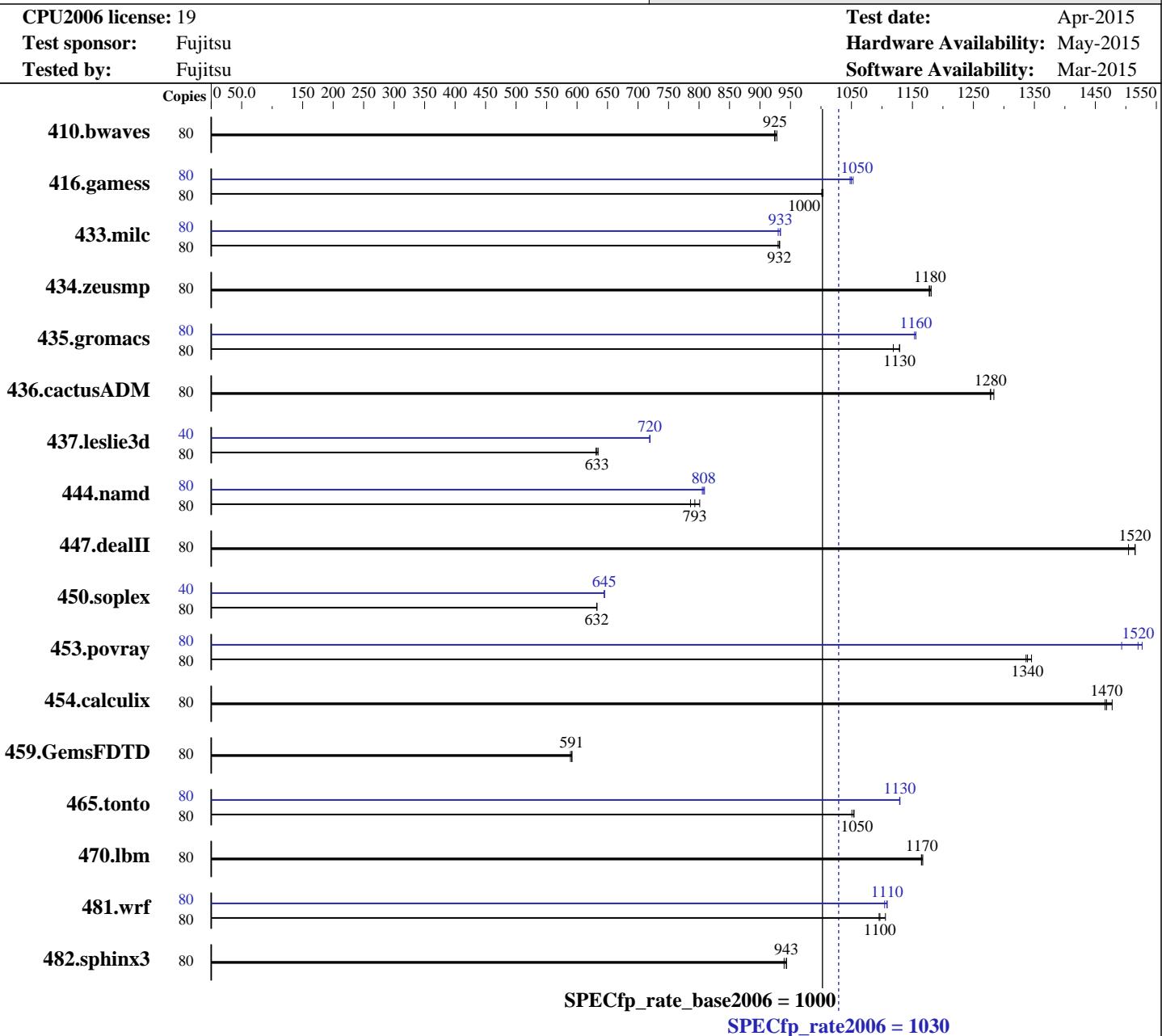
SPEC® CFP2006 Result

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Fujitsu

PRIMERGY RX4770 M2, Intel Xeon E7-4820 v3, 1.90 GHz

SPECfp_rate2006 = 1030
SPECfp_rate_base2006 = 1000



Hardware

CPU Name: Intel Xeon E7-4820 v3
CPU Characteristics:
CPU MHz: 1900
FPU: Integrated
CPU(s) enabled: 40 cores, 4 chips, 10 cores/chip, 2 threads/core
CPU(s) orderable: 2,4 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: Red Hat Enterprise Linux Server release 7.1 (Maipo)
Compiler: 3.10.0-229.el7.x86_64
C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;
Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux
Auto Parallel: No
File System: ext4

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Hardware Availability: May-2015

Software Availability: Mar-2015

L3 Cache: 25 MB I+D on chip per chip
 Other Cache: None
 Memory: 512 GB (32 x 16 GB 2Rx4 PC4-2133P-R, running at 1333 MHz)
 Disk Subsystem: 1 x SATA, 600 GB, 10000 RPM
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	80	1177	924	<u>1176</u>	<u>925</u>	1172	928	80	1177	924	<u>1176</u>	<u>925</u>	1172	928		
416.gamess	80	1564	1000	<u>1563</u>	<u>1000</u>	1561	1000	80	1488	1050	<u>1492</u>	<u>1050</u>	1495	1050		
433.milc	80	788	932	790	930	<u>788</u>	<u>932</u>	80	790	930	<u>787</u>	<u>933</u>	787	933		
434.zeusmp	80	618	1180	<u>618</u>	<u>1180</u>	617	1180	80	618	1180	<u>618</u>	<u>1180</u>	617	1180		
435.gromacs	80	<u>506</u>	<u>1130</u>	511	1120	506	1130	80	494	1160	495	1150	<u>494</u>	<u>1160</u>		
436.cactusADM	80	748	1280	<u>748</u>	<u>1280</u>	745	1280	80	748	1280	<u>748</u>	<u>1280</u>	745	1280		
437.leslie3d	80	<u>1188</u>	<u>633</u>	1192	631	1185	635	40	523	720	523	719	<u>523</u>	<u>720</u>		
444.namd	80	801	801	<u>809</u>	<u>793</u>	816	786	80	793	809	<u>794</u>	<u>808</u>	797	805		
447.dealII	80	<u>604</u>	<u>1520</u>	608	1500	604	1520	80	<u>604</u>	<u>1520</u>	608	1500	604	1520		
450.soplex	80	1054	633	1055	632	<u>1055</u>	<u>632</u>	40	518	644	517	645	<u>518</u>	<u>645</u>		
453.povray	80	316	1350	318	1340	<u>318</u>	<u>1340</u>	80	279	1530	<u>280</u>	<u>1520</u>	285	1490		
454.calculix	80	<u>449</u>	<u>1470</u>	450	1470	447	1480	80	<u>449</u>	<u>1470</u>	450	1470	447	1480		
459.GemsFDTD	80	1434	592	1441	589	<u>1436</u>	<u>591</u>	80	1434	592	1441	589	<u>1436</u>	<u>591</u>		
465.tonto	80	747	1050	749	1050	<u>747</u>	<u>1050</u>	80	697	1130	697	1130	<u>697</u>	<u>1130</u>		
470.lbm	80	944	1160	942	1170	<u>943</u>	<u>1170</u>	80	944	1160	942	1170	<u>943</u>	<u>1170</u>		
481.wrf	80	808	1110	816	1100	<u>815</u>	<u>1100</u>	80	809	1100	806	1110	<u>806</u>	<u>1110</u>		
482.sphinx3	80	1659	940	1653	943	<u>1653</u>	<u>943</u>	80	1659	940	1653	943	<u>1653</u>	<u>943</u>		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
 Energy Performance = Performance



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General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/home/SPECcpu2006/libs/32:/home/SPECcpu2006/libs/64:/home/SPECcpu2006/sh"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

For information about Fujitsu please visit: <http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64



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Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias
```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks (except as noted below):

```
icpc -m64
```

```
450.soplex: icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

Peak Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64  
416.gamess: -DSPEC_CPU_LP64  
433.milc: -DSPEC_CPU_LP64  
434.zeusmp: -DSPEC_CPU_LP64  
435.gromacs: -DSPEC_CPU_LP64 -nofor_main  
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main  
437.leslie3d: -DSPEC_CPU_LP64  
444.namd: -DSPEC_CPU_LP64  
447.dealII: -DSPEC_CPU_LP64  
453.povray: -DSPEC_CPU_LP64  
454.calculix: -DSPEC_CPU_LP64 -nofor_main  
459.GemsFDTD: -DSPEC_CPU_LP64
```

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Peak Portability Flags (Continued)

465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll14
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

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Peak Optimization Flags (Continued)

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll14
-auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.html>
<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.xml>
<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

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