SPEC® CFP2006 Result

Fujitsu

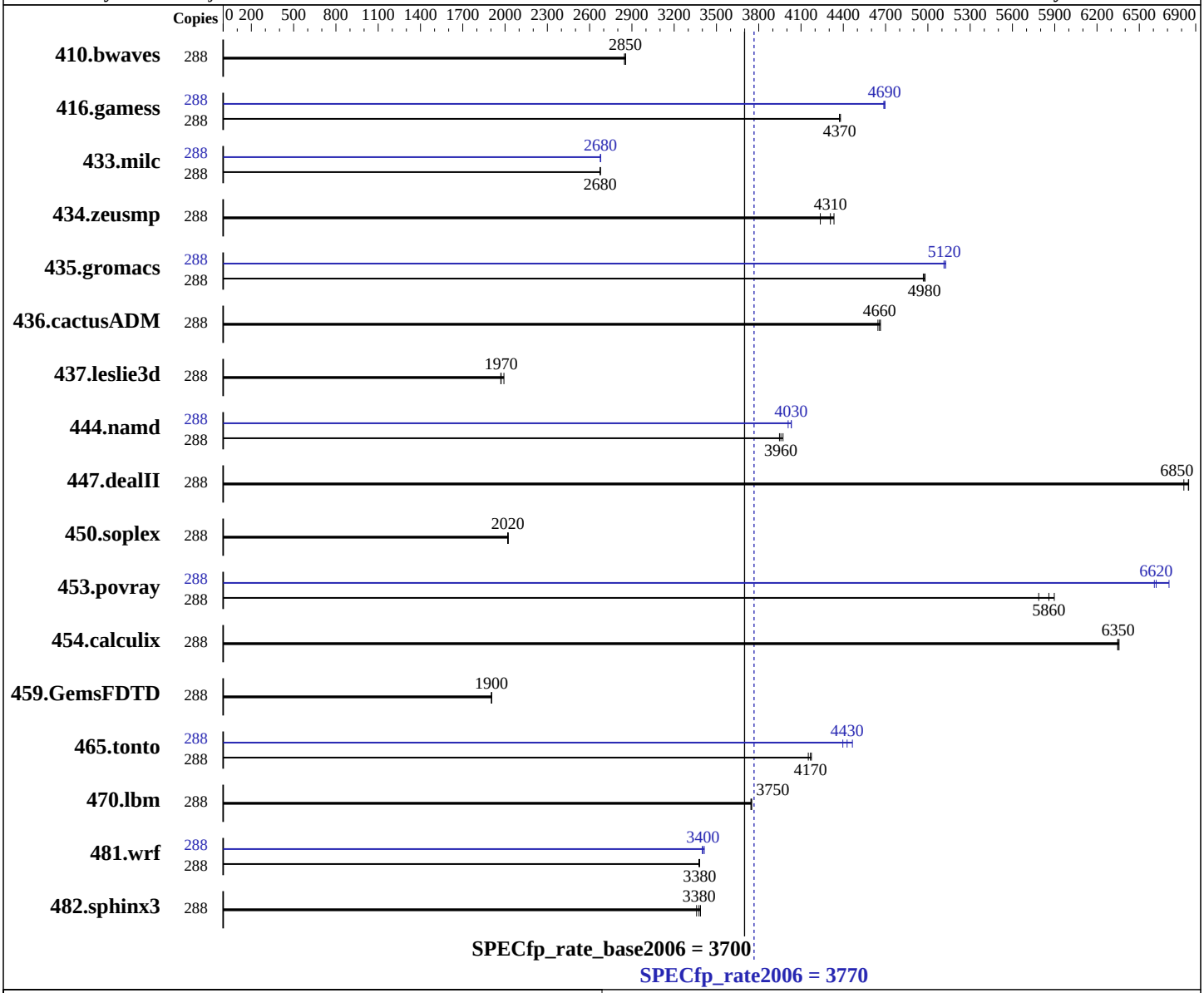
PRIMEQUEST 2800E2, Intel Xeon E7-8880 v3, 2.30 GHz

SPECfp®_rate2006 = 3770

SPECfp_rate_base2006 = 3700

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: May-2015
Hardware Availability: May-2015
Software Availability: Mar-2015



Hardware

CPU Name:	Intel Xeon E7-8880 v3
CPU Characteristics:	Intel Turbo Boost Technology up to 3.10 GHz
CPU MHz:	2300
FPU:	Integrated
CPU(s) enabled:	144 cores, 8 chips, 18 cores/chip, 2 threads/core
CPU(s) orderable:	1,2,4,6,8 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	Red Hat Enterprise Linux Server release 7.1 (Maipo) 3.10.0-229.el7.x86_64
Compiler:	C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux; Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux
Auto Parallel:	No
File System:	ext4

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L3 Cache: 45 MB I+D on chip per chip
Other Cache: None
Memory: 2 TB (128 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)
Disk Subsystem: 2 x SATA, 600 GB, 10000 RPM
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	288	**1372**	**2850**	1375	2850	1370	2860	288	**1372**	**2850**	1375	2850	1370	2860
416.gamess	288	1290	4370	1287	4380	**1289**	**4370**	288	1203	4690	**1202**	**4690**	1201	4700
433.milc	288	**988**	**2680**	989	2670	988	2680	288	987	2680	988	2680	**988**	**2680**
434.zeusmp	288	**608**	**4310**	605	4330	619	4240	288	**608**	**4310**	605	4330	619	4240
435.gromacs	288	**413**	**4980**	413	4980	414	4970	288	401	5130	402	5110	**402**	**5120**
436.cactusADM	288	741	4650	**739**	**4660**	738	4660	288	741	4650	**739**	**4660**	738	4660
437.leslie3d	288	1359	1990	1374	1970	**1373**	**1970**	288	1359	1990	1374	1970	**1373**	**1970**
444.namd	288	581	3970	**584**	**3960**	585	3950	288	573	4030	576	4010	**573**	**4030**
447.dealII	288	**481**	**6850**	481	6850	483	6820	288	**481**	**6850**	481	6850	483	6820
450.soplex	288	1190	2020	1187	2020	**1189**	**2020**	288	1190	2020	1187	2020	**1189**	**2020**
453.povray	288	265	5790	260	5900	**262**	**5860**	288	228	6710	**231**	**6620**	232	6610
454.calculix	288	**374**	**6350**	374	6350	374	6360	288	**374**	**6350**	374	6350	374	6360
459.GemsFDTD	288	**1605**	**1900**	1604	1910	1605	1900	288	**1605**	**1900**	1604	1910	1605	1900
465.tonto	288	**680**	**4170**	679	4170	683	4150	288	**640**	**4430**	645	4400	635	4460
470.lbm	288	1056	3750	1057	3740	**1056**	**3750**	288	1056	3750	1057	3740	**1056**	**3750**
481.wrf	288	953	3380	952	3380	**952**	**3380**	288	942	3410	946	3400	**945**	**3400**
482.sphinx3	288	1657	3390	1671	3360	**1662**	**3380**	288	1657	3390	1671	3360	**1662**	**3380**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS configuration:
Energy Performance = Performance
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/SPECcpu2006/libs/32:/home/SPECcpu2006/libs/64:/home/SPECcpu2006/sh"

 Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB
 memory using RedHat EL 7.0
 Filesystem page cache cleared with:
 echo 1>       /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>

 For information about Fujitsu please visit: http://www.fujitsu.com
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
   435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
   437.leslie3d: -DSPEC_CPU_LP64
     444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
   454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
      465.tonto: -DSPEC_CPU_LP64
       470.lbm: -DSPEC_CPU_LP64
       481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
   482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias`

C++ benchmarks:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias`

Fortran benchmarks:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch`

Benchmarks using both Fortran and C:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias`

Peak Compiler Invocation

C benchmarks:
`icc -m64`

C++ benchmarks:
`icpc -m64`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: `-xCORE-AVX2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-auto-ilp32`

470.lbm: `basepeak = yes`

Continued on next page

Peak Optimization Flags (Continued)

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.html
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.xml
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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