



# SPEC<sup>®</sup> CFP2006 Result

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## ACTION S.A.

ACTINA SOLAR 210 X6 (Intel Xeon E5-2603 v3, 1.60 GHz)

SPECfp<sup>®</sup>\_rate2006 = 285

SPECfp\_rate\_base2006 = 280

CPU2006 license: 9008

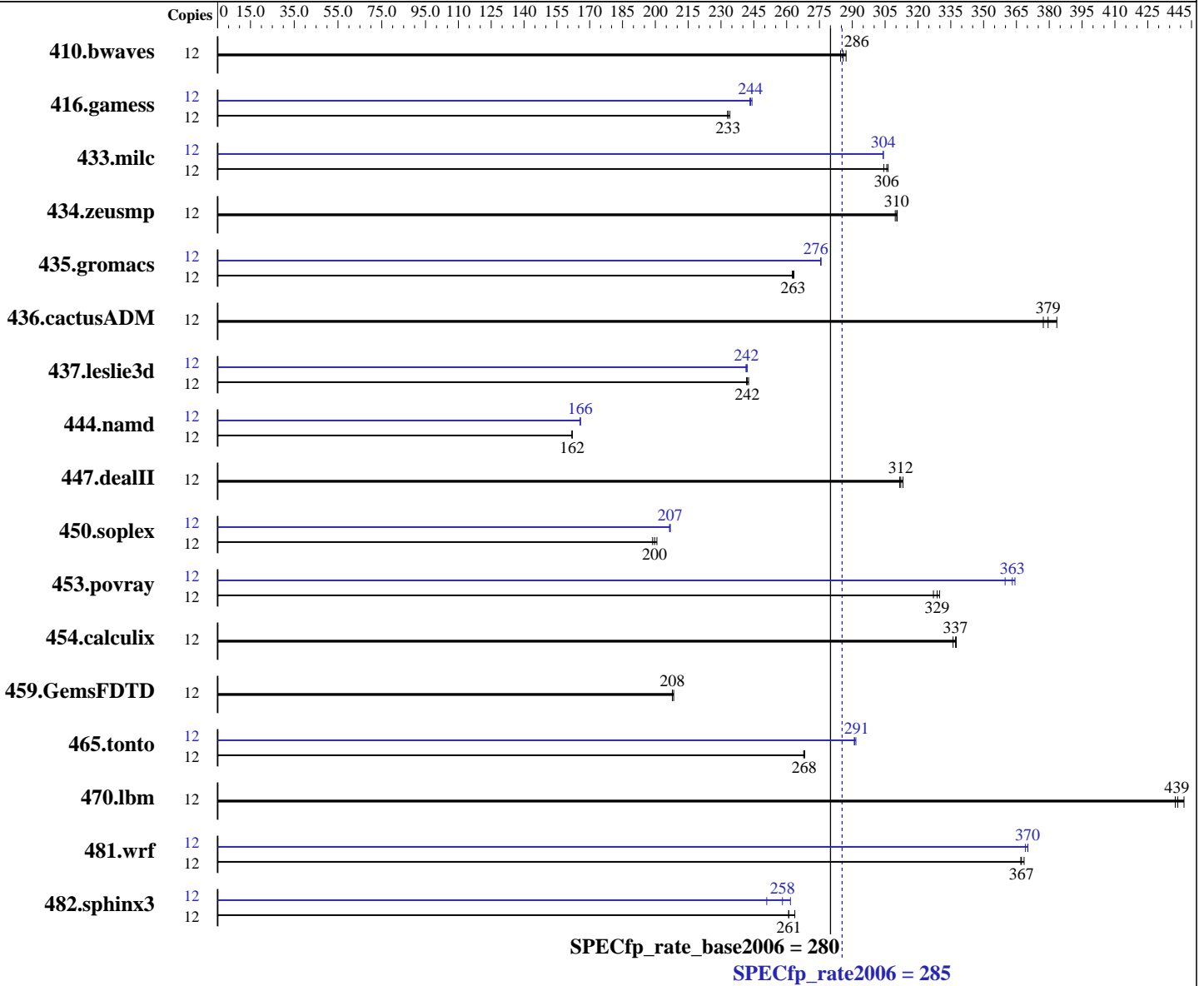
Test sponsor: ACTION S.A.

Tested by: ACTION S.A.

Test date: Apr-2015

Hardware Availability: Sep-2014

Software Availability: Oct-2014



### Hardware

CPU Name: Intel Xeon E5-2603 v3  
 CPU Characteristics:  
 CPU MHz: 1600  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

### Software

Operating System: Red Hat Enterprise Linux Server release 6.6 (Santiago)  
 2.6.32-504.8.1.el6.x86\_64  
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;  
 Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4

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L3 Cache: 15 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)  
Disk Subsystem: 1 x 240 GB SATA II SSD  
Other Hardware: None

System State: Run level 3 (multi-user)  
Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	12	573	285	<b>570</b>	<b>286</b>	568	287	12	573	285	<b>570</b>	<b>286</b>	568	287		
416.gamess	12	1008	233	<b>1007</b>	<b>233</b>	1004	234	12	<b>965</b>	<b>244</b>	962	244	966	243		
433.milc	12	360	306	<b>360</b>	<b>306</b>	362	304	12	362	304	362	304	<b>362</b>	<b>304</b>		
434.zeusmp	12	352	311	353	310	<b>352</b>	<b>310</b>	12	352	311	353	310	<b>352</b>	<b>310</b>		
435.gromacs	12	325	263	<b>326</b>	<b>263</b>	326	263	12	311	276	311	276	<b>311</b>	<b>276</b>		
436.cactusADM	12	<b>378</b>	<b>379</b>	380	377	374	383	12	<b>378</b>	<b>379</b>	380	377	374	383		
437.leslie3d	12	465	243	467	242	<b>466</b>	<b>242</b>	12	467	241	466	242	<b>467</b>	<b>242</b>		
444.namd	12	594	162	594	162	<b>594</b>	<b>162</b>	12	<b>581</b>	<b>166</b>	581	166	581	166		
447.dealII	12	440	312	<b>440</b>	<b>312</b>	438	313	12	440	312	<b>440</b>	<b>312</b>	438	313		
450.soplex	12	504	199	499	201	<b>501</b>	<b>200</b>	12	<b>484</b>	<b>207</b>	484	207	485	206		
453.povray	12	<b>194</b>	<b>329</b>	195	327	194	330	12	<b>176</b>	<b>363</b>	177	360	175	364		
454.calculix	12	293	338	295	336	<b>294</b>	<b>337</b>	12	293	338	295	336	<b>294</b>	<b>337</b>		
459.GemsFDTD	12	<b>613</b>	<b>208</b>	613	208	611	208	12	<b>613</b>	<b>208</b>	613	208	611	208		
465.tonto	12	440	268	<b>440</b>	<b>268</b>	441	268	12	<b>406</b>	<b>291</b>	405	292	406	291		
470.lbm	12	377	438	373	442	<b>376</b>	<b>439</b>	12	377	438	373	442	<b>376</b>	<b>439</b>		
481.wrf	12	365	367	<b>365</b>	<b>367</b>	364	368	12	363	369	362	370	<b>362</b>	<b>370</b>		
482.sphinx3	12	<b>896</b>	<b>261</b>	887	264	896	261	12	894	262	932	251	<b>906</b>	<b>258</b>		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

Bios Settings:  
Power & Performance = Performance

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### Platform Notes (Continued)

Enforce POR = Disabled  
Memory Operating Speed Selection = Auto  
Set Fan Profile = Performance  
Fan PWM Offset = 0

Sysinfo program /cpu2006.1.2/config/sysinfo.rev6818  
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191  
running on SUT Sat Apr 11 03:04:19 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2603 v3 @ 1.60GHz
 2 "physical id"s (chips)
 12 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 6
  siblings  : 6
  physical 0: cores 0 1 2 3 4 5
  physical 1: cores 0 1 2 3 4 5
cache size : 15360 KB
```

```
From /proc/meminfo
MemTotal:      264424040 kB
HugePages_Total:    0
Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.6 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.6 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.6 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux SUT 2.6.32-504.8.1.el6.x86_64 #1 SMP Wed Mar 11 12:12:13 CET 2015
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Apr 10 16:09
```

```
SPEC is set to: /cpu2006.1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sdal        ext4  212G   37G  165G  18% /
```

```
Additional information from dmidecode:
BIOS Intel Corporation SE5C610.86B.01.01.0008.021120151325 02/11/2015
Memory:
```

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## Platform Notes (Continued)

16x 16 GB  
16x Micron 36ASF2G72PZ-2G1A2 16 GB 1600 MHz 2 rank  
8x NO DIMM NO DIMM

(End of data from sysinfo program)  
dmidecode does not properly detect memory modules  
16 modules of 16 GB were used to run the test (256 GB total)

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/cpu2006.1.2/libs/32:/cpu2006.1.2/libs/64:/cpu2006.1.2/sh"

Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1> /proc/sys/vm/drop\_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>  
Binaries compiled on a system with 2x Xeon E5-2650 v3 chips + 256 GB memory  
using RedHat EL 6.6

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64

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## Base Portability Flags (Continued)

```
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

## Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

## Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks (except as noted below):

```
icpc -m64
```

```
450.soplex: icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```



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## Peak Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Peak Optimization Flags

C benchmarks:

```

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
         -O3(pass 2) -no-prec-div(pass 2)
         -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
         -auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-mem-layout-trans=3
            -unroll2

```

C++ benchmarks:

```

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
         -O3(pass 2) -no-prec-div(pass 2)
         -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias
         -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
         -O3(pass 2) -no-prec-div(pass 2)
         -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
         -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
         -O3(pass 2) -no-prec-div(pass 2)
         -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4
         -ansi-alias

```

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## Peak Optimization Flags (Continued)

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4  
-auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2)  
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/ACTION.SA-Platform-Flags-RevA-mar-2015-For-Intel-Platform.20150519.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/ACTION.SA-Platform-Flags-RevA-mar-2015-For-Intel-Platform.20150519.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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