



SPEC[®] CFP2006 Result

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Dell Inc.

SPECfp[®]_rate2006 = 1550

PowerEdge R930 (Intel Xeon E7-4850 v3, 2.20 GHz)

SPECfp_rate_base2006 = 1510

CPU2006 license: 55

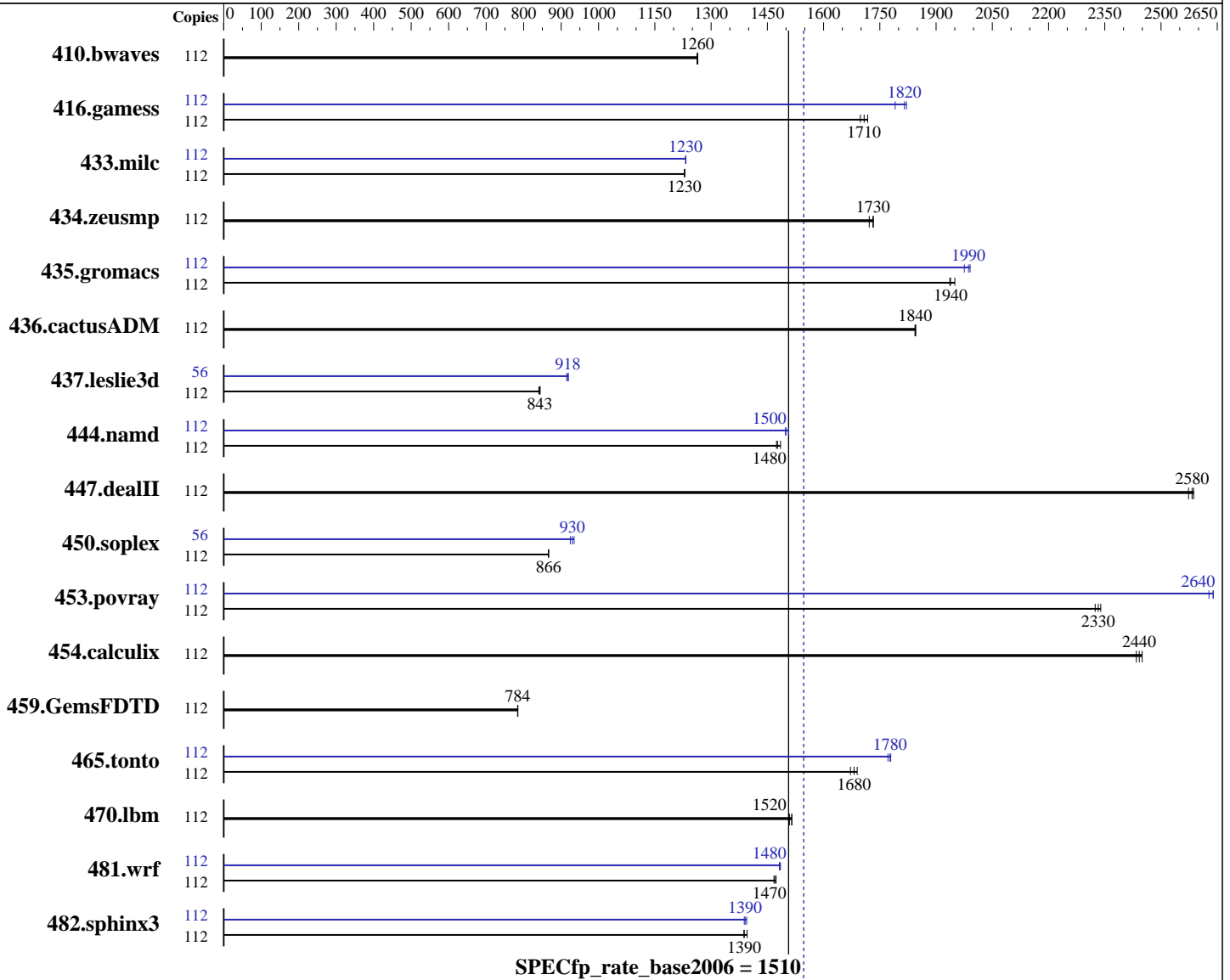
Test date: Apr-2015

Test sponsor: Dell Inc.

Hardware Availability: Jun-2015

Tested by: Dell Inc.

Software Availability: Oct-2014



Hardware

CPU Name: Intel Xeon E7-4850 v3
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
 CPU MHz: 2200
 FPU: Integrated
 CPU(s) enabled: 56 cores, 4 chips, 14 cores/chip, 2 threads/core
 CPU(s) orderable: 2,4 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 12
 3.12.28-4-default
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;
 Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)

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L3 Cache: 35 MB I+D on chip per chip
 Other Cache: None
 Memory: 512 GB (32 x 16 GB 2Rx4 PC4-2133P-R, running at 1333 MHz)
 Disk Subsystem: 2 x 200 GB SAS6 SSD, RAID0
 Other Hardware: None

Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

| Benchmark | Base | | | | | | | | Peak | | | | | | | |
|---------------|--------|-------------|-------------|-------------|-------------|-------------|-------------|--------|-------------|-------------|-------------|-------------|-------------|-------------|--|--|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | | |
| 410.bwaves | 112 | 1205 | 1260 | 1205 | 1260 | 1204 | 1260 | 112 | 1205 | 1260 | 1205 | 1260 | 1204 | 1260 | | |
| 416.gamess | 112 | 1284 | 1710 | 1291 | 1700 | 1277 | 1720 | 112 | 1204 | 1820 | 1224 | 1790 | 1208 | 1820 | | |
| 433.milc | 112 | 836 | 1230 | 836 | 1230 | 837 | 1230 | 112 | 834 | 1230 | 834 | 1230 | 834 | 1230 | | |
| 434.zeusmp | 112 | 589 | 1730 | 588 | 1730 | 592 | 1720 | 112 | 589 | 1730 | 588 | 1730 | 592 | 1720 | | |
| 435.gromacs | 112 | 410 | 1950 | 413 | 1940 | 412 | 1940 | 112 | 405 | 1980 | 403 | 1990 | 402 | 1990 | | |
| 436.cactusADM | 112 | 725 | 1850 | 726 | 1840 | 726 | 1840 | 112 | 725 | 1850 | 726 | 1840 | 726 | 1840 | | |
| 437.leslie3d | 112 | 1247 | 844 | 1253 | 841 | 1249 | 843 | 56 | 573 | 919 | 573 | 918 | 576 | 915 | | |
| 444.namd | 112 | 605 | 1490 | 608 | 1480 | 609 | 1470 | 112 | 596 | 1510 | 599 | 1500 | 599 | 1500 | | |
| 447.dealII | 112 | 498 | 2570 | 495 | 2590 | 496 | 2580 | 112 | 498 | 2570 | 495 | 2590 | 496 | 2580 | | |
| 450.soplex | 112 | 1078 | 866 | 1078 | 867 | 1079 | 866 | 56 | 505 | 925 | 502 | 930 | 500 | 935 | | |
| 453.povray | 112 | 256 | 2320 | 255 | 2330 | 255 | 2340 | 112 | 227 | 2630 | 226 | 2640 | 226 | 2640 | | |
| 454.calculix | 112 | 378 | 2440 | 377 | 2450 | 380 | 2430 | 112 | 378 | 2440 | 377 | 2450 | 380 | 2430 | | |
| 459.GemsFDTD | 112 | 1516 | 784 | 1516 | 784 | 1514 | 785 | 112 | 1516 | 784 | 1516 | 784 | 1514 | 785 | | |
| 465.tonto | 112 | 659 | 1670 | 656 | 1680 | 652 | 1690 | 112 | 620 | 1780 | 622 | 1770 | 619 | 1780 | | |
| 470.lbm | 112 | 1016 | 1520 | 1020 | 1510 | 1016 | 1520 | 112 | 1016 | 1520 | 1020 | 1510 | 1016 | 1520 | | |
| 481.wrf | 112 | 851 | 1470 | 852 | 1470 | 849 | 1470 | 112 | 844 | 1480 | 843 | 1480 | 842 | 1480 | | |
| 482.sphinx3 | 112 | 1573 | 1390 | 1564 | 1400 | 1572 | 1390 | 112 | 1565 | 1390 | 1572 | 1390 | 1570 | 1390 | | |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Settings:
Virtualization Technology disabled
System Profile set to Custom

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Platform Notes (Continued)

CPU Power Management set to Maximum Performance
 Memory Frequency set to Maximum Performance
 Turbo Boost enabled
 Energy Efficient Turbo disabled
 C1E disabled
 C States disabled
 Collaborative CPU Performance Control disabled
 Memory Patrol Scrub disabled
 Memory Refresh Rate set to 1x
 Uncore Frequency set to Maximum
 Energy Efficient Policy set to Performance
 Monitor/MWait enabled

Sysinfo program

```
/root/Desktop/Performance/ic15.0_Aug29_2014/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on linux-luzu Wed Apr 8 04:32:00 2015
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) CPU E7-4850 v3 @ 2.20GHz
 4 "physical id"s (chips)
 112 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 14
siblings : 28
physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
physical 2: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
physical 3: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
cache size : 35840 KB
```

From /proc/meminfo

```
MemTotal: 529204236 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

/usr/bin/lsc_release -d

SUSE Linux Enterprise Server 12

From /etc/*release* /etc/*version*

```
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 0
# This file is deprecated and will be removed in a future service pack or
release.
```

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Platform Notes (Continued)

```
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12"
VERSION_ID="12"
PRETTY_NAME="SUSE Linux Enterprise Server 12"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12"

uname -a:
Linux linux-luzu 3.12.28-4-default #1 SMP Thu Sep 25 17:02:34 UTC 2014
(9879bd4) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Apr 7 15:01 last=5

SPEC is set to: /root/Desktop/Performance/ic15.0_Aug29_2014
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda3       ext4  325G   21G  304G   7% /home
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 1.0.1 [MRC_096] 03/27/2015
Memory:
32x 00AD00B300AD Not Specified 16 GB 2 rank 2133 MHz, configured at 1333 MHz
64x Not Specified Not Specified

(End of data from sysinfo program)
```

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = */root/Desktop/Performance/ic15.0_Aug29_2014/libs/32:/root/Desktop/Performance/ic15.0_Aug29_2014/libs/64:/root/Desktop/Performance/ic15.0_Aug29_2014/sh*

```
Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB
memory using RedHat EL 7.0
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```



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Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

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Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

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Peak Optimization Flags (Continued)

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
-auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-mem-layout-trans=3
-unroll2

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
-auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

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Peak Optimization Flags (Continued)

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revE.20150421.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revE.20150421.xml>

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