



SPEC® CFP2006 Result

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ACTION S.A.

ACTINA SOLAR 202 S6 (Intel Xeon E5-2640 v3, 2.60 GHz)

SPECfp[®]_rate2006 = 602

SPECfp_rate_base2006 = 589

CPU2006 license: 9008

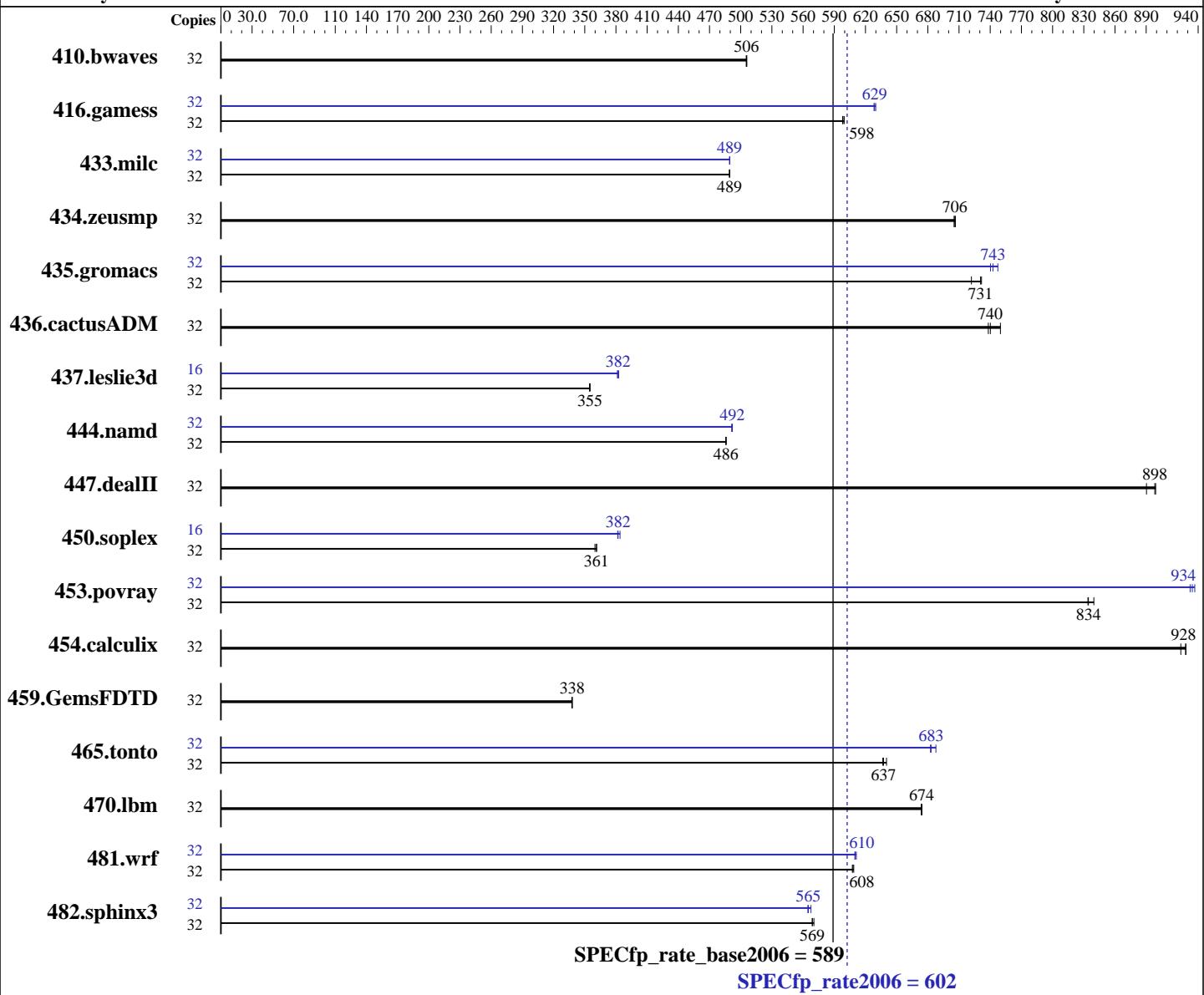
Test date: Mar-2015

Test sponsor: ACTION S.A.

Hardware Availability: Sep-2014

Tested by: ACTION S.A.

Software Availability: Oct-2014



Hardware

CPU Name: Intel Xeon E5-2640 v3
 CPU Characteristics: Intel Turbo Boost Technology up to 3.40 GHz
 CPU MHz: 2600
 FPU: Integrated
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: Red Hat Enterprise Linux Server release 6.6 (Santiago)
 Compiler: 2.6.32-504.8.1.el6.x86_64
 C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;
 Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux
 Auto Parallel: No
 File System: ext4

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L3 Cache: 20 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R, running at 1866 MHz)
 Disk Subsystem: 1 x 240 GB SATA II SSD
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	32	860	506	<u>860</u>	<u>506</u>	861	505	32	860	506	<u>860</u>	<u>506</u>	861	505		
416.gamess	32	1048	598	1045	600	<u>1047</u>	<u>598</u>	32	<u>996</u>	<u>629</u>	997	628	995	630		
433.milc	32	600	489	<u>600</u>	<u>489</u>	601	489	32	600	489	<u>600</u>	<u>489</u>	600	489		
434.zeusmp	32	413	705	<u>412</u>	<u>706</u>	412	706	32	413	705	<u>412</u>	<u>706</u>	412	706		
435.gromacs	32	<u>313</u>	<u>731</u>	316	722	312	732	32	306	747	309	740	<u>308</u>	<u>743</u>		
436.cactusADM	32	518	738	<u>517</u>	<u>740</u>	510	750	32	518	738	<u>517</u>	<u>740</u>	510	750		
437.leslie3d	32	<u>847</u>	<u>355</u>	848	355	847	355	16	394	381	393	383	<u>394</u>	<u>382</u>		
444.namd	32	<u>528</u>	<u>486</u>	528	486	528	486	32	522	492	<u>522</u>	<u>492</u>	522	491		
447.dealII	32	<u>408</u>	<u>898</u>	411	890	407	899	32	<u>408</u>	<u>898</u>	411	890	407	899		
450.soplex	32	738	361	<u>739</u>	<u>361</u>	742	360	16	350	382	348	384	<u>349</u>	<u>382</u>		
453.povray	32	204	834	<u>204</u>	<u>834</u>	203	840	32	183	932	182	937	<u>182</u>	<u>934</u>		
454.calculix	32	<u>285</u>	<u>928</u>	284	928	286	923	32	<u>285</u>	<u>928</u>	284	928	286	923		
459.GemsFDTD	32	1004	338	<u>1005</u>	<u>338</u>	1006	338	32	1004	338	<u>1005</u>	<u>338</u>	1006	338		
465.tonto	32	492	640	<u>494</u>	<u>637</u>	495	637	32	<u>461</u>	<u>683</u>	461	683	458	688		
470.lbm	32	<u>652</u>	<u>674</u>	652	674	653	674	32	<u>652</u>	<u>674</u>	652	674	653	674		
481.wrf	32	589	607	<u>588</u>	<u>608</u>	587	609	32	586	610	<u>586</u>	<u>610</u>	585	611		
482.sphinx3	32	1093	570	<u>1096</u>	<u>569</u>	1096	569	32	<u>1104</u>	<u>565</u>	1105	565	1099	568		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

Bios Settings

Power Technology = Energy Efficient

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Hardware Availability: Sep-2014

Tested by: ACTION S.A.

Software Availability: Oct-2014

Platform Notes (Continued)

Enforce POR = Disabled

BMC Setting

Fan Mode = Full Speed

```
Sysinfo program /cpu2006.1.2/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on SUT Tue Mar 24 03:45:42 2015
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2640 v3 @ 2.60GHz
        2 "physical id"s (chips)
        32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
        cpu cores : 8
        siblings   : 16
        physical 0: cores 0 1 2 3 4 5 6 7
        physical 1: cores 0 1 2 3 4 5 6 7
cache size : 20480 KB
```

```
From /proc/meminfo
MemTotal:      264425916 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.6 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.6 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.6 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux SUT 2.6.32-504.8.1.el6.x86_64 #1 SMP Wed Mar 11 12:12:13 CET 2015
x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Mar 23 17:01

```
SPEC is set to: /cpu2006.1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sdal      ext4  212G   37G  165G  18%  /
```

Additional information from dmidecode:
BIOS American Megatrends Inc. 1.0c 02/12/2015
Memory:

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Platform Notes (Continued)

16x 16 GB
2x Samsung(date:14/33) M393A2G40DB0-CPB 16 GB 1866 MHz 2 rank
14x Samsung(date:14/40) M393A2G40DB0-CPB 16 GB 1866 MHz 2 rank

(End of data from sysinfo program)
dmidecode does not properly detect memory modules
16 modules of 16 GB were used to run the test (256 GB total)

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/cpu2006.1.2/libs/32:/cpu2006.1.2/libs/64:/cpu2006.1.2/sh"

Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
Binaries compiled on a system with 2x Xeon E5-2650 v3 chips + 256 GB memory
using RedHat EL 6.6

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

```
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks (except as noted below):

```
icpc -m64
```

450.soplex: icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```



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Software Availability: Oct-2014

Peak Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
    435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
        444.namd: -DSPEC_CPU_LP64
        447.dealII: -DSPEC_CPU_LP64
        453.povray: -DSPEC_CPU_LP64
        454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
        481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Peak Optimization Flags

C benchmarks:

```

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
    -O3(pass 2) -no-prec-div(pass 2)
    -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
    -auto-ilp32

```

470.lbm: basepeak = yes

```

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-mem-layout-trans=3
    -unroll12

```

C++ benchmarks:

```

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
    -O3(pass 2) -no-prec-div(pass 2)
    -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias
    -auto-ilp32

```

447.dealII: basepeak = yes

```

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
    -O3(pass 2) -no-prec-div(pass 2)
    -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
    -opt-malloc-options=3

```

```

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
    -O3(pass 2) -no-prec-div(pass 2)
    -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll14
    -ansi-alias

```

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Peak Optimization Flags (Continued)

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
-auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/ACTION.SA-Platform-Flags-RevC-jan-2015-For-Supermicro-Platform.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/ACTION.SA-Platform-Flags-RevC-jan-2015-For-Supermicro-Platform.xml>



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