



SPEC® CFP2006 Result

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ACTION S.A.

ACTINA SOLAR 222 S6 (Intel Xeon E5-2603 v3, 1.60 GHz)

SPECfp[®]_rate2006 = 286

SPECfp_rate_base2006 = 280

CPU2006 license: 9008

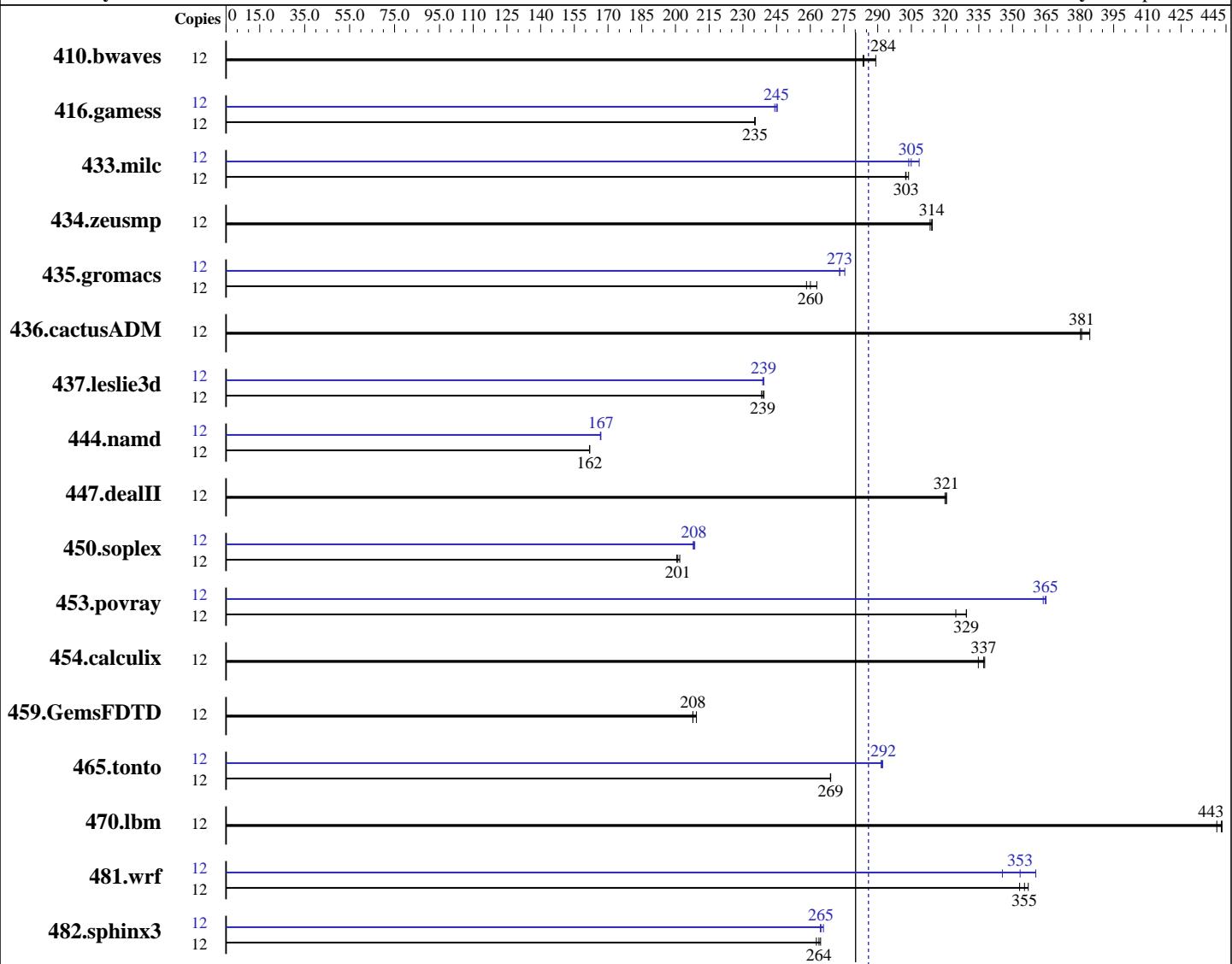
Test date: Mar-2015

Test sponsor: ACTION S.A.

Hardware Availability: Sep-2014

Tested by: ACTION S.A.

Software Availability: Sep-2014



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Hardware

CPU Name: Intel Xeon E5-2603 v3
CPU Characteristics:
CPU MHz:
FPU:
CPU(s) enabled: 1600
CPU(s) orderable: Integrated
Primary Cache: 12 cores, 2 chips, 6 cores/chip
Secondary Cache: 1,2 chips
32 KB I + 32 KB D on chip per core
256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)
Compiler: 3.10.0-123.20.1.el7.x86_64
C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;
Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux
Auto Parallel: No
File System: ext4
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L3 Cache: 15 MB I+D on chip per chip
 Other Cache: None
 Memory: 128 GB (16 x 8 GB 1Rx4 PC4-2133P-R, running at 1600 MHz)
 Disk Subsystem: 1 x 240 GB SATA II SSD
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	12	575	283	<u>575</u>	<u>284</u>	564	289	12	575	283	<u>575</u>	<u>284</u>	564	289		
416.gamess	12	998	235	999	235	<u>998</u>	<u>235</u>	12	<u>959</u>	<u>245</u>	962	244	958	245		
433.milc	12	364	302	363	304	<u>364</u>	<u>303</u>	12	<u>361</u>	<u>305</u>	363	304	357	308		
434.zeusmp	12	349	313	347	314	<u>348</u>	<u>314</u>	12	349	313	347	314	<u>348</u>	<u>314</u>		
435.gromacs	12	326	263	<u>329</u>	<u>260</u>	332	258	12	314	273	311	275	<u>314</u>	<u>273</u>		
436.cactusADM	12	<u>377</u>	<u>381</u>	377	380	373	384	12	<u>377</u>	<u>381</u>	377	380	373	384		
437.leslie3d	12	<u>472</u>	<u>239</u>	471	239	473	238	12	<u>472</u>	<u>239</u>	471	239	472	239		
444.namd	12	595	162	595	162	<u>595</u>	<u>162</u>	12	<u>577</u>	167	578	167	<u>577</u>	<u>167</u>		
447.dealII	12	<u>428</u>	<u>321</u>	429	320	428	321	12	<u>428</u>	<u>321</u>	429	320	428	321		
450.soplex	12	<u>498</u>	<u>201</u>	499	201	496	202	12	480	209	481	208	<u>481</u>	<u>208</u>		
453.povray	12	<u>194</u>	<u>329</u>	194	329	197	325	12	176	364	175	365	<u>175</u>	<u>365</u>		
454.calculix	12	<u>294</u>	<u>337</u>	296	335	293	338	12	<u>294</u>	<u>337</u>	296	335	293	338		
459.GemsFDTD	12	608	209	<u>613</u>	<u>208</u>	613	208	12	608	209	<u>613</u>	<u>208</u>	613	208		
465.tonto	12	439	269	439	269	<u>439</u>	<u>269</u>	12	404	292	<u>405</u>	<u>292</u>	405	292		
470.lbm	12	<u>372</u>	<u>443</u>	372	443	374	441	12	<u>372</u>	<u>443</u>	372	443	374	441		
481.wrf	12	375	357	380	353	<u>377</u>	<u>355</u>	12	372	360	<u>379</u>	<u>353</u>	388	345		
482.sphinx3	12	890	263	884	265	<u>886</u>	<u>264</u>	12	<u>883</u>	<u>265</u>	884	264	880	266		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

Bios Settings

Power Technology = Energy Efficient

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Platform Notes (Continued)

Enforce POR = Disabled

BMC Setting

Fan Mode = Full Speed

```
Sysinfo program /cpu2006.1.2/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on localhost.localdomain Thu Mar  5 20:55:12 2015
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2603 v3 @ 1.60GHz
        2 "physical id"s (chips)
        12 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
        cpu cores : 6
        siblings   : 6
        physical 0: cores 0 1 2 3 4 5
        physical 1: cores 0 1 2 3 4 5
cache size : 15360 KB
```

```
From /proc/meminfo
MemTotal:      131761400 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 7.0 (Maipo)
```

```
From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server
```

```
uname -a:
Linux localhost.localdomain 3.10.0-123.20.1.el7.x86_64 #3 SMP Wed Feb 4
14:55:18 CET 2015 x86_64 x86_64 x86_64 GNU/Linux
```

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Platform Notes (Continued)

run-level 3 Mar 5 10:02

```
SPEC is set to: /cpu2006.1.2
Filesystem      Type  Size  Used  Avail Use% Mounted on
/dev/sdal       ext4  212G   26G  176G  13%  /
```

Additional information from dmidecode:

BIOS American Megatrends Inc. 1.0a 11/05/2014

Memory:

```
16x 8 GB
1x Micron(data:14/25) 18ASF1G72PZ-2G1A2 8 GB 1600 MHz 1 rank
2x Micron(data:14/30) 18ASF1G72PZ-2G1A2 8 GB 1600 MHz 1 rank
10x Micron(data:14/32) 18ASF1G72PZ-2G1A2 8 GB 1600 MHz 1 rank
1x Micron(data:14/34) 18ASF1G72PZ-2G1A2 8 GB 1600 MHz 1 rank
2x Micron(data:14/35) 18ASF1G72PZ-2G1A2 8 GB 1600 MHz 1 rank
```

(End of data from sysinfo program)

dmidecode does not properly detect memory modules

16 modules of 8 GB were used to run the test (128 GB total)

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/cpu2006.1.2/lib32:/cpu2006.1.2/lib64:/cpu2006.1.2/sh"

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1> /proc/sys/vm/drop_caches
```

runspec command invoked through numactl i.e.:

```
numactl --interleave=all runspec <etc>
```

Binaries compiled on a system with 2x Xeon E5-2650 v3 chips + 256 GB memory
using RedHat EL 7.0

Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```



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Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks (except as noted below):

```
icpc -m64
```

```
450.soplex: icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

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Peak Compiler Invocation (Continued)

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
-auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-mem-layout-trans=3
-unroll12

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

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Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll14
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
-inline-level=0 -scalar-rep

434.zeusmp: basepeak = yes

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll14
-auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/ACTION.SA-Platform-Flags-RevC-jan-2015-For-Supermicro-Platform.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

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