



SPEC® CINT2006 Result

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Lenovo Group Limited

Lenovo ThinkServer RD640 (Intel Xeon E5-2630 v2, 2.60 GHz)

SPECint®_rate2006 = 498

SPECint_rate_base2006 = 471

CPU2006 license: 9017

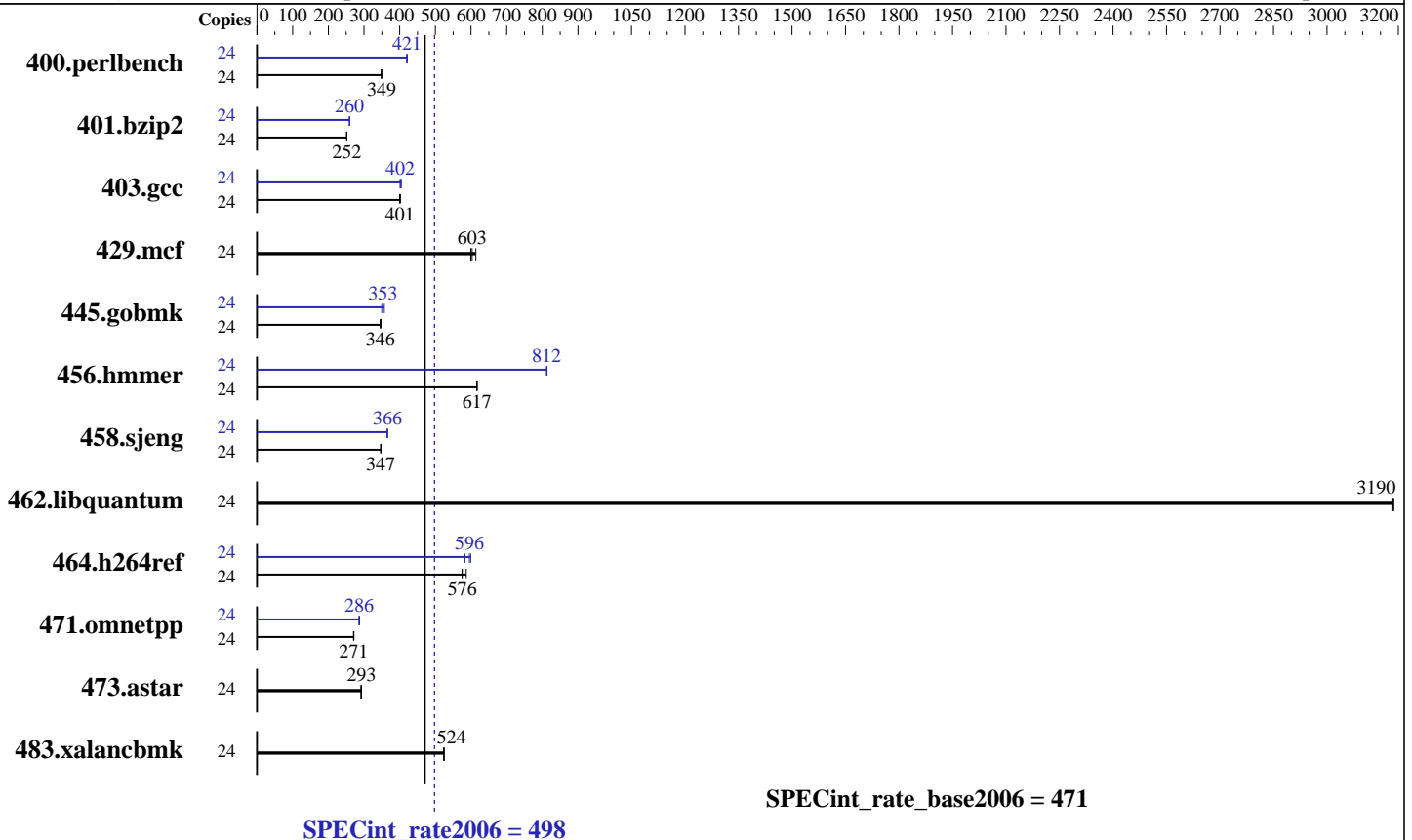
Test sponsor: Lenovo Group Limited

Tested by: Lenovo Group Limited

Test date: Jan-2015

Hardware Availability: Sep-2013

Software Availability: Sep-2014



Hardware

CPU Name: Intel Xeon E5-2630 v2
 CPU Characteristics: Intel Turbo Boost Technology up to 3.10 GHz
 CPU MHz: 2600
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 15 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC, running at 1600 MHz and CL11)
 Disk Subsystem: 1 x 240 SATA SSD
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)
 3.10.0-123.el7.x86_64
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.0



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	671	349	674	348	<u>671</u>	<u>349</u>	24	559	420	556	421	<u>557</u>	<u>421</u>
401.bzip2	24	919	252	923	251	<u>920</u>	<u>252</u>	24	<u>892</u>	<u>260</u>	892	260	894	259
403.gcc	24	481	402	483	400	<u>482</u>	<u>401</u>	24	477	405	<u>481</u>	<u>402</u>	481	401
429.mcf	24	365	599	357	614	<u>363</u>	<u>603</u>	24	365	599	357	614	<u>363</u>	<u>603</u>
445.gobmk	24	729	346	<u>727</u>	<u>346</u>	725	347	24	705	357	720	350	<u>712</u>	<u>353</u>
456.hammer	24	363	616	363	617	<u>363</u>	<u>617</u>	24	<u>276</u>	<u>812</u>	275	813	276	812
458.sjeng	24	835	348	<u>837</u>	<u>347</u>	837	347	24	<u>794</u>	<u>366</u>	796	365	794	366
462.libquantum	24	156	3190	156	3180	<u>156</u>	<u>3190</u>	24	156	3190	156	3180	<u>156</u>	<u>3190</u>
464.h264ref	24	924	575	<u>923</u>	<u>576</u>	905	587	24	886	599	<u>892</u>	<u>596</u>	911	583
471.omnetpp	24	554	271	<u>553</u>	<u>271</u>	552	272	24	524	286	<u>524</u>	<u>286</u>	522	288
473.astar	24	579	291	575	293	<u>575</u>	<u>293</u>	24	579	291	575	293	<u>575</u>	<u>293</u>
483.xalancbmk	24	316	524	316	525	<u>316</u>	<u>524</u>	24	316	524	316	525	<u>316</u>	<u>524</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration
ClE Support set to Disabled
Sysinfo program /usr/cpu2006/config/sysinfo.rev6914
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
running on RD640 Fri Jan 23 07:46:27 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2630 v2 @ 2.60GHz
2 "physical id"s (chips)
24 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

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Platform Notes (Continued)

```

cpu cores : 6
siblings  : 12
physical 0: cores 0 1 2 3 4 5
physical 1: cores 0 1 2 3 4 5
cache size : 15360 KB

```

From /proc/meminfo

```

MemTotal:      263941036 kB
HugePages_Total: 0
Hugepagesize:  2048 kB

```

From /etc/*release* /etc/*version*

```

os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

```

uname -a:

```

Linux RD640 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014 x86_64
x86_64 x86_64 GNU/Linux

```

run-level 3 Jan 23 07:40

SPEC is set to: /usr/cpu2006

```

Filesystem      Type      Size      Used Avail Use% Mounted on
/dev/sda2        xfs       219G      12G  208G   6% /

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS LENOVO A1TS8AA 10/15/2014

Memory:

```

1x DimmA3_Manufacturer DimmA3_PartNumA3
1x DimmB3_Manufacturer DimmB3_PartNumB3
1x DimmC3_Manufacturer DimmC3_PartNumC3
1x DimmD3_Manufacturer DimmD3_PartNumD3
16x Samsung M393B2G70QH0-CMA 16 GB 2 rank 1600 MHz

```

(End of data from sysinfo program)

RD640 support 4 channels and 12 DIMMs for processor0, 4 channels and 8 DIMMs for processor1, total 8 channels and 20 DIMMs. 16 DIMM slots installed with 16 GB DIMM for this run.



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General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"
```

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB
memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1> /proc/sys/vm/drop_caches
```

runspec command invoked through numactl i.e.:

```
numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:

```
icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

C++ benchmarks:

```
icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
```

```
462.libquantum: -DSPEC_CPU_LINUX
```

```
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch
```

C++ benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
```

```
-L/sh -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```



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Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

```
400.perlbench: icc -m64
```

```
401.bzip2: icc -m64
```

```
456.hmmer: icc -m64
```

```
458.sjeng: icc -m64
```

C++ benchmarks:

```
icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
```

```
401.bzip2: -DSPEC_CPU_LP64
```

```
456.hmmer: -DSPEC_CPU_LP64
```

```
458.sjeng: -DSPEC_CPU_LP64
```

```
462.libquantum: -DSPEC_CPU_LINUX
```

```
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32
```

```
401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
-auto-ilp32 -ansi-alias
```

```
403.gcc: -xAVX -ipo -O3 -no-prec-div
```

```
429.mcf: basepeak = yes
```

```
445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias
```

```
456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
```

```
458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4
-auto-ilp32
```

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Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias
-opt-ra-region-strategy=block -Wl,-z,muldefs
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-RD640-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-RD640-revA.xml>

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