



# SPEC® CINT2006 Result

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## Lenovo Group Limited

Lenovo ThinkServer RD650 (Intel Xeon E5-2630 v3, 2.40 GHz)

SPECint®\_rate2006 = **680**

SPECint\_rate\_base2006 = **657**

CPU2006 license: 9017

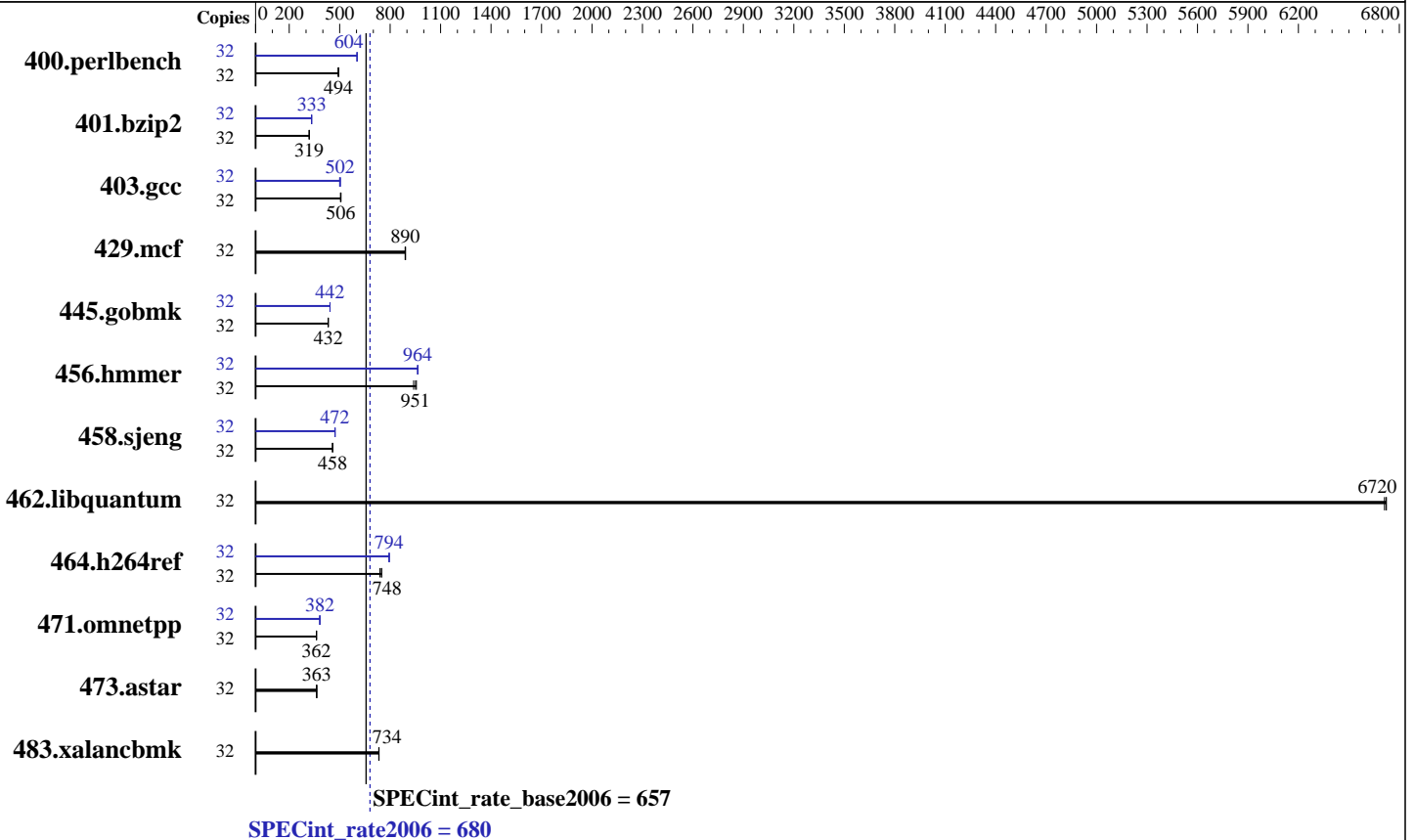
Test sponsor: Lenovo Group Limited

Tested by: Lenovo Group Limited

Test date: Jan-2015

Hardware Availability: Sep-2014

Software Availability: Jan-2014



### Hardware

CPU Name: Intel Xeon E5-2630 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 20 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R, running at 1866 MHz)  
 Disk Subsystem: 1 x 240 GB SATA SSD  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 2.6.32-431.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	32	638	490	633	494	<b>633</b>	<b>494</b>	32	518	604	519	603	<b>518</b>	<b>604</b>
401.bzip2	32	970	318	<b>968</b>	<b>319</b>	967	319	32	<b>927</b>	<b>333</b>	928	333	926	333
403.gcc	32	<b>509</b>	<b>506</b>	507	508	510	505	32	516	500	<b>513</b>	<b>502</b>	510	505
429.mcf	32	<b>328</b>	<b>890</b>	328	889	327	892	32	<b>328</b>	<b>890</b>	328	889	327	892
445.gobmk	32	777	432	777	432	<b>777</b>	<b>432</b>	32	<b>759</b>	<b>442</b>	759	443	759	442
456.hammer	32	318	940	312	956	<b>314</b>	<b>951</b>	32	309	965	<b>310</b>	<b>964</b>	310	963
458.sjeng	32	<b>846</b>	<b>458</b>	846	458	846	458	32	820	472	819	473	<b>820</b>	<b>472</b>
462.libquantum	32	<b>98.7</b>	<b>6720</b>	98.6	6720	98.8	6710	32	<b>98.7</b>	<b>6720</b>	98.6	6720	98.8	6710
464.h264ref	32	958	739	<b>946</b>	<b>748</b>	945	750	32	893	793	<b>892</b>	<b>794</b>	890	795
471.omnetpp	32	<b>553</b>	<b>362</b>	551	363	554	361	32	<b>523</b>	<b>382</b>	525	381	523	382
473.astar	32	616	365	622	361	<b>619</b>	<b>363</b>	32	616	365	622	361	<b>619</b>	<b>363</b>
483.xalancbmk	32	<b>301</b>	<b>734</b>	301	734	301	732	32	<b>301</b>	<b>734</b>	301	734	301	732

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

```

BIOS configuration:
Cluster On Die set to Auto
Early Snoop set to Auto
Performance Profile set to Custom
ClE Support set to Disabled
Core C3 set to Disabled
Core C6 set to Disabled
Thermal Profile set to High Fan Speed
Memory Power Savings set to Disabled
Sysinfo program /usr/cpu2006/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on RD550 Sun Jan 4 22:15:57 2015

```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

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### Platform Notes (Continued)

```

From /proc/cpuinfo
model name      : Intel(R) Xeon(R) CPU E5-2630 v3 @ 2.40GHz
 2 "physical id"s (chips)
 32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores    : 8
  siblings     : 16
  physical 0   : cores 0 1 2 3 4 5 6 7
  physical 1   : cores 0 1 2 3 4 5 6 7
 cache size    : 20480 KB

From /proc/meminfo
MemTotal:      264413604 kB
HugePages_Total: 0
Hugepagesize:  2048 kB

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
Linux RD550 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013 x86_64
x86_64 x86_64 GNU/Linux

run-level 3 Jan 4 22:11

SPEC is set to: /usr/cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       ext4  217G   68G  138G  33% /

Additional information from dmidecode:
BIOS LENOVO PB2TS110 10/06/2014
Memory:
 16x 16 GB
 16x Hynix Semiconductor HMA42GR7MFR4N-TFTD 16 GB 1866 MHz 2 rank
 8x NO DIMM NO DIMM

(End of data from sysinfo program)
RD650 support 4 channels and 12 DIMMs per processor, total 8 channels and
24 DIMMs. 16 DIMM slots installed with 16 GB DIMM for this run.

```



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## General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"
```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1> /proc/sys/vm/drop_caches
```

runspec command invoked through numactl i.e.:

```
numactl --interleave=all runspec <etc>
```

## Base Compiler Invocation

C benchmarks:

```
icc -m32
```

C++ benchmarks:

```
icpc -m32
```

## Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
```

```
462.libquantum: -DSPEC_CPU_LINUX
```

```
483.xalancbmk: -DSPEC_CPU_LINUX
```

## Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

```
-opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

```
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```



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## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

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## Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>  
<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-HSW-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>  
<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-HSW-revA.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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