



SPEC® CFP2006 Result

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Fujitsu

SPECfp®2006 = 108

PRIMERGY CX2570 M1, Intel Xeon E5-2630 v3, 2.4 GHz

SPECfp_base2006 = 102

CPU2006 license: 19

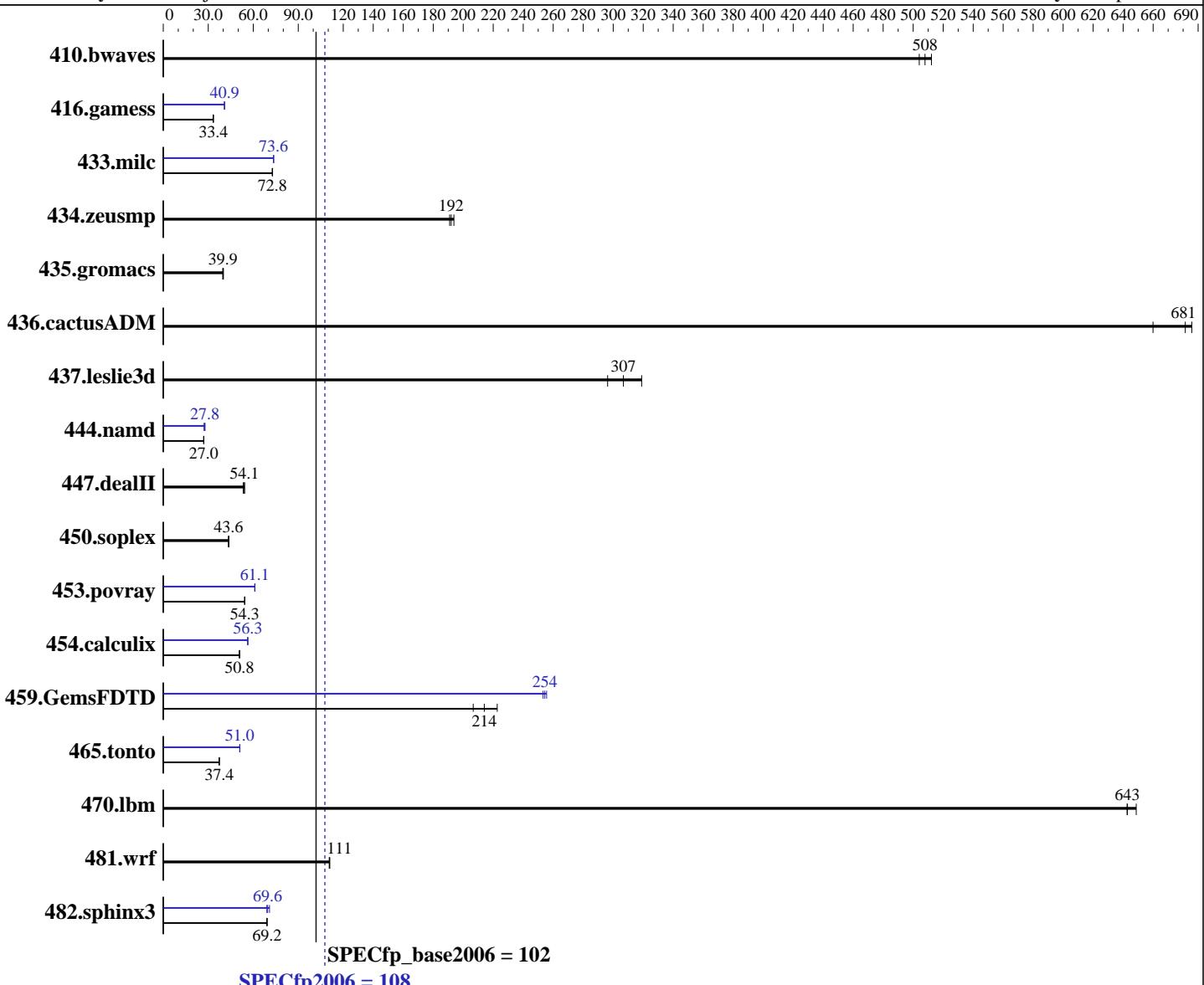
Test date: Nov-2014

Test sponsor: Fujitsu

Hardware Availability: Sep-2014

Tested by: Fujitsu

Software Availability: Sep-2014



Hardware

CPU Name: Intel Xeon E5-2630 v3
CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz
CPU MHz: 2400
FPU: Integrated
CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)
Compiler: Kernel 3.10.0-123.8.1.el7.x86_64
Auto Parallel: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;
File System: Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux
xfs

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L3 Cache: 20 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R, running at 1866 MHz)
 Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
410.bwaves	26.8	508	26.5	512	27.0	504	26.8	508	26.5	512	27.0	504
416.gamess	586	33.4	589	33.2	586	33.4	479	40.9	479	40.9	480	40.8
433.milc	126	72.8	126	72.9	126	72.7	125	73.6	125	73.5	125	73.6
434.zeusmp	47.7	191	47.4	192	47.0	194	47.7	191	47.4	192	47.0	194
435.gromacs	179	39.9	180	39.7	178	40.1	179	39.9	180	39.7	178	40.1
436.cactusADM	18.1	660	17.4	686	17.5	681	18.1	660	17.4	686	17.5	681
437.leslie3d	30.6	307	31.7	296	29.5	319	30.6	307	31.7	296	29.5	319
444.namd	297	27.0	297	27.0	297	27.0	289	27.8	295	27.2	289	27.8
447.dealII	215	53.3	211	54.2	211	54.1	215	53.3	211	54.2	211	54.1
450.soplex	191	43.7	191	43.6	192	43.5	191	43.7	191	43.6	192	43.5
453.povray	98.0	54.3	97.9	54.4	98.0	54.3	87.1	61.1	87.3	61.0	87.0	61.2
454.calculix	163	50.8	162	50.8	162	50.9	147	56.3	146	56.4	147	56.3
459.GemsFDTD	51.3	207	49.6	214	47.6	223	41.7	254	41.9	253	41.5	256
465.tonto	263	37.4	263	37.4	264	37.2	193	51.0	193	51.1	193	51.0
470.lbm	21.4	643	21.2	649	21.4	643	21.4	643	21.2	649	21.4	643
481.wrf	101	111	101	111	101	111	101	111	101	111	101	111
482.sphinx3	282	69.1	282	69.2	282	69.2	275	70.8	282	69.1	280	69.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:

Energy Performance = Performance

Utilization Profile = Unbalanced

QPI snoop mode: Home Snoop

COD Enable = Disabled, Early Snoop = Disabled

CPU C1E Support = Disabled



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General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"

LD_LIBRARY_PATH = "/home/SPECcpu2006/libs/32:/home/SPECcpu2006/libs/64:/home/SPECcpu2006/sh"

OMP_NUM_THREADS = "16"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

This result was measured on the PRIMERGY CX2550 M1. The PRIMERGY CX2550 M1 and the PRIMERGY CX2570 M1 are electronically equivalent.

For information about Fujitsu please visit: <http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64



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Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch  
-ansi-alias
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch  
-ansi-alias
```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32 -ansi-alias
```

```
470.lbm: basepeak = yes
```

```
482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel
```

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Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll14
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-inline-calloc -opt-malloc-options=3 -auto -unroll14

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.xml>

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For other inquiries, please contact webmaster@spec.org.

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