



SPEC® CFP2006 Result

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Lenovo Group Limited

Lenovo ThinkServer TD350 (Intel Xeon E5-2683 v3,
2.00 GHz)

SPECfp®2006 = 99.0

SPECfp_base2006 = 95.3

CPU2006 license: 9017

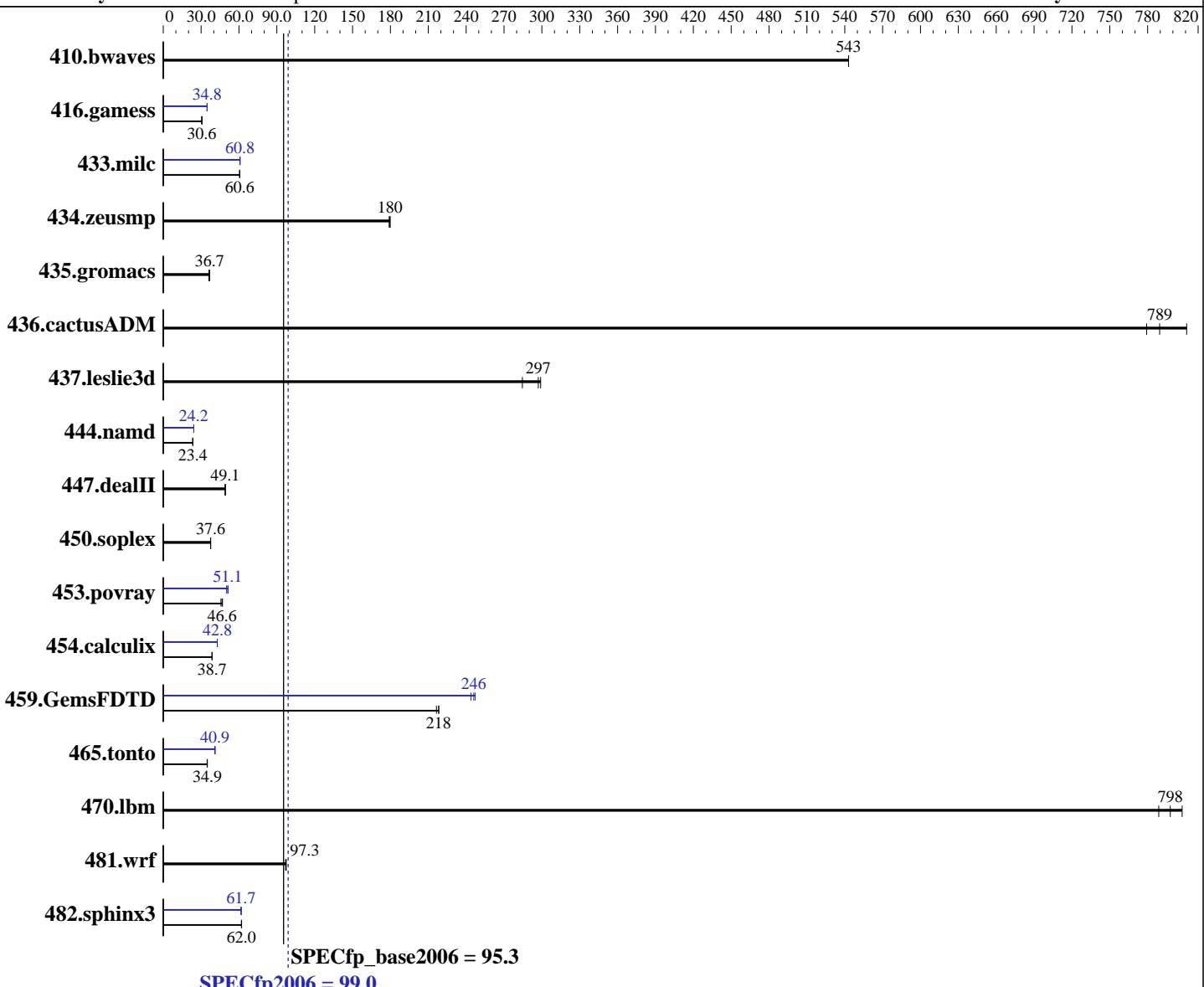
Test sponsor: Lenovo Group Limited

Tested by: Lenovo Group Limited

Test date: Nov-2014

Hardware Availability: Sep-2014

Software Availability: Jan-2014



Hardware

CPU Name: Intel Xeon E5-2683 v3
 CPU Characteristics: Intel Turbo Boost Technology up to 3.00 GHz
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 28 cores, 2 chips, 14 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
 Compiler: 2.6.32-431.el6.x86_64
 Auto Parallel: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
 File System: Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
 Software: ext4

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L3 Cache: 35 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)
 Disk Subsystem: 1 x 800 GB SATA SSD
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
410.bwaves	25.0	543	25.0	543	<u>25.0</u>	<u>543</u>	25.0	543	25.0	543	<u>25.0</u>	<u>543</u>
416.gamess	639	30.6	<u>640</u>	<u>30.6</u>	642	30.5	<u>564</u>	<u>34.7</u>	<u>563</u>	<u>34.8</u>	<u>563</u>	<u>34.8</u>
433.milc	152	60.5	151	60.6	<u>151</u>	<u>60.6</u>	<u>151</u>	<u>60.8</u>	151	60.9	151	60.7
434.zeusmp	<u>50.6</u>	<u>180</u>	50.6	180	50.8	179	<u>50.6</u>	<u>180</u>	50.6	180	50.8	179
435.gromacs	198	36.1	195	36.7	<u>195</u>	<u>36.7</u>	198	36.1	195	36.7	<u>195</u>	<u>36.7</u>
436.cactusADM	<u>15.1</u>	<u>789</u>	14.7	811	15.3	779	<u>15.1</u>	<u>789</u>	14.7	811	15.3	779
437.leslie3d	33.0	285	<u>31.6</u>	<u>297</u>	31.4	299	<u>33.0</u>	<u>285</u>	<u>31.6</u>	<u>297</u>	31.4	299
444.namd	343	23.4	342	23.4	<u>342</u>	<u>23.4</u>	331	24.2	331	24.2	<u>331</u>	<u>24.2</u>
447.dealII	233	49.1	233	49.1	<u>233</u>	<u>49.1</u>	233	49.1	233	49.1	<u>233</u>	<u>49.1</u>
450.soplex	<u>222</u>	<u>37.6</u>	222	37.6	222	37.5	<u>222</u>	<u>37.6</u>	222	37.6	222	37.5
453.povray	<u>114</u>	<u>46.6</u>	113	46.9	116	45.7	<u>104</u>	<u>51.3</u>	<u>104</u>	<u>51.1</u>	106	50.2
454.calculix	214	38.6	213	38.7	<u>213</u>	<u>38.7</u>	193	42.8	<u>193</u>	<u>42.8</u>	193	42.8
459.GemsFDTD	48.6	218	<u>48.6</u>	<u>218</u>	49.0	216	<u>43.5</u>	<u>244</u>	<u>43.1</u>	<u>246</u>	42.9	247
465.tonto	283	34.7	<u>282</u>	<u>34.9</u>	282	34.9	241	40.9	<u>241</u>	<u>40.9</u>	239	41.1
470.lbm	17.0	807	17.4	789	<u>17.2</u>	<u>798</u>	17.0	807	17.4	789	<u>17.2</u>	<u>798</u>
481.wrf	115	97.4	115	96.9	<u>115</u>	<u>97.3</u>	115	97.4	115	96.9	<u>115</u>	<u>97.3</u>
482.sphinx3	315	61.9	314	62.1	<u>315</u>	<u>62.0</u>	<u>316</u>	<u>61.7</u>	315	61.8	317	61.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:

Cluster On Die set to Disabled

Early Snoop set to Disabled

Performance Profile set to Custom

C1E Support set to Disabled

Core C3 set to Disabled

Core C6 set to Disabled

Thermal Profile set to Max Performance

Memory Power Savings set to Disabled

Sysinfo program /usr/cpu2006/config/sysinfo.rev6818

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Platform Notes (Continued)

\$Rev: 6818 \$ \$Date::: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191
running on TD350 Thu Nov 6 17:05:54 2014

This section contains SUT (System Under Test) info as seen by
some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-2683 v3 @ 2.00GHz
        2 "physical id"s (chips)
        56 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 14
    siblings : 28
    physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
    physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
cache size : 35840 KB
```

```
From /proc/meminfo
    MemTotal:       264410680 kB
    HugePages_Total:      0
    Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
    Red Hat Enterprise Linux Server release 6.5 (Santiago)
```

```
From /etc/*release* /etc/*version*
    redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
    system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
    system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
    Linux TD350 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013 x86_64
    x86_64 x86_64 GNU/Linux
```

```
run-level 3 Nov 6 17:05
```

```
SPEC is set to: /usr/cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2        ext4  730G  59G  634G  9%  /
```

Additional information from dmidecode:

BIOS LENOVO TB5TS110 10/06/2014

Memory:

16x 16 GB
2x Hynix Semiconductor HMA42GR7MFR4N-TF 16 GB 2133 MHz 2 rank
14x Hynix Semiconductor HMA42GR7MFR4N-TFTD 16 GB 2133 MHz 2 rank

(End of data from sysinfo program)

TD350 support 4 channels and 8 DIMMs per processor, total 8 channels and
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Platform Notes (Continued)

16 DIMMs. All 16 DIMM slots installed with 16 GB DIMM for this run.

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"

LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"

OMP_NUM_THREADS = "28"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:

 icc -m64

C++ benchmarks:

 icpc -m64

Fortran benchmarks:

 ifort -m64

Benchmarks using both Fortran and C:

 icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

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Peak Optimization Flags (Continued)

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32 -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

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Peak Optimization Flags (Continued)

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>
<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-TD350-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>
<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-TD350-revA.xml>

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