



# SPEC® CFP2006 Result

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## Lenovo Group Limited

Lenovo ThinkServer RD650 (Intel Xeon E5-2685 v3,  
2.60 GHz)

**SPECfp®\_rate2006 = 743**

**SPECfp\_rate\_base2006 = 727**

CPU2006 license: 9017

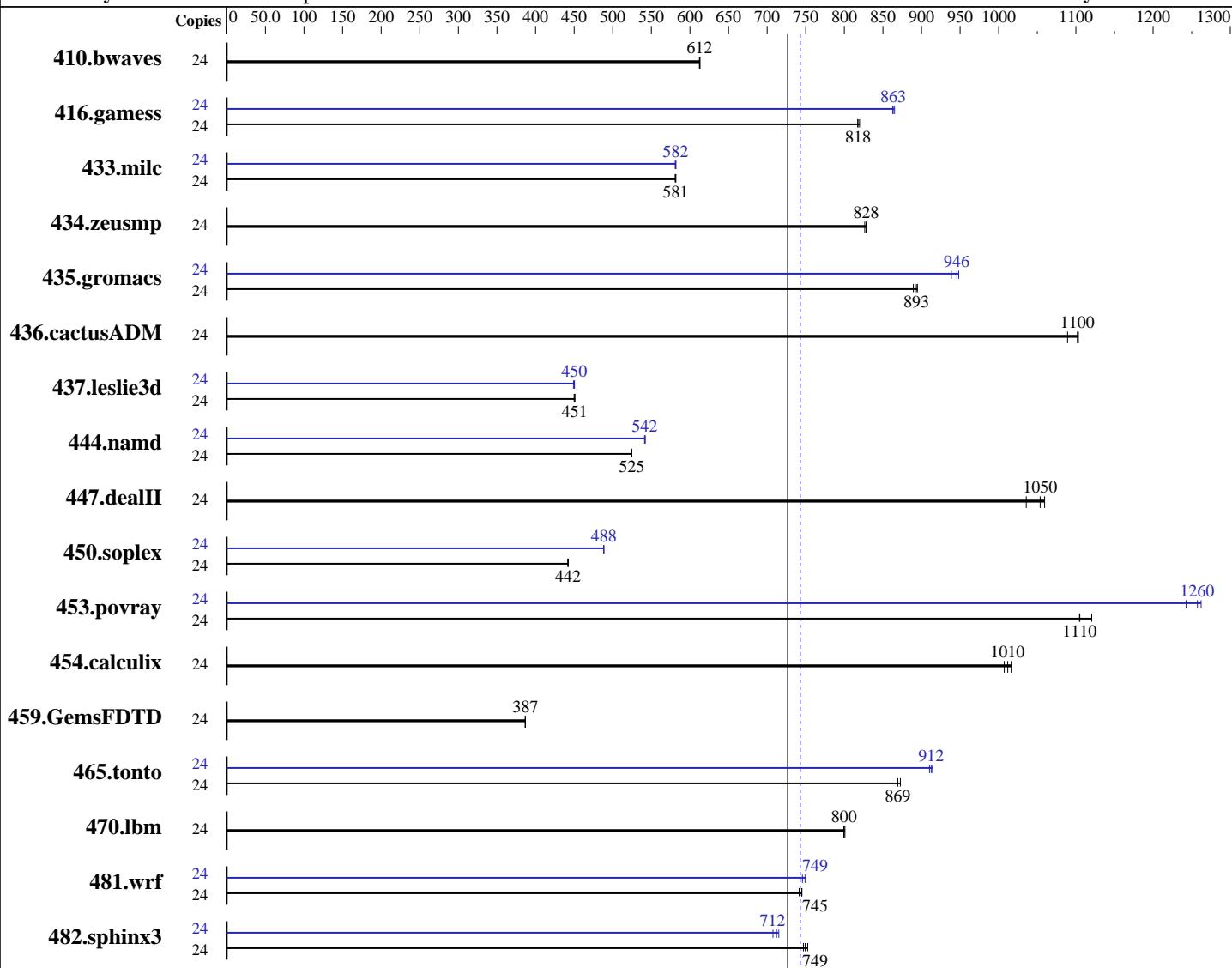
Test sponsor: Lenovo Group Limited

Tested by: Lenovo Group Limited

**Test date:** Nov-2014

**Hardware Availability:** Sep-2014

**Software Availability:** Jan-2014



**SPECfp\_rate\_base2006 = 727**

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### Hardware

CPU Name: Intel Xeon E5-2685 v3  
CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz  
CPU MHz: 2600  
FPU: Integrated  
CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip  
CPU(s) orderable: 1,2 chip  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core

### Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
Compiler: 2.6.32-431.el6.x86\_64  
C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;  
Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux  
Auto Parallel: No  
File System: ext4

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L3 Cache: 30 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)  
 Disk Subsystem: 1 x 240 GB SATA SSD  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	24	532	613	533	612	<b>533</b>	<b>612</b>	24	532	613	533	612	<b>533</b>	<b>612</b>
416.gamess	24	573	820	<b>575</b>	<b>818</b>	575	817	24	543	865	545	863	<b>544</b>	<b>863</b>
433.milc	24	379	581	379	582	<b>379</b>	<b>581</b>	24	<b>379</b>	<b>582</b>	379	582	379	581
434.zeusmp	24	264	826	<b>264</b>	<b>828</b>	264	829	24	264	826	<b>264</b>	<b>828</b>	264	829
435.gromacs	24	193	889	<b>192</b>	<b>893</b>	192	895	24	183	939	181	948	<b>181</b>	<b>946</b>
436.cactusADM	24	260	1100	263	1090	<b>260</b>	<b>1100</b>	24	260	1100	263	1090	<b>260</b>	<b>1100</b>
437.leslie3d	24	<b>501</b>	<b>451</b>	501	450	500	451	24	<b>501</b>	<b>450</b>	502	449	501	450
444.namd	24	<b>367</b>	<b>525</b>	367	525	367	524	24	356	541	355	542	<b>355</b>	<b>542</b>
447.dealII	24	259	1060	265	1040	<b>261</b>	<b>1050</b>	24	259	1060	265	1040	<b>261</b>	<b>1050</b>
450.soplex	24	<b>453</b>	<b>442</b>	453	442	453	442	24	410	489	410	488	<b>410</b>	<b>488</b>
453.povray	24	116	1100	<b>116</b>	<b>1110</b>	114	1120	24	101	1260	<b>102</b>	<b>1260</b>	103	1240
454.calculix	24	195	1020	197	1010	<b>196</b>	<b>1010</b>	24	195	1020	197	1010	<b>196</b>	<b>1010</b>
459.GemsFDTD	24	659	386	<b>658</b>	<b>387</b>	658	387	24	659	386	<b>658</b>	<b>387</b>	658	387
465.tonto	24	<b>272</b>	<b>869</b>	272	869	271	873	24	258	914	<b>259</b>	<b>912</b>	259	910
470.lbm	24	412	801	413	799	<b>412</b>	<b>800</b>	24	412	801	413	799	<b>412</b>	<b>800</b>
481.wrf	24	360	745	<b>360</b>	<b>745</b>	362	741	24	357	750	359	746	<b>358</b>	<b>749</b>
482.sphinx3	24	622	753	626	747	<b>624</b>	<b>749</b>	24	654	715	<b>657</b>	<b>712</b>	661	708

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
 Cluster On Die set to Enabled  
 Early Snoop set to Disabled

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## Platform Notes (Continued)

```
Performance Profile set to Custom
C1E Support set to Disabled
Core C3 set to Disabled
Core C6 set to Disabled
Thermal Profile set to High Fan Speed
Memory Power Savings set to Disabled
Sysinfo program /usr/cpu2006/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on RD650 Sat Nov 22 00:47:35 2014
```

This section contains SUT (System Under Test) info as seen by  
some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2685 v3 @ 2.60GHz
        2 "physical id"s (chips)
        24 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
        cpu cores : 12
        siblings : 12
        physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
        physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
cache size : 15360 KB
```

```
From /proc/meminfo
MemTotal:      264414004 kB
HugePages_Total:      0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux RD650 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013 x86_64
x86_64 x86_64 GNU/Linux
```

```
run-level 3 Nov 22 00:46
```

```
SPEC is set to: /usr/cpu2006
Filesystem      Type  Size  Used  Avail Use% Mounted on
/dev/sda2       ext4  217G   13G  193G   7%  /
```

Additional information from dmidecode:

BIOS LENOVO PB2TS110 10/06/2014

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## Platform Notes (Continued)

### Memory:

16x 16 GB  
16x Hynix Semiconductor HMA42GR7MFR4N-TFTD 16 GB 2133 MHz 2 rank  
8x NO DIMM NO DIMM

(End of data from sysinfo program)  
RD650 support 4 channels and 12 DIMMs per processor, total 8 channels and  
24 DIMMs. 16 DIMM slots installed with 16 GB DIMM for this run.

## General Notes

LD\_LIBRARY\_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB  
memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop\_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64

Continued on next page



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## Base Portability Flags (Continued)

```
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

## Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

## Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc -m64
```

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

```
icpc -m64
```

450.soplex: icpc -m32

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```



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## Peak Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
    435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
        444.namd: -DSPEC_CPU_LP64
        447.dealII: -DSPEC_CPU_LP64
        453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
        470.lbm: -DSPEC_CPU_LP64
            481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

```

## Peak Optimization Flags

C benchmarks:

```

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
    -O3(pass 2) -no-prec-div(pass 2)
    -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
    -auto-ilp32

```

470.lbm: basepeak = yes

```

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-mem-layout-trans=3
    -unroll2

```

C++ benchmarks:

```

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
    -O3(pass 2) -no-prec-div(pass 2)
    -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias
    -auto-ilp32

```

447.dealII: basepeak = yes

```

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
    -O3(pass 2) -no-prec-div(pass 2)
    -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
    -opt-malloc-options=3

```

```

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
    -O3(pass 2) -no-prec-div(pass 2)
    -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4
    -ansi-alias

```

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## Peak Optimization Flags (Continued)

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4  
-auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2)  
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>  
<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-RD650-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>  
<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-RD650-revA.xml>



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