



SPEC® CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Lenovo Group Limited

Lenovo ThinkServer TD350 (Intel Xeon E5-2650 v3, 2.30 GHz)

SPECint®2006 = 55.8

SPECint_base2006 = 53.1

CPU2006 license: 9017

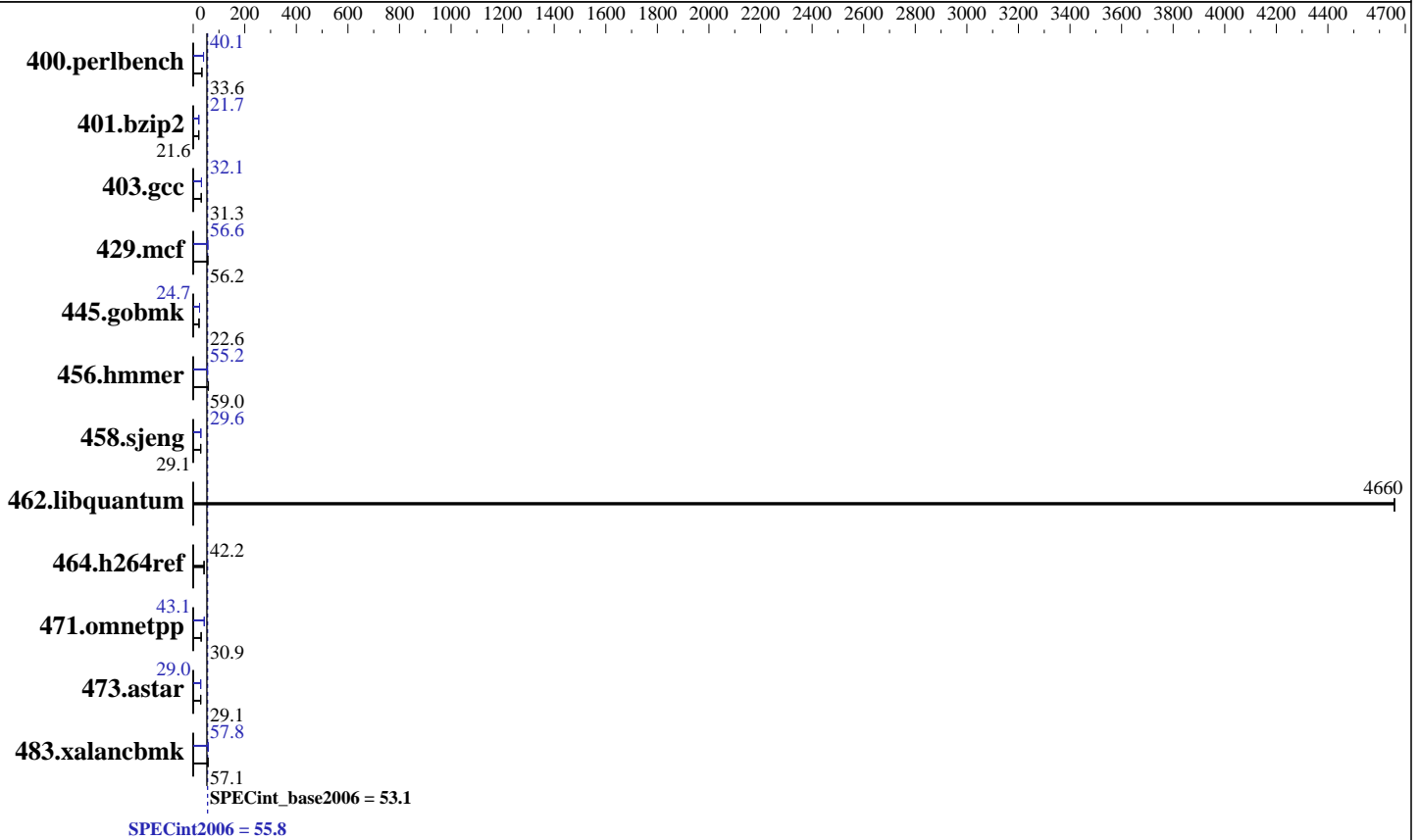
Test sponsor: Lenovo Group Limited

Tested by: Lenovo Group Limited

Test date: Nov-2014

Hardware Availability: Sep-2014

Software Availability: Jan-2014



Hardware

CPU Name: Intel Xeon E5-2650 v3
CPU Characteristics: Intel Turbo Boost Technology up to 3.00 GHz
CPU MHz: 2300
FPU: Integrated
CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip
CPU(s) orderable: 1,2 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 25 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)
Disk Subsystem: 1 x 800 GB SATA SSD
Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
 2.6.32-431.el6.x86_64
Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
Auto Parallel: Yes
File System: ext4
System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V10.0



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Lenovo Group Limited

Lenovo ThinkServer TD350 (Intel Xeon E5-2650 v3, 2.30 GHz)

SPECint2006 = **55.8**

SPECint_base2006 = **53.1**

CPU2006 license: 9017

Test sponsor: Lenovo Group Limited

Tested by: Lenovo Group Limited

Test date: Nov-2014

Hardware Availability: Sep-2014

Software Availability: Jan-2014

Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|----------------|-------------------|--------------------|-------------------|--------------------|--------------------|--------------------|-------------------|--------------------|-------------------|--------------------|--------------------|--------------------|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 291 | 33.6 | <u>291</u> | <u>33.6</u> | 290 | 33.6 | <u>244</u> | <u>40.1</u> | 244 | 40.0 | 243 | 40.2 |
| 401.bzip2 | <u>448</u> | <u>21.6</u> | 447 | 21.6 | 448 | 21.5 | <u>445</u> | <u>21.7</u> | 444 | 21.7 | 445 | 21.7 |
| 403.gcc | 256 | 31.4 | <u>257</u> | <u>31.3</u> | 258 | 31.2 | 251 | 32.1 | 250 | 32.2 | <u>251</u> | <u>32.1</u> |
| 429.mcf | <u>162</u> | <u>56.2</u> | 160 | 57.1 | 163 | 56.0 | 161 | 56.7 | <u>161</u> | <u>56.6</u> | 163 | 55.9 |
| 445.gobmk | 465 | 22.5 | 464 | 22.6 | <u>465</u> | <u>22.6</u> | <u>424</u> | <u>24.7</u> | 424 | 24.8 | 424 | 24.7 |
| 456.hammer | 157 | 59.3 | 159 | 58.6 | <u>158</u> | <u>59.0</u> | 167 | 55.9 | <u>169</u> | <u>55.2</u> | 169 | 55.2 |
| 458.sjeng | 415 | 29.1 | 415 | 29.1 | <u>415</u> | <u>29.1</u> | 409 | 29.6 | 409 | 29.6 | <u>409</u> | <u>29.6</u> |
| 462.libquantum | 4.45 | 4660 | 4.45 | 4660 | <u>4.45</u> | <u>4660</u> | 4.45 | 4660 | 4.45 | 4660 | <u>4.45</u> | <u>4660</u> |
| 464.h264ref | 523 | 42.3 | 525 | 42.1 | <u>525</u> | <u>42.2</u> | 523 | 42.3 | 525 | 42.1 | <u>525</u> | <u>42.2</u> |
| 471.omnetpp | 202 | 31.0 | 203 | 30.9 | <u>202</u> | <u>30.9</u> | <u>145</u> | <u>43.1</u> | 145 | 43.2 | 146 | 42.7 |
| 473.astar | 240 | 29.3 | 243 | 28.9 | <u>241</u> | <u>29.1</u> | 242 | 29.0 | 241 | 29.1 | <u>242</u> | <u>29.0</u> |
| 483.xalancbmk | 121 | 57.1 | <u>121</u> | <u>57.1</u> | 120 | 57.3 | <u>119</u> | <u>57.8</u> | 120 | 57.7 | 118 | 58.6 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Hyper-Threading set to Disabled
Performance Profile set to Custom
C1E Support set to Disabled
Core C3 set to Disabled
Core C6 set to Disabled
Thermal Profile set to Max Performance
Memory Power Savings set to Disabled
Sysinfo program /usr/cpu2006/config/sysinfo.rev6818
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191
running on TD350 Tue Nov 11 17:59:19 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2650 v3 @ 2.30GHz
2 "physical id"s (chips)

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Lenovo Group Limited

SPECint2006 = 55.8

Lenovo ThinkServer TD350 (Intel Xeon E5-2650 v3, 2.30 GHz)

SPECint_base2006 = 53.1

CPU2006 license: 9017

Test date: Nov-2014

Test sponsor: Lenovo Group Limited

Hardware Availability: Sep-2014

Tested by: Lenovo Group Limited

Software Availability: Jan-2014

Platform Notes (Continued)

20 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 10
siblings  : 10
physical 0: cores 0 1 2 3 4 8 9 10 11 12
physical 1: cores 0 1 2 3 4 8 9 10 11 12
cache size : 25600 KB
```

From /proc/meminfo

```
MemTotal:      264415172 kB
HugePages_Total: 0
Hugepagesize:  2048 kB
```

/usr/bin/lsb_release -d

```
Red Hat Enterprise Linux Server release 6.5 (Santiago)
```

From /etc/*release* /etc/*version*

```
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

uname -a:

```
Linux TD350 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013 x86_64
x86_64 x86_64 GNU/Linux
```

run-level 3 Nov 11 17:58

SPEC is set to: /usr/cpu2006

```
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2        ext4  730G  11G  682G   2% /
```

Additional information from dmidecode:

BIOS LENOVO TB5TS110 10/06/2014

Memory:

```
16x 16 GB
16x Samsung M393A2G40DB0-CPB 16 GB 2133 MHz 2 rank
```

(End of data from sysinfo program)

TD350 support 4 channels and 8 DIMMs per processor, total 8 channels and 16 DIMMs. All 16 DIMM slots installed with 16 GB DIMM for this run.

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,scatter"

LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"

OMP_NUM_THREADS = "20"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB

Continued on next page

Standard Performance Evaluation Corporation

info@spec.org

http://www.spec.org/



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Lenovo Group Limited

SPECint2006 = 55.8

Lenovo ThinkServer TD350 (Intel Xeon E5-2650 v3, 2.30 GHz)

SPECint_base2006 = 53.1

CPU2006 license: 9017

Test date: Nov-2014

Test sponsor: Lenovo Group Limited

Hardware Availability: Sep-2014

Tested by: Lenovo Group Limited

Software Availability: Jan-2014

General Notes (Continued)

memory using RedHat EL 6.4
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmcr: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Lenovo Group Limited

Lenovo ThinkServer TD350 (Intel Xeon E5-2650 v3, 2.30 GHz)

SPECint2006 = 55.8

SPECint_base2006 = 53.1

CPU2006 license: 9017

Test sponsor: Lenovo Group Limited

Tested by: Lenovo Group Limited

Test date: Nov-2014

Hardware Availability: Sep-2014

Software Availability: Jan-2014

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -ansi-alias
401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
-opt-prefetch -ansi-alias
403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32
429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel
-opt-prefetch -auto-p32
445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Lenovo Group Limited

SPECint2006 = 55.8

Lenovo ThinkServer TD350 (Intel Xeon E5-2650 v3, 2.30 GHz)

SPECint_base2006 = 53.1

CPU2006 license: 9017

Test date: Nov-2014

Test sponsor: Lenovo Group Limited

Hardware Availability: Sep-2014

Tested by: Lenovo Group Limited

Software Availability: Jan-2014

Peak Optimization Flags (Continued)

456.hmmr: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-ansi-alias

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-TD350-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-TD350-revA.xml>



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Lenovo Group Limited

Lenovo ThinkServer TD350 (Intel Xeon E5-2650 v3, 2.30 GHz)

SPECint2006 = 55.8

SPECint_base2006 = 53.1

CPU2006 license: 9017

Test sponsor: Lenovo Group Limited

Tested by: Lenovo Group Limited

Test date: Nov-2014

Hardware Availability: Sep-2014

Software Availability: Jan-2014

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Tue Jan 27 13:31:44 2015 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 27 January 2015.