



SPEC[®] CFP2006 Result

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Lenovo Group Limited

Lenovo ThinkServer TD350 (Intel Xeon E5-2670 v3, 2.30 GHz)

SPECfp[®]2006 = 105

SPECfp_base2006 = 101

CPU2006 license: 9017

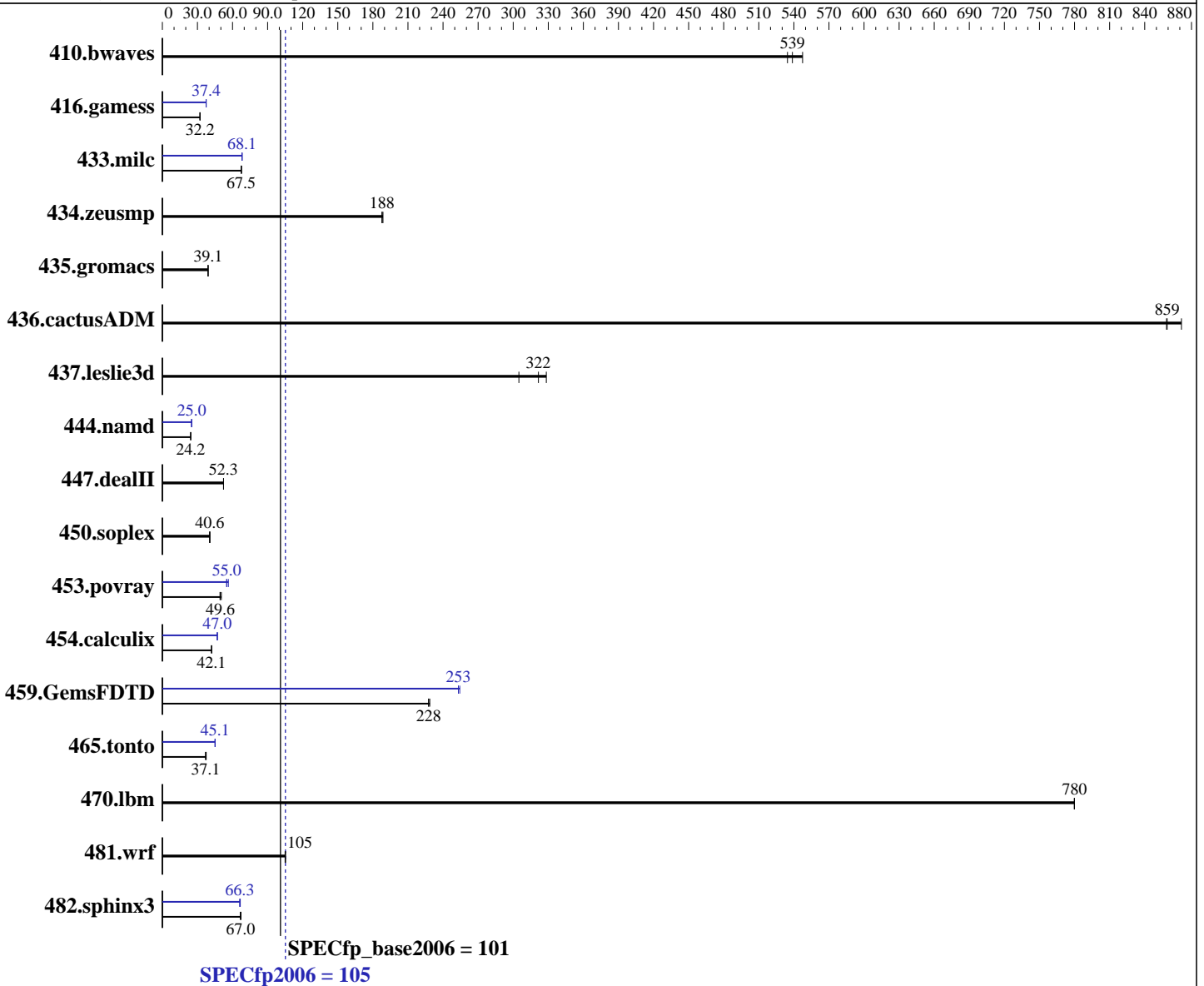
Test sponsor: Lenovo Group Limited

Tested by: Lenovo Group Limited

Test date: Oct-2014

Hardware Availability: Sep-2014

Software Availability: Jan-2014



Hardware

CPU Name: Intel Xeon E5-2670 v3
 CPU Characteristics: Intel Turbo Boost Technology up to 3.10 GHz
 CPU MHz: 2300
 FPU: Integrated
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
 2.6.32-431.el6.x86_64
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4

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L3 Cache: 30 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)
Disk Subsystem: 1 x 800 GB SATA SSD
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	25.4	535	<u>25.2</u>	<u>539</u>	24.8	547	25.4	535	<u>25.2</u>	<u>539</u>	24.8	547
416.gamess	610	32.1	607	32.2	<u>608</u>	<u>32.2</u>	523	37.5	523	37.4	<u>523</u>	<u>37.4</u>
433.milc	<u>136</u>	<u>67.5</u>	136	67.7	136	67.3	<u>135</u>	<u>68.1</u>	135	68.2	135	68.1
434.zeusmp	<u>48.4</u>	<u>188</u>	48.2	189	48.4	188	<u>48.4</u>	<u>188</u>	48.2	189	48.4	188
435.gromacs	<u>183</u>	<u>39.1</u>	183	39.1	183	39.1	<u>183</u>	<u>39.1</u>	183	39.1	183	39.1
436.cactusADM	13.9	859	<u>13.9</u>	<u>859</u>	13.7	871	13.9	859	<u>13.9</u>	<u>859</u>	13.7	871
437.leslie3d	<u>29.2</u>	<u>322</u>	30.8	305	28.6	328	<u>29.2</u>	<u>322</u>	30.8	305	28.6	328
444.namd	<u>331</u>	<u>24.2</u>	331	24.3	331	24.2	320	25.0	<u>320</u>	<u>25.0</u>	320	25.0
447.dealII	219	52.3	219	52.3	<u>219</u>	<u>52.3</u>	219	52.3	219	52.3	<u>219</u>	<u>52.3</u>
450.soplex	206	40.4	<u>206</u>	<u>40.6</u>	205	40.6	206	40.4	<u>206</u>	<u>40.6</u>	205	40.6
453.povray	105	50.5	108	49.3	<u>107</u>	<u>49.6</u>	94.3	56.4	96.7	55.0	<u>96.6</u>	<u>55.0</u>
454.calculix	196	42.1	196	42.1	<u>196</u>	<u>42.1</u>	<u>176</u>	<u>47.0</u>	176	47.0	176	47.0
459.GemsFDTD	46.4	229	<u>46.6</u>	<u>228</u>	46.6	228	<u>41.9</u>	<u>253</u>	41.7	254	41.9	253
465.tonto	267	36.9	262	37.5	<u>265</u>	<u>37.1</u>	219	45.0	<u>218</u>	<u>45.1</u>	218	45.2
470.lbm	17.6	780	17.6	780	<u>17.6</u>	<u>780</u>	17.6	780	17.6	780	<u>17.6</u>	<u>780</u>
481.wrf	106	105	<u>106</u>	<u>105</u>	106	105	106	105	<u>106</u>	<u>105</u>	106	105
482.sphinx3	292	66.7	290	67.2	<u>291</u>	<u>67.0</u>	295	66.1	293	66.6	<u>294</u>	<u>66.3</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Cluster On Die set to Disabled
Early Snoop set to Disabled
Performance Profile set to Custom
ClE Support set to Disabled
Core C3 set to Disabled
Core C6 set to Disabled
Thermal Profile set to Max Performance
Memory Power Savings set to Disabled
Sysinfo program /usr/cpu2006/config/sysinfo.rev6818

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Platform Notes (Continued)

\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191
running on TD350 Fri Oct 31 17:10:15 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2670 v3 @ 2.30GHz
 2 "physical id"s (chips)
 48 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 12
siblings : 24
physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
cache size : 30720 KB
```

```
From /proc/meminfo
MemTotal: 264411672 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux TD350 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013 x86_64
x86_64 x86_64 GNU/Linux
```

```
run-level 3 Oct 31 17:09
```

```
SPEC is set to: /usr/cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       ext4  730G   53G  641G   8% /
```

Additional information from dmidecode:

BIOS LENOVO TB5TS110 10/06/2014

Memory:

16x 16 GB

2x Hynix Semiconductor HMA42GR7MFR4N-TF 16 GB 2133 MHz 2 rank

14x Hynix Semiconductor HMA42GR7MFR4N-TFTD 16 GB 2133 MHz 2 rank

(End of data from sysinfo program)

TD350 support 4 channels and 8 DIMMs per processor, total 8 channels and
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Platform Notes (Continued)

16 DIMMs. All 16 DIMM slots installed with 16 GB DIMM for this run.

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"

LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"

OMP_NUM_THREADS = "24"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

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Peak Optimization Flags (Continued)

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32 -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

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Peak Optimization Flags (Continued)

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-TD350-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-TD350-revA.xml>

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