



SPEC[®] CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

HITACHI

SPECint[®]2006 = 64.3

BladeSymphony BS2500 (Intel Xeon E5-2699 v3)

SPECint_base2006 = 61.0

CPU2006 license: 35

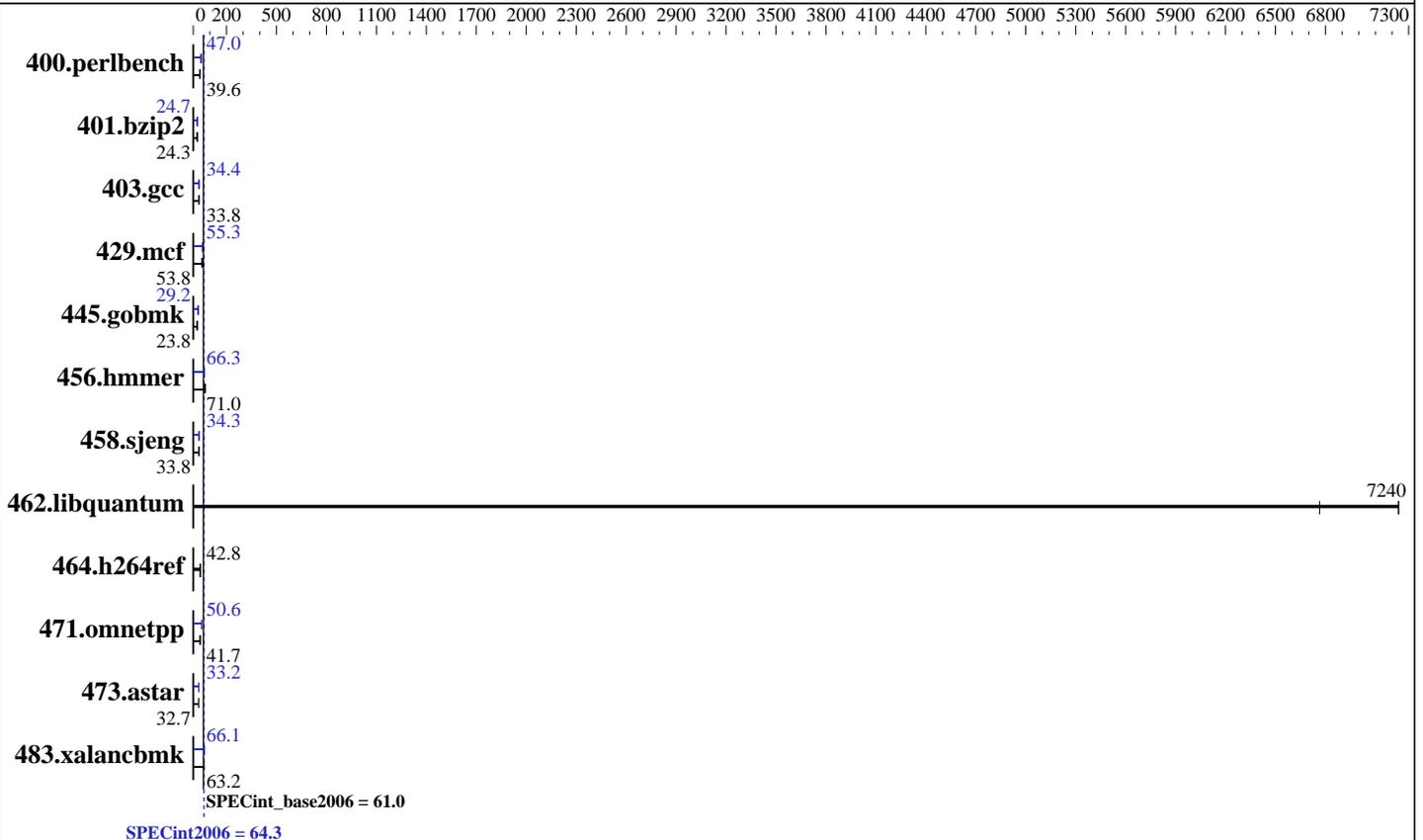
Test sponsor: HITACHI

Tested by: HITACHI

Test date: Jan-2015

Hardware Availability: Dec-2014

Software Availability: Nov-2013



Hardware

CPU Name: Intel Xeon E5-2699 v3
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz
 CPU MHz: 2300
 FPU: Integrated
 CPU(s) enabled: 36 cores, 2 chips, 18 cores/chip, 2 threads/core
 CPU(s) orderable: 1, 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 45 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)
 Disk Subsystem: 2 x 600 GB SAS, 10000 RPM, RAID1
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
 2.6.32-431.el6.x86_64
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.0



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

HITACHI

SPECint2006 = **64.3**

BladeSymphony BS2500 (Intel Xeon E5-2699 v3)

SPECint_base2006 = **61.0**

CPU2006 license: 35
Test sponsor: HITACHI
Tested by: HITACHI

Test date: Jan-2015
Hardware Availability: Dec-2014
Software Availability: Nov-2013

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	246	39.7	247	39.6	247	39.5	208	47.0	208	46.9	208	47.0
401.bzip2	397	24.3	398	24.3	398	24.2	392	24.6	391	24.7	391	24.7
403.gcc	239	33.7	238	33.8	237	33.9	234	34.4	235	34.3	234	34.4
429.mcf	172	53.0	168	54.2	170	53.8	164	55.5	166	55.0	165	55.3
445.gobmk	441	23.8	441	23.8	441	23.8	359	29.2	359	29.2	359	29.2
456.hmmer	131	71.0	131	71.2	133	70.3	142	65.9	141	66.3	141	66.3
458.sjeng	358	33.8	358	33.8	357	33.9	353	34.3	352	34.3	352	34.3
462.libquantum	2.86	7240	3.06	6760	2.86	7240	2.86	7240	3.06	6760	2.86	7240
464.h264ref	517	42.8	519	42.6	517	42.8	517	42.8	519	42.6	517	42.8
471.omnetpp	151	41.3	150	41.8	150	41.7	125	49.9	122	51.0	124	50.6
473.astar	214	32.8	214	32.7	215	32.6	213	32.9	212	33.2	212	33.2
483.xalancbmk	109	63.5	109	63.2	110	62.7	104	66.1	104	66.0	104	66.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:

Patrol Scrub = Disable
Per Core P-state = Disable

Sysinfo program /home/speccpu2006/cpu2006/config/sysinfo.rev6818
\$Rev: 6818 \$ \$Date: 2012-07-17 # \$ e86d102572650a6e4d596a3cee98f191
running on 520Hx36564 Thu Jan 8 22:18:05 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) CPU E5-2699 v3 @ 2.30GHz
2 "physical id"s (chips)
72 "processors"
```

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

HITACHI

SPECint2006 = 64.3

BladeSymphony BS2500 (Intel Xeon E5-2699 v3)

SPECint_base2006 = 61.0

CPU2006 license: 35

Test sponsor: HITACHI

Tested by: HITACHI

Test date: Jan-2015

Hardware Availability: Dec-2014

Software Availability: Nov-2013

Platform Notes (Continued)

```

cpu cores : 18
siblings  : 36
physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 1: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
cache size : 46080 KB

```

From /proc/meminfo

MemTotal: 263985944 kB

HugePages_Total: 0

Hugepagesize: 2048 kB

/usr/bin/lsb_release -d

Red Hat Enterprise Linux Server release 6.5 (Santiago)

From /etc/*release* /etc/*version*

redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)

system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)

system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:

Linux 520Hx36564 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013
x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jan 8 22:11

SPEC is set to: /home/speccpu2006/cpu2006

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/mapper/vg_520hx36564-lv_home	ext4	485G	5.5G	455G	2%	/home

Additional information from dmidecode:

BIOS HITACHI 08-20 01/06/2015

Memory:

8x NO DIMM Unknown

16x Samsung M393A2G40DB0-CPB 16 GB 2133 MHz 2 rank

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"

LD_LIBRARY_PATH = "/home/speccpu2006/cpu2006/libs/32:/home/speccpu2006/cpu2006/libs/64:/home/speccpu2006/cpu2006/sh"

OMP_NUM_THREADS = "36"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB

memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

BladeSymphony BS520H, Hitachi Compute Blade 520H and BladeSymphony BS2500 HC0A1 are electronically equivalent.

Continued on next page

Standard Performance Evaluation Corporation

info@spec.org

http://www.spec.org/



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

HITACHI

SPECint2006 = 64.3

BladeSymphony BS2500 (Intel Xeon E5-2699 v3)

SPECint_base2006 = 61.0

CPU2006 license: 35

Test sponsor: HITACHI

Tested by: HITACHI

Test date: Jan-2015

Hardware Availability: Dec-2014

Software Availability: Nov-2013

General Notes (Continued)

The results have been measured on a Hitachi Compute Blade 520H.

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32

-Wl,-z,muldefs -L/sh -lsmartheap64

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

HITACHI

SPECint2006 = 64.3

BladeSymphony BS2500 (Intel Xeon E5-2699 v3)

SPECint_base2006 = 61.0

CPU2006 license: 35

Test sponsor: HITACHI

Tested by: HITACHI

Test date: Jan-2015

Hardware Availability: Dec-2014

Software Availability: Nov-2013

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
 401.bzip2: -DSPEC_CPU_LP64
 403.gcc: -DSPEC_CPU_LP64
 429.mcf: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 464.h264ref: -DSPEC_CPU_LP64
 473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -opt-prefetch -ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
 -opt-prefetch -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
 -opt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel
 -opt-prefetch -auto-p32

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
 -ansi-alias

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

HITACHI

SPECint2006 = 64.3

BladeSymphony BS2500 (Intel Xeon E5-2699 v3)

SPECint_base2006 = 61.0

CPU2006 license: 35

Test sponsor: HITACHI

Tested by: HITACHI

Test date: Jan-2015

Hardware Availability: Dec-2014

Software Availability: Nov-2013

Peak Optimization Flags (Continued)

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-ansi-alias

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.html>

<http://www.spec.org/cpu2006/flags/PlatformHitachi-V1.2.20150127.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.xml>

<http://www.spec.org/cpu2006/flags/PlatformHitachi-V1.2.20150127.xml>



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

HITACHI

SPECint2006 = 64.3

BladeSymphony BS2500 (Intel Xeon E5-2699 v3)

SPECint_base2006 = 61.0

CPU2006 license: 35

Test sponsor: HITACHI

Tested by: HITACHI

Test date: Jan-2015

Hardware Availability: Dec-2014

Software Availability: Nov-2013

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Tue Jan 27 13:29:47 2015 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 27 January 2015.