



# SPEC® CINT2006 Result

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## HITACHI

**SPECint®\_rate2006 = 1360**

Compute Blade 520H (Intel Xeon E5-2699 v3)

**SPECint\_rate\_base2006 = 1320**

CPU2006 license: 35

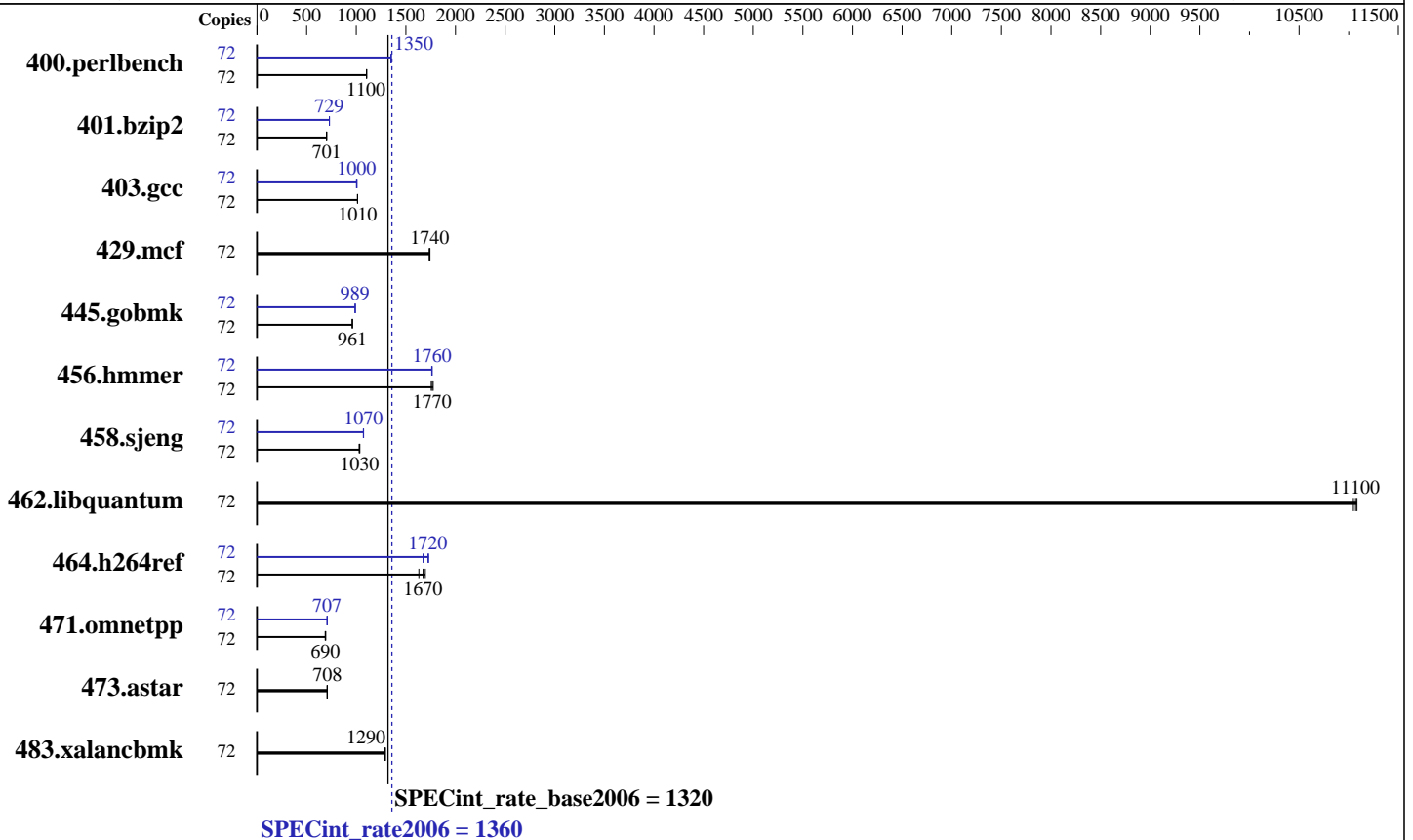
Test sponsor: HITACHI

Tested by: HITACHI

Test date: Jan-2015

Hardware Availability: Sep-2014

Software Availability: Nov-2013



### Hardware

CPU Name: Intel Xeon E5-2699 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz  
 CPU MHz: 2300  
 FPU: Integrated  
 CPU(s) enabled: 36 cores, 2 chips, 18 cores/chip, 2 threads/core  
 CPU(s) orderable: 1, 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 45 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)  
 Disk Subsystem: 2 x 600 GB SAS, 10000 RPM, RAID1  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 2.6.32-431.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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### Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	72	637	1100	638	1100	<b>637</b>	<b>1100</b>	72	521	1350	<b>520</b>	<b>1350</b>	520	1350
401.bzip2	72	990	702	<b>991</b>	<b>701</b>	993	700	72	952	730	953	729	<b>953</b>	<b>729</b>
403.gcc	72	<b>572</b>	<b>1010</b>	572	1010	572	1010	72	575	1010	<b>577</b>	<b>1000</b>	579	1000
429.mcf	72	<b>377</b>	<b>1740</b>	379	1730	377	1740	72	<b>377</b>	<b>1740</b>	379	1730	377	1740
445.gobmk	72	786	961	<b>786</b>	<b>961</b>	787	959	72	764	988	763	990	<b>764</b>	<b>989</b>
456.hammer	72	383	1760	378	1770	<b>380</b>	<b>1770</b>	72	381	1760	<b>381</b>	<b>1760</b>	382	1760
458.sjeng	72	841	1040	846	1030	<b>844</b>	<b>1030</b>	72	811	1070	813	1070	<b>813</b>	<b>1070</b>
462.libquantum	72	<b>135</b>	<b>11100</b>	135	11000	135	11100	72	<b>135</b>	<b>11100</b>	135	11000	135	11100
464.h264ref	72	977	1630	940	1700	<b>952</b>	<b>1670</b>	72	921	1730	952	1670	<b>925</b>	<b>1720</b>
471.omnetpp	72	650	693	655	687	<b>652</b>	<b>690</b>	72	637	706	636	707	<b>637</b>	<b>707</b>
473.astar	72	<b>714</b>	<b>708</b>	713	709	716	706	72	<b>714</b>	<b>708</b>	713	709	716	706
483.xalancbmk	72	385	1290	384	1290	<b>385</b>	<b>1290</b>	72	385	1290	384	1290	<b>385</b>	<b>1290</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Platform Notes

BIOS configuration:  
Patrol Scrub = Disable  
Per Core P-state = Disable  
COD Preferenc = Enable

Sysinfo program /home/speccpu2006/cpu2006/config/sysinfo.rev6818  
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191  
running on 520Hx36564 Tue Jan 6 16:50:24 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) CPU E5-2699 v3 @ 2.30GHz  
2 "physical id"s (chips)  
72 "processors"

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### Platform Notes (Continued)

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 18
siblings  : 36
physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 1: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
cache size : 23040 KB
```

From /proc/meminfo

```
MemTotal:      263985352 kB
HugePages_Total:    0
Hugepagesize:     2048 kB
```

/usr/bin/lsb\_release -d

```
Red Hat Enterprise Linux Server release 6.5 (Santiago)
```

From /etc/\*release\* /etc/\*version\*

```
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

uname -a:

```
Linux 520Hx36564 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013
x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Jan 6 16:46

SPEC is set to: /home/speccpu2006/cpu2006

```
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/mapper/vg_520hx36564-lv_home ext4      485G   61G  400G  14% /home
```

Additional information from dmidecode:

```
BIOS HITACHI 08-20 01/06/2015
Memory:
 8x NO DIMM Unknown
16x Samsung M393A2G40DB0-CPB 16 GB 2133 MHz 2 rank
```

(End of data from sysinfo program)

### General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/home/speccpu2006/cpu2006/libs/32:/home/speccpu2006/cpu2006/libs/64:/home/speccpu2006/cpu2006/sh"
```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1> /proc/sys/vm/drop_caches
```

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### General Notes (Continued)

runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>  
BladeSymphony BS520H, Hitachi Compute Blade 520H and BladeSymphony BS2500 HC0A1 are electronically equivalent.  
The results have been measured on a Hitachi Compute Blade 520H.

### Base Compiler Invocation

C benchmarks:  
icc -m32

C++ benchmarks:  
icpc -m32

### Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

### Base Optimization Flags

C benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3

C++ benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

### Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

### Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

400.perlbench: icc -m64

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**Hardware Availability:** Sep-2014

**Software Availability:** Nov-2013

## Peak Compiler Invocation (Continued)

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:

`icpc -m32`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`  
401.bzip2: `-DSPEC_CPU_LP64`  
456.hmmer: `-DSPEC_CPU_LP64`  
458.sjeng: `-DSPEC_CPU_LP64`  
462.libquantum: `-DSPEC_CPU_LINUX`  
483.xalancbmk: `-DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`  
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`  
`-auto-ilp32`

401.bzip2: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`  
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`  
`-opt-prefetch -auto-ilp32 -ansi-alias`

403.gcc: `-xCORE-AVX2 -ipo -O3 -no-prec-div`

429.mcf: `basepeak = yes`

445.gobmk: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)`  
`-ansi-alias -opt-mem-layout-trans=3`

456.hmmer: `-xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32`

458.sjeng: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`  
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`  
`-unroll4 -auto-ilp32`

462.libquantum: `basepeak = yes`

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## Peak Optimization Flags (Continued)

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.html>

<http://www.spec.org/cpu2006/flags/PlatformHitachi-V1.2.20150127.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.xml>

<http://www.spec.org/cpu2006/flags/PlatformHitachi-V1.2.20150127.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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