



SPEC[®] CFP2006 Result

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Huawei

SPECfp[®]2006 = 102

Huawei CH121 V3 (Intel Xeon E5-2650 v3)

SPECfp_base2006 = 97.7

CPU2006 license: 3175

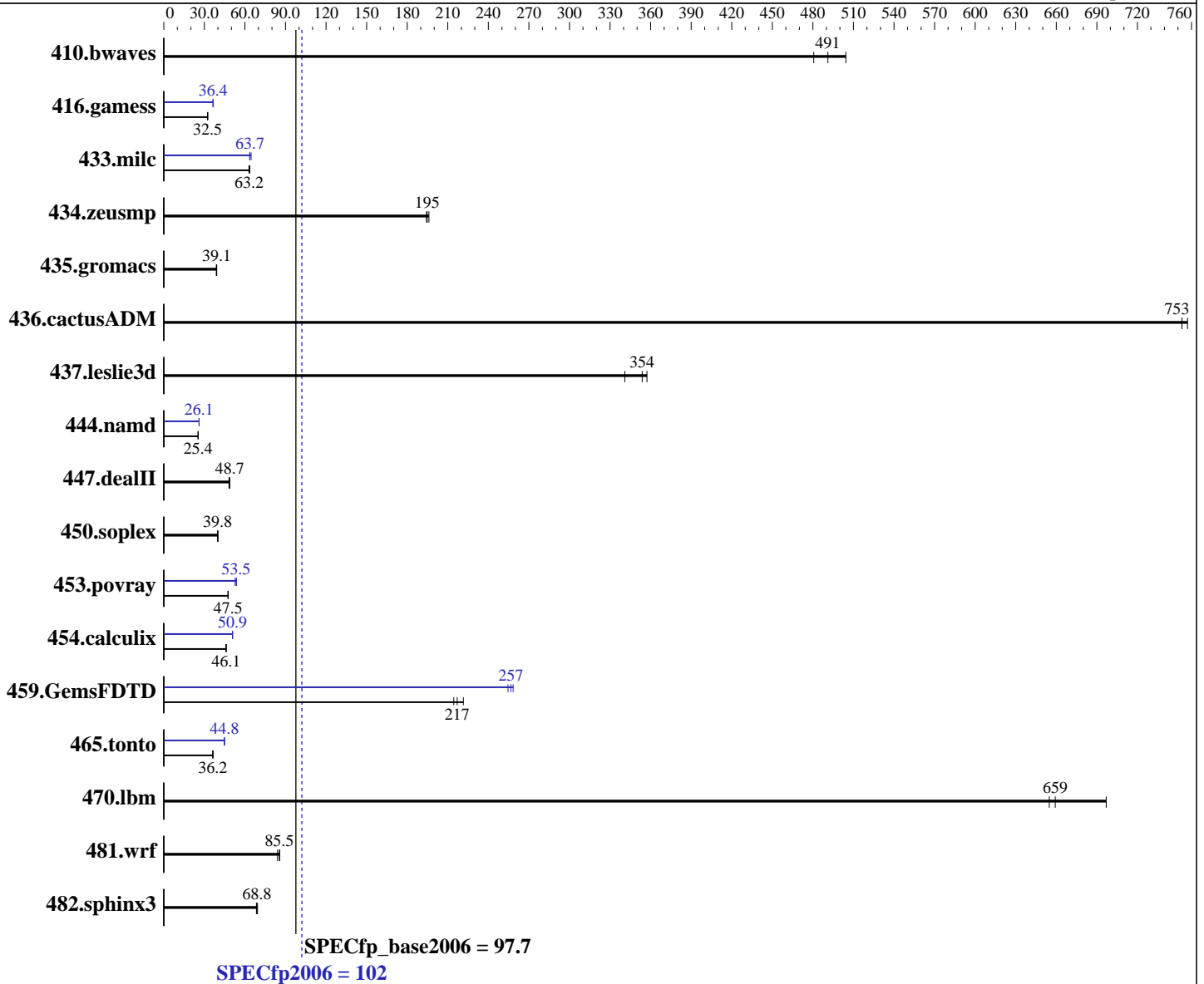
Test date: Dec-2014

Test sponsor: Huawei

Hardware Availability: Sep-2014

Tested by: Huawei

Software Availability: Sep-2014



Hardware

CPU Name: Intel Xeon E5-2650 v3
 CPU Characteristics: Intel Turbo Boost Technology up to 3.00 GHz
 CPU MHz: 2300
 FPU: Integrated
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)
 3.10.0-123.el7.x86_64
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;
 Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4

Continued on next page



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L3 Cache: 25 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)
 Disk Subsystem: 1 x 500 GB SATA, 7200 RPM
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	28.3	481	26.9	504	<u>27.7</u>	<u>491</u>	28.3	481	26.9	504	<u>27.7</u>	<u>491</u>
416.gamess	607	32.3	602	32.5	<u>603</u>	<u>32.5</u>	537	36.5	538	36.4	<u>537</u>	<u>36.4</u>
433.milc	<u>145</u>	<u>63.2</u>	144	63.6	146	63.0	<u>144</u>	<u>63.7</u>	145	63.5	142	64.6
434.zeusmp	<u>46.7</u>	<u>195</u>	46.4	196	46.9	194	<u>46.7</u>	<u>195</u>	46.4	196	46.9	194
435.gromacs	<u>183</u>	<u>39.1</u>	183	39.1	184	38.8	<u>183</u>	<u>39.1</u>	183	39.1	184	38.8
436.cactusADM	<u>15.9</u>	<u>753</u>	15.9	753	15.8	757	<u>15.9</u>	<u>753</u>	15.9	753	15.8	757
437.leslie3d	27.6	341	26.3	357	<u>26.6</u>	<u>354</u>	27.6	341	26.3	357	<u>26.6</u>	<u>354</u>
444.namd	<u>316</u>	<u>25.4</u>	316	25.4	316	25.4	<u>307</u>	<u>26.1</u>	307	26.1	307	26.1
447.dealII	235	48.7	237	48.2	<u>235</u>	<u>48.7</u>	235	48.7	237	48.2	<u>235</u>	<u>48.7</u>
450.soplex	<u>209</u>	<u>39.8</u>	208	40.1	210	39.8	<u>209</u>	<u>39.8</u>	208	40.1	210	39.8
453.povray	112	47.7	<u>112</u>	<u>47.5</u>	112	47.5	99.0	53.8	<u>99.4</u>	<u>53.5</u>	101	52.7
454.calculix	179	46.1	<u>179</u>	<u>46.1</u>	179	46.1	162	50.9	<u>162</u>	<u>50.9</u>	162	50.8
459.GemsFDTD	47.9	222	<u>48.9</u>	<u>217</u>	49.5	214	41.7	255	41.1	258	<u>41.3</u>	<u>257</u>
465.tonto	270	36.5	272	36.2	<u>272</u>	<u>36.2</u>	220	44.8	219	44.9	<u>220</u>	<u>44.8</u>
470.lbm	19.7	697	21.0	655	<u>20.8</u>	<u>659</u>	19.7	697	21.0	655	<u>20.8</u>	<u>659</u>
481.wrf	130	85.7	133	84.1	<u>131</u>	<u>85.5</u>	130	85.7	133	84.1	<u>131</u>	<u>85.5</u>
482.sphinx3	<u>283</u>	<u>68.8</u>	285	68.5	281	69.3	<u>283</u>	<u>68.8</u>	285	68.5	281	69.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
 Set Power Efficiency Mode to Custom
 Set Snoop Mode to HS
 Set Hyper-Threading to Disabled
 Baseboard Management Controller used to adjust the fan speed to 100%
 Sysinfo program /spec15/config/sysinfo.rev6914
 \$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
 running on localhost.localdomain Thu Jan 2 06:57:31 2014

This section contains SUT (System Under Test) info as seen by
Continued on next page



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Platform Notes (Continued)

some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name      : Intel(R) Xeon(R) CPU E5-2650 v3 @ 2.30GHz
 2 "physical id"s (chips)
 20 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores     : 10
  siblings      : 10
  physical 0    : cores 0 1 2 3 4 8 9 10 11 12
  physical 1    : cores 0 1 2 3 4 8 9 10 11 12
cache size     : 25600 KB

```

```

From /proc/meminfo
MemTotal:      263721024 kB
HugePages_Total: 0
Hugepagesize:  2048 kB

```

```

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

```

```

uname -a:
Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57
EDT 2014 x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Jan 1 22:59

```

SPEC is set to: /spec15
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel-root ext4  439G  19G  398G   5% /

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 1.19 10/10/2014

Continued on next page



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Platform Notes (Continued)

Memory:

8x NO DIMM NO DIMM 3 rank
8x Samsung M393A2G40DB0-CPB 16 GB 1 rank 2133 MHz
8x Samsung M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/spec15/libs/32:/spec15/libs/64:/spec15/sh"
OMP_NUM_THREADS = "20"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

The Huawei CH121 V3 and Huawei CH222 V3 models are electronically equivalent.

The results have been measured on a Huawei CH121 V3 model.

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64

Continued on next page

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Page 4



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Base Portability Flags (Continued)

```

444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

```

Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks:

```

icpc -m64

```

Fortran benchmarks:

```

ifort -m64

```

Benchmarks using both Fortran and C:

```

icc -m64 ifort -m64

```



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Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32 -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-inline-calloc -opt-malloc-options=3 -auto -unroll4

Continued on next page



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Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-HASWELL-V1.1.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-HASWELL-V1.1.xml>

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