SPEC® CFP2006 Result

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Cisco Systems

Cisco UCS B200 M4 (Intel Xeon E5-2660 v3 @ 2.60GHz)

$SPECfp®_rate2006 = $ 708

$SPECfp_rate_base2006 = $ 691
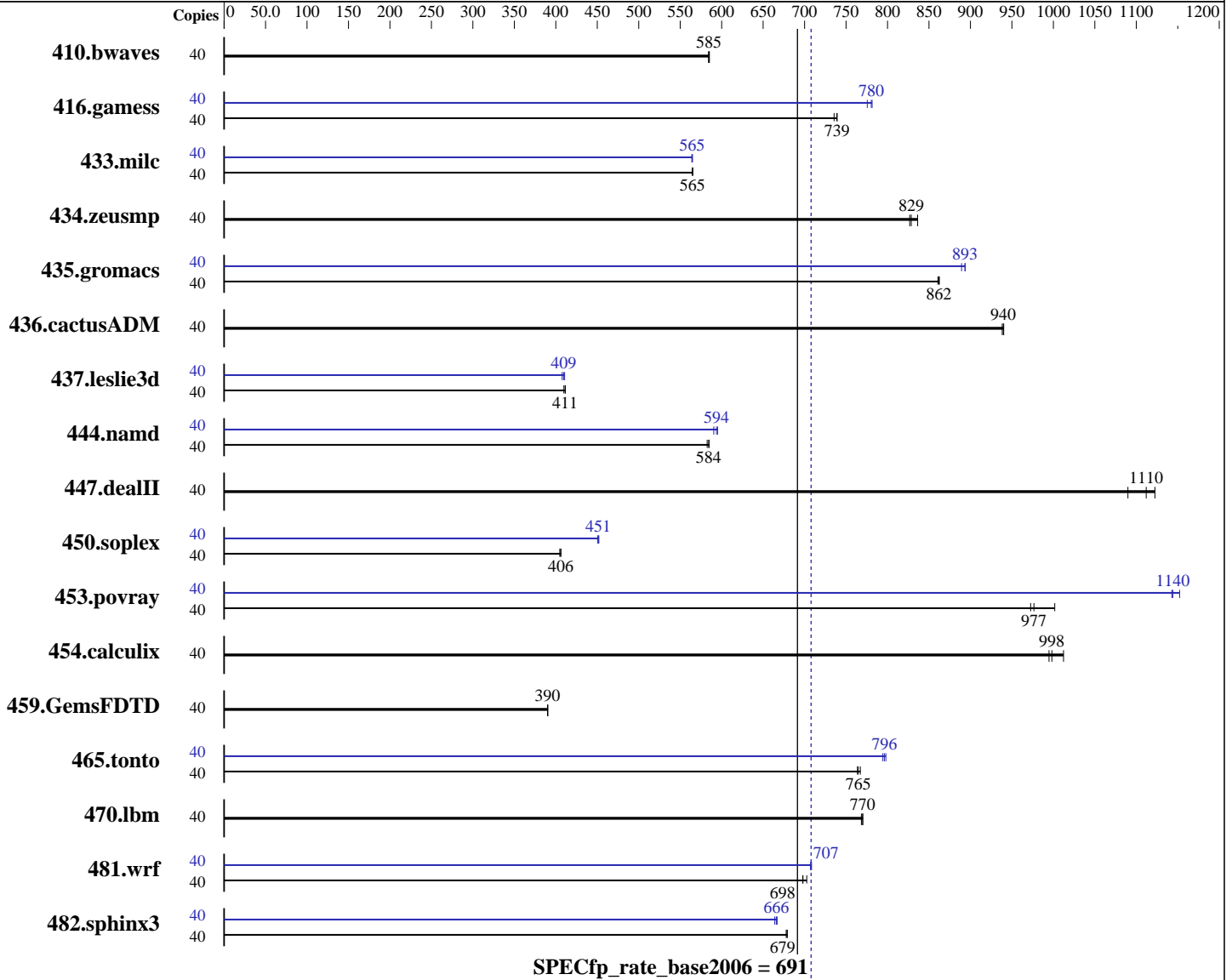
CPU2006 license: 9019   **Test date:** Dec-2014
Test sponsor: Cisco Systems   **Hardware Availability:** Sep-2014
Tested by: Cisco Systems   **Software Availability:** Nov-2013

Benchmark	Copies	Result
410.bwaves	40	585
416.gamess	40 / 40	780 / 739
433.milc	40 / 40	565 / 565
434.zeusmp	40	829
435.gromacs	40 / 40	893 / 862
436.cactusADM	40	940
437.leslie3d	40 / 40	409 / 411
444.namd	40 / 40	594 / 584
447.dealII	40	1110
450.soplex	40 / 40	451 / 406
453.povray	40 / 40	1140 / 977
454.calculix	40	998
459.GemsFDTD	40	390
465.tonto	40 / 40	796 / 765
470.lbm	40	770
481.wrf	40 / 40	707 / 698
482.sphinx3	40 / 40	666 / 679

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Hardware

CPU Name: Intel Xeon E5-2660 v3
CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz
CPU MHz: 2600
FPU: Integrated
CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
2.6.32-431.el6.x86_64
Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
Auto Parallel: No
File System: ext4

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L3 Cache:	25 MB I+D on chip per chip	System State:	Run level 3 (multi-user)
Other Cache:	None	Base Pointers:	32/64-bit
Memory:	256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)	Peak Pointers:	32/64-bit
Disk Subsystem:	1 x 600GB SAS, 10K RPM	Other Software:	None
Other Hardware:	None		

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	40	929	585	931	584	**930**	**585**	40	929	585	931	584	**930**	**585**
416.gamess	40	1065	736	1060	739	**1060**	**739**	40	**1004**	**780**	1010	776	1002	781
433.milc	40	650	565	**650**	**565**	650	565	40	650	565	651	564	**650**	**565**
434.zeusmp	40	**439**	**829**	435	836	440	827	40	**439**	**829**	435	836	440	827
435.gromacs	40	332	861	**331**	**862**	331	862	40	**320**	**893**	320	894	321	889
436.cactusADM	40	509	938	**509**	**940**	508	940	40	509	938	**509**	**940**	508	940
437.leslie3d	40	**914**	**411**	918	410	914	412	40	916	411	**918**	**409**	922	408
444.namd	40	**549**	**584**	549	585	551	583	40	539	595	543	590	**540**	**594**
447.dealII	40	408	1120	**412**	**1110**	420	1090	40	408	1120	**412**	**1110**	420	1090
450.soplex	40	821	406	**822**	**406**	824	405	40	741	450	738	452	**739**	**451**
453.povray	40	219	973	**218**	**977**	212	1000	40	186	1140	185	1150	**186**	**1140**
454.calculix	40	332	995	**331**	**998**	326	1010	40	332	995	**331**	**998**	326	1010
459.GemsFDTD	40	1087	390	**1088**	**390**	1088	390	40	1087	390	**1088**	**390**	1088	390
465.tonto	40	**515**	**765**	513	767	515	764	40	495	794	493	798	**494**	**796**
470.lbm	40	**714**	**770**	714	770	715	768	40	**714**	**770**	714	770	715	768
481.wrf	40	636	703	**640**	**698**	640	698	40	**632**	**707**	631	708	632	707
482.sphinx3	40	1150	678	**1148**	**679**	1148	679	40	**1171**	**666**	1169	667	1174	664

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

   The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

   Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

   CPU performance set to HPC
   Power Technology set to Custom
   Processor Power State C6 set to Disabled

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Platform Notes (Continued)

```
Energy Performance BIAS setting set to Performance
Memory RAS configuration set to Maximum Performance
QPI Snoop Mode set to Cluster-on-Die
 Sysinfo program /opt/cpu2006-1.2/config/sysinfo.rev6818
 $Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
 running on rhel65 Tue Dec 16 04:45:55 2014

 This section contains SUT (System Under Test) info as seen by
 some common utilities.  To remove or add to this section, see:
   http://www.spec.org/cpu2006/Docs/config.html#sysinfo

 From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-2660 v3 @ 2.60GHz
       2 "physical id"s (chips)
       40 "processors"
    cores, siblings (Caution: counting these is hw and system dependent.  The
    following excerpts from /proc/cpuinfo might not be reliable.  Use with
    caution.)
       cpu cores : 10
       siblings  : 20
       physical 0: cores 0 1 2 3 4 8 9 10 11 12
       physical 1: cores 0 1 2 3 4 8 9 10 11 12
    cache size : 12800 KB

 From /proc/meminfo
    MemTotal:       264257424 kB
    HugePages_Total:        0
    Hugepagesize:        2048 kB

 /usr/bin/lsb_release -d
    Red Hat Enterprise Linux Server release 6.5 (Santiago)

 From /etc/*release* /etc/*version*
    redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
    system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
    system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

 uname -a:
    Linux rhel65 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013 x86_64
    x86_64 x86_64 GNU/Linux

 run-level 3 Dec 15 18:00

 SPEC is set to: /opt/cpu2006-1.2
    Filesystem      Type  Size  Used Avail Use% Mounted on
    /dev/sda2       ext4  500G   69G  406G  15% /

 Additional information from dmidecode:
   BIOS Cisco Systems, Inc. B200M4.2.2.3c.0.101420141352 10/14/2014
   Memory:
   16x 0xCE00 M393A2G40DB0-CPB 16 GB 2133 MHz 2 rank
   8x NO DIMM NO DIMM
```

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Platform Notes (Continued)

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/opt/cpu2006-1.2/libs/32:/opt/cpu2006-1.2/libs/64:/opt/cpu2006-1.2/sh"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RedHat EL 6.4
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>   /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:
     icc   -m64

C++ benchmarks:
     icpc   -m64

Fortran benchmarks:
     ifort -m64

Benchmarks using both Fortran and C:
     icc   -m64 ifort -m64

Base Portability Flags

        410.bwaves: -DSPEC_CPU_LP64
        416.gamess: -DSPEC_CPU_LP64
          433.milc: -DSPEC_CPU_LP64
        434.zeusmp: -DSPEC_CPU_LP64
       435.gromacs: -DSPEC_CPU_LP64 -nofor_main
      436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
       437.leslie3d: -DSPEC_CPU_LP64
          444.namd: -DSPEC_CPU_LP64
         447.dealII: -DSPEC_CPU_LP64
        450.soplex: -DSPEC_CPU_LP64
        453.povray: -DSPEC_CPU_LP64
       454.calculix: -DSPEC_CPU_LP64 -nofor_main
      459.GemsFDTD: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

```
     465.tonto: -DSPEC_CPU_LP64
      470.lbm: -DSPEC_CPU_LP64
      481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
   482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc    -m64
```

```
   482.sphinx3: icc -m32
```

C++ benchmarks (except as noted below):
```
icpc   -m64
```

```
    450.soplex: icpc -m32
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc    -m64 ifort -m64
```

Peak Portability Flags

```
   410.bwaves: -DSPEC_CPU_LP64
```

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Peak Portability Flags (Continued)

```
       416.gamess: -DSPEC_CPU_LP64
         433.milc: -DSPEC_CPU_LP64
       434.zeusmp: -DSPEC_CPU_LP64
      435.gromacs: -DSPEC_CPU_LP64 -nofor_main
     436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
      437.leslie3d: -DSPEC_CPU_LP64
         444.namd: -DSPEC_CPU_LP64
        447.dealII: -DSPEC_CPU_LP64
        453.povray: -DSPEC_CPU_LP64
      454.calculix: -DSPEC_CPU_LP64 -nofor_main
      459.GemsFDTD: -DSPEC_CPU_LP64
         465.tonto: -DSPEC_CPU_LP64
          470.lbm: -DSPEC_CPU_LP64
          481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
        433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2)
                  -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
                  -auto-ilp32

         470.lbm: basepeak = yes

      482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-mem-layout-trans=3
                  -unroll2
```

C++ benchmarks:

```
        444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2)
                  -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias
                  -auto-ilp32

        447.dealII: basepeak = yes

       450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2)
                  -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
                  -opt-malloc-options=3

       453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2)
                  -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4
                  -ansi-alias
```

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Peak Optimization Flags (Continued)

Fortran benchmarks:

   410.bwaves: basepeak = yes

   416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
          -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
          -inline-level=0 -scalar-rep-

   434.zeusmp: basepeak = yes

   437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

 459.GemsFDTD: basepeak = yes

    465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
          -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
          -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

   435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
          -O3(pass 2) -no-prec-div(pass 2)
          -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
          -opt-prefetch -auto-ilp32

 436.cactusADM: basepeak = yes

  454.calculix: basepeak = yes

    481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revC.html

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You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revC.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Wed Jan 14 10:28:54 2015 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 13 January 2015.