SPEC® CFP2006 Result

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Huawei

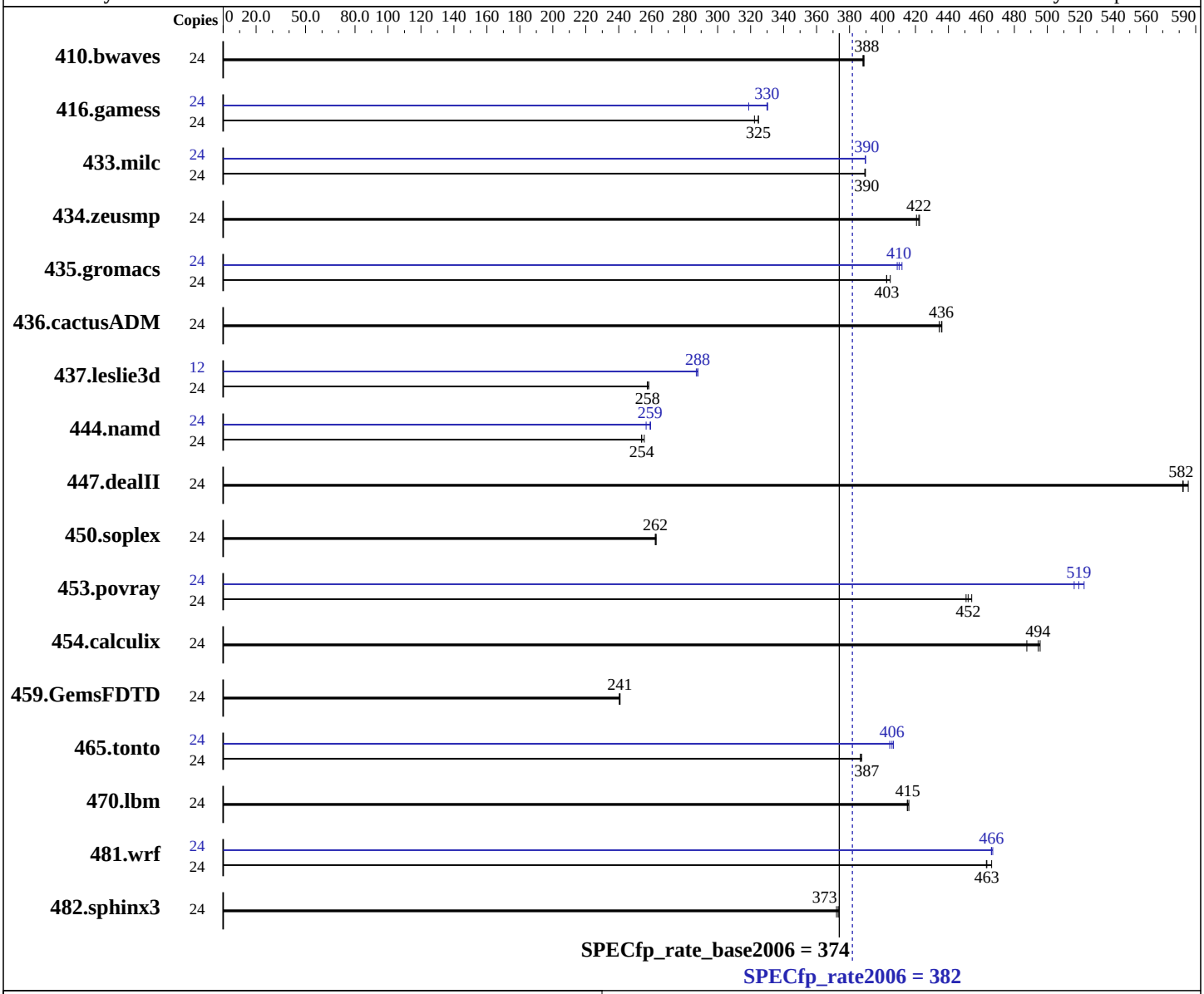
Huawei CH222 (Intel Xeon E5-2620 v2)

SPECfp®_rate2006 = 382

SPECfp_rate_base2006 = 374

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: Aug-2014
Hardware Availability: Sep-2013
Software Availability: Sep-2013



Hardware

CPU Name:	Intel Xeon E5-2620 v2
CPU Characteristics:	Intel Turbo Boost Technology up to 2.60 GHz
CPU MHz:	2100
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	Red Hat Enterprise Linux Server release 6.5 (Santiago) 2.6.32-431.el6.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux; Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
Auto Parallel:	No
File System:	ext4

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L3 Cache: 15 MB I+D on chip per chip
Other Cache: None
Memory: 128 GB (16 x 8 GB 2Rx8 PC3-12800R-11, ECC)
Disk Subsystem: 1 x 300 GB SAS, 10K RPM
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	24	839	389	**840**	**388**	840	388	24	839	389	**840**	**388**	840	388
416.gamess	24	1458	322	1447	325	**1447**	**325**	24	**1424**	**330**	1474	319	1422	330
433.milc	24	566	389	**566**	**390**	566	390	24	565	390	**565**	**390**	566	390
434.zeusmp	24	**517**	**422**	517	422	519	421	24	**517**	**422**	517	422	519	421
435.gromacs	24	426	402	423	405	**426**	**403**	24	419	409	416	412	**418**	**410**
436.cactusADM	24	**658**	**436**	660	434	658	436	24	**658**	**436**	660	434	658	436
437.leslie3d	24	874	258	877	257	**876**	**258**	12	**392**	**288**	393	287	392	288
444.namd	24	758	254	**758**	**254**	753	255	24	750	257	742	259	**743**	**259**
447.dealII	24	469	585	**471**	**582**	472	582	24	469	585	**471**	**582**	472	582
450.soplex	24	**763**	**262**	762	263	763	262	24	**763**	**262**	762	263	763	262
453.povray	24	283	451	281	454	**283**	**452**	24	247	516	244	522	**246**	**519**
454.calculix	24	**400**	**494**	406	488	399	496	24	**400**	**494**	406	488	399	496
459.GemsFDTD	24	1058	241	**1058**	**241**	1060	240	24	1058	241	**1058**	**241**	1060	240
465.tonto	24	611	387	**611**	**387**	610	387	24	**582**	**406**	584	404	581	407
470.lbm	24	795	415	**794**	**415**	793	416	24	795	415	**794**	**415**	793	416
481.wrf	24	**579**	**463**	575	466	579	463	24	574	467	575	466	**575**	**466**
482.sphinx3	24	1252	374	**1254**	**373**	1257	372	24	1252	374	**1254**	**373**	1257	372

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS configuration:
Set Power Efficiency Mode to Custom
Baseboard Management Controller used to adjust the fan speed to 100%
```

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Platform Notes (Continued)

```
Sysinfo program /spec/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on localhost Wed Aug 20 01:19:15 2014

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2620 v2 @ 2.10GHz
      2 "physical id"s (chips)
      24 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 6
      siblings  : 12
      physical 0: cores 0 1 2 3 4 5
      physical 1: cores 0 1 2 3 4 5
   cache size : 15360 KB

From /proc/meminfo
   MemTotal:       132103752 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
   Red Hat Enterprise Linux Server release 6.5 (Santiago)

From /etc/*release* /etc/*version*
   redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
   system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
   Linux localhost 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013
   x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Aug 13 19:36

SPEC is set to: /spec
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/sda1      ext4  193G   62G  121G  34% /

Additional information from dmidecode:
  BIOS Insyde Corp. RMIBV629 05/12/2014
  Memory:
   16x Micron 18JSF1G72PDZ-1G6E 8 GB 1600 MHz 2 rank
   8x NO DIMM NO DIMM

(End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RedHat EL 6.4
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>       /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
    435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
     444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
      465.tonto: -DSPEC_CPU_LP64
      470.lbm: -DSPEC_CPU_LP64
      481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
  482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
`-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias -opt-mem-layout-trans=3`

C++ benchmarks:
`-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias -opt-mem-layout-trans=3`

Fortran benchmarks:
`-xAVX -ipo -O3 -no-prec-div -opt-prefetch`

Benchmarks using both Fortran and C:
`-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias -opt-mem-layout-trans=3`

Peak Compiler Invocation

C benchmarks:
`icc -m64`

C++ benchmarks:
`icpc -m64`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: `-xAVX`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-opt-mem-layout-trans=3`(pass 2) `-prof-use`(pass 2) `-auto-ilp32`

470.lbm: `basepeak = yes`

Continued on next page

Peak Optimization Flags (Continued)

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xAVX -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml

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