SPEC® CFP2006 Result

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Huawei

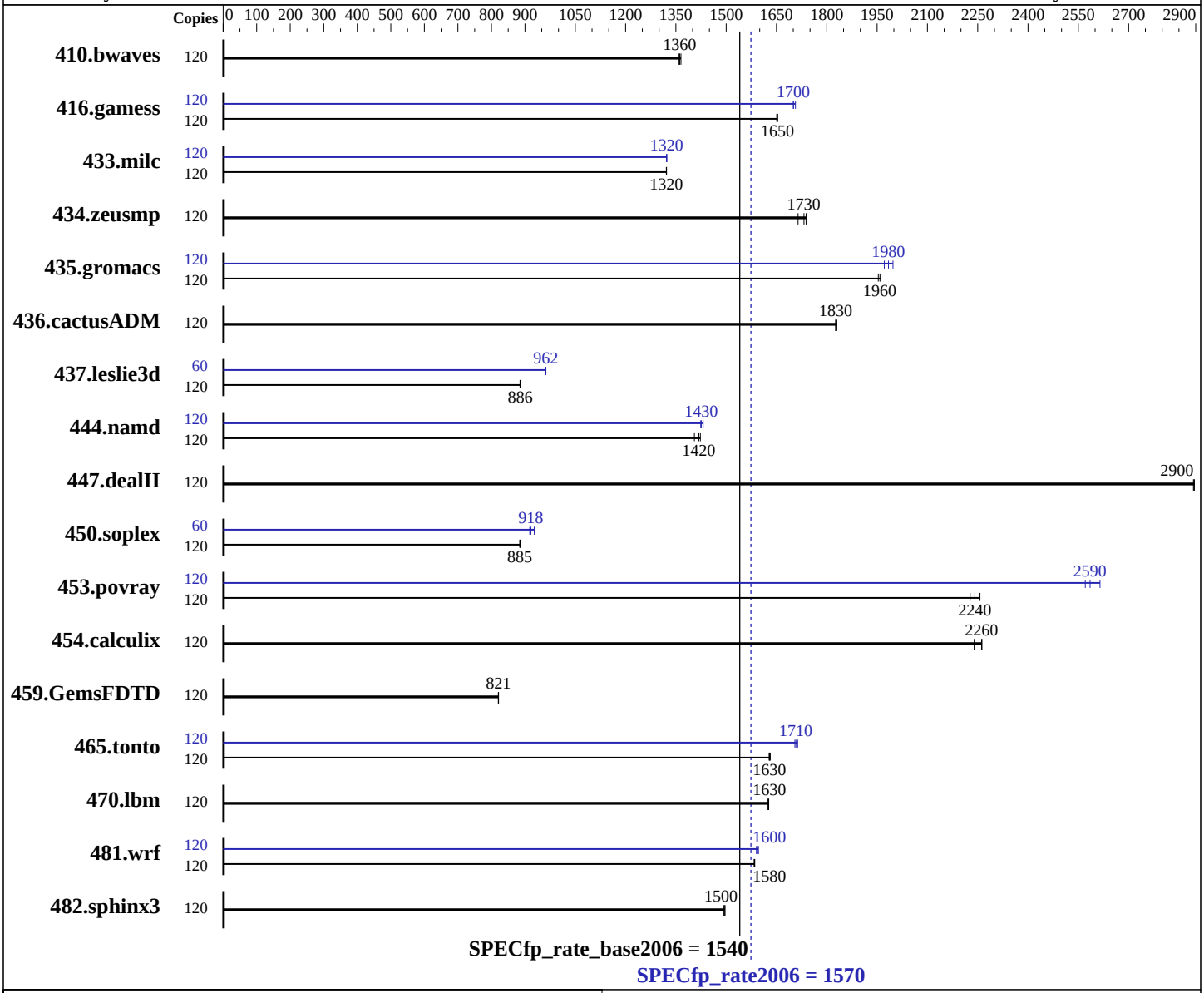
Huawei RH5885H v3 (Intel Xeon E7-4870 v2)

SPECfp®_rate2006 = 1570

SPECfp_rate_base2006 = 1540

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: Apr-2014
Hardware Availability: Feb-2014
Software Availability: Nov-2013



Hardware

CPU Name:	Intel Xeon E7-4870 v2
CPU Characteristics:	Intel Turbo Boost Technology up to 2.90 GHz
CPU MHz:	2300
FPU:	Integrated
CPU(s) enabled:	60 cores, 4 chips, 15 cores/chip, 2 threads/core
CPU(s) orderable:	2,4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	Red Hat Enterprise Linux Server release 6.5 (Santiago) 2.6.32-431.el6.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux; Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
Auto Parallel:	No
File System:	ext4

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L3 Cache: 30 MB I+D on chip per chip
Other Cache: None
Memory: 512 GB (64 x 8 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem: 2 x 300 GB SAS, 10K RPM
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	120	1200	1360	1195	1370	**1198**	**1360**	120	1200	1360	1195	1370	**1198**	**1360**
416.gamess	120	**1421**	**1650**	1421	1650	1423	1650	120	1382	1700	**1382**	**1700**	1377	1710
433.milc	120	834	1320	**833**	**1320**	833	1320	120	**833**	**1320**	833	1320	833	1320
434.zeusmp	120	628	1740	**631**	**1730**	637	1710	120	628	1740	**631**	**1730**	637	1710
435.gromacs	120	437	1960	439	1950	**437**	**1960**	120	**432**	**1980**	429	2000	435	1970
436.cactusADM	120	784	1830	**785**	**1830**	785	1830	120	784	1830	**785**	**1830**	785	1830
437.leslie3d	120	**1273**	**886**	1274	885	1273	886	60	**586**	**962**	586	962	586	962
444.namd	120	685	1410	676	1420	**679**	**1420**	120	672	1430	**675**	**1430**	676	1420
447.dealII	120	474	2890	474	2900	**474**	**2900**	120	474	2890	474	2900	**474**	**2900**
450.soplex	120	1130	885	**1131**	**885**	1131	885	60	**545**	**918**	547	914	540	927
453.povray	120	283	2260	287	2230	**285**	**2240**	120	248	2570	**247**	**2590**	244	2610
454.calculix	120	442	2240	**438**	**2260**	438	2260	120	442	2240	**438**	**2260**	438	2260
459.GemsFDTD	120	1550	821	1551	821	**1551**	**821**	120	1550	821	1551	821	**1551**	**821**
465.tonto	120	**725**	**1630**	725	1630	724	1630	120	**691**	**1710**	689	1710	693	1700
470.lbm	120	1013	1630	1015	1620	**1014**	**1630**	120	1013	1630	1015	1620	**1014**	**1630**
481.wrf	120	846	1590	**846**	**1580**	847	1580	120	840	1600	**840**	**1600**	843	1590
482.sphinx3	120	1563	1500	**1563**	**1500**	1567	1490	120	1563	1500	**1563**	**1500**	1567	1490

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS configuration:
Set Power Efficiency Mode to Performance
Set Lock_step to disabled
```

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Platform Notes (Continued)

```
Baseboard Management Controller used to adjust the fan speed to 100%
Sysinfo program /spec/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on speccpu Wed Apr  2 17:19:37 2014

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E7-4870 v2 @ 2.30GHz
      4 "physical id"s (chips)
      120 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 15
      siblings  : 30
      physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14
      physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14
      physical 2: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14
      physical 3: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14
   cache size : 30720 KB

From /proc/meminfo
   MemTotal:       529098320 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
   Red Hat Enterprise Linux Server release 6.5 (Santiago)

From /etc/*release* /etc/*version*
   redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
   system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
   Linux speccpu 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013
   x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Apr 2 17:18

SPEC is set to: /spec
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/sda2      ext4  259G   13G  234G   6% /

Additional information from dmidecode:
  BIOS American Megatrends Inc. BLISV099 02/09/2014
  Memory:
   64x   8 GB
   32x NO DIMM NO DIMM
```

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Platform Notes (Continued)

```
      27x Samsung M393B1K70CH0-CH9 8 GB 1333 MHz 2 rank
      37x Samsung M393B1K70DH0-CH9 8 GB 1333 MHz 2 rank

   (End of data from sysinfo program)
   Regarding the sysinfo display about the memory installed, the correct amount of
   memory is 512 GB and the dmidecode description should have two lines reading as:
      27x Samsung M393B1K70CH0-CH9 8 GB 1333 MHz 2 rank
      37x Samsung M393B1K70CH0-CH9 8 GB 1333 MHz 2 rank
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RedHat EL 6.4
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>       /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
    435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
```

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Base Portability Flags (Continued)

444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

Fortran benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks (except as noted below):
icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-malloc-options=3

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xAVX -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml

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Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 22:17:40 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 6 May 2014.