SPEC® CFP2006 Result
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Huawei

Huawei RH5885H v3 (Intel Xeon E7-4860 v2)

$SPECfp^{®}_rate2006 = $ 1460

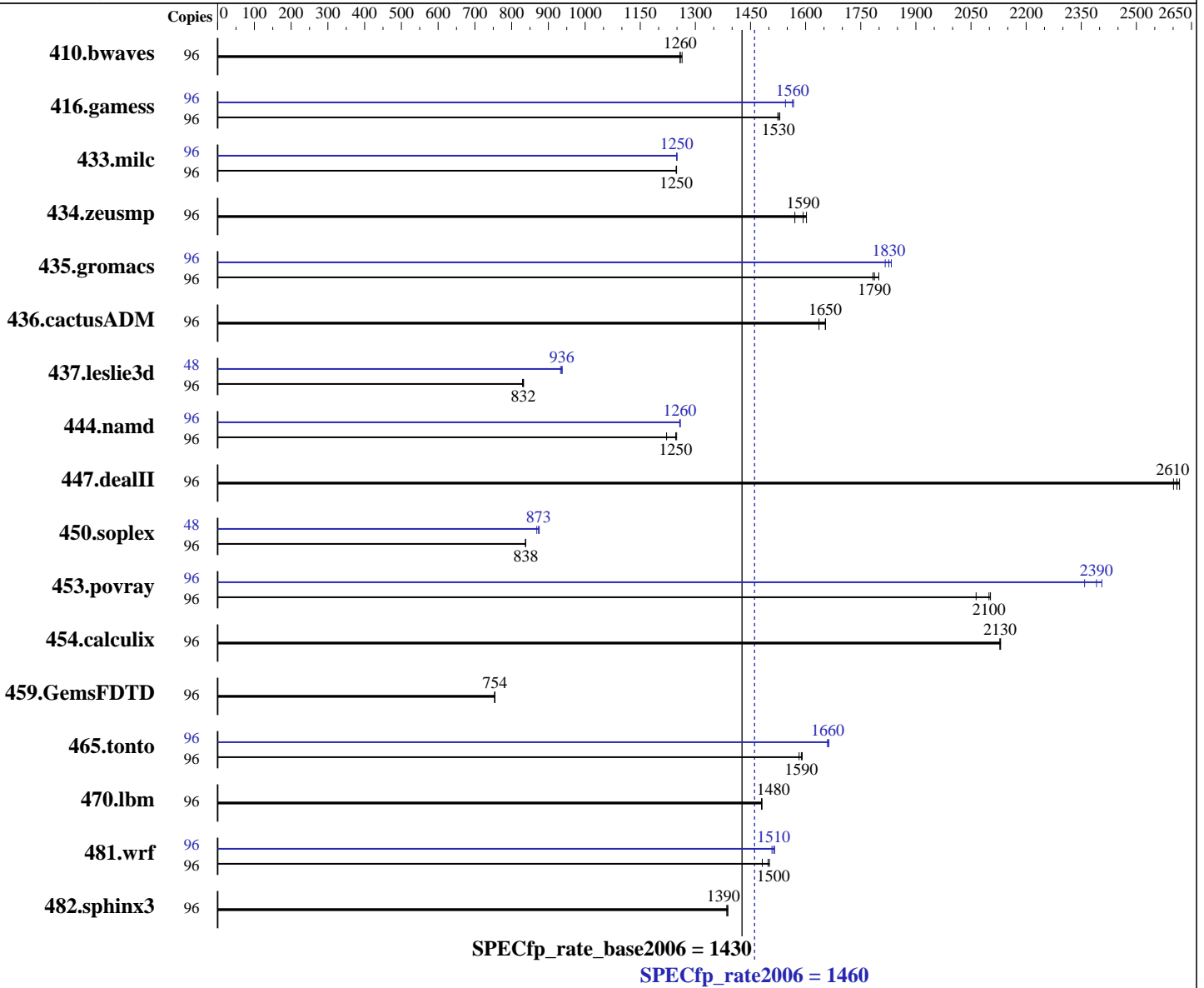$SPECfp_rate_base2006 = $ 1430

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: Apr-2014
Hardware Availability: Feb-2014
Software Availability: Nov-2013

SPECfp_rate_base2006 = 1430

SPECfp_rate2006 = 1460

Hardware

CPU Name:	Intel Xeon E7-4860 v2
CPU Characteristics:	Intel Turbo Boost Technology up to 3.20 GHz
CPU MHz:	2600
FPU:	Integrated
CPU(s) enabled:	48 cores, 4 chips, 12 cores/chip, 2 threads/core
CPU(s) orderable:	2,4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	Red Hat Enterprise Linux Server release 6.5 (Santiago) 2.6.32-431.el6.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux; Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
Auto Parallel:	No
File System:	ext4

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L3 Cache:	30 MB I+D on chip per chip	System State:	Run level 3 (multi-user)
Other Cache:	None	Base Pointers:	32/64-bit
Memory:	512 GB (64 x 8 GB 2Rx4 PC3-10600R-9, ECC)	Peak Pointers:	32/64-bit
Disk Subsystem:	2 x 300 GB SAS, 10K RPM	Other Software:	None
Other Hardware:	None		

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	96	1037	1260	1032	1260	**1037**	**1260**	96	1037	1260	1032	1260	**1037**	**1260**
416.gamess	96	1233	1520	**1232**	**1530**	1229	1530	96	**1202**	**1560**	1199	1570	1217	1550
433.milc	96	706	1250	**706**	**1250**	706	1250	96	705	1250	**705**	**1250**	705	1250
434.zeusmp	96	556	1570	**548**	**1590**	545	1600	96	556	1570	**548**	**1590**	545	1600
435.gromacs	96	381	1800	384	1780	**384**	**1790**	96	377	1820	374	1830	**375**	**1830**
436.cactusADM	96	694	1650	**694**	**1650**	701	1640	96	694	1650	**694**	**1650**	701	1640
437.leslie3d	96	1088	830	**1085**	**832**	1084	832	48	483	933	**482**	**936**	481	938
444.namd	96	616	1250	**618**	**1250**	630	1220	96	611	1260	**612**	**1260**	612	1260
447.dealII	96	420	2620	**421**	**2610**	422	2600	96	420	2620	**421**	**2610**	422	2600
450.soplex	96	955	839	**955**	**838**	956	837	48	461	868	**459**	**873**	457	875
453.povray	96	247	2060	243	2100	**243**	**2100**	96	**214**	**2390**	212	2410	216	2360
454.calculix	96	372	2130	372	2130	**372**	**2130**	96	372	2130	372	2130	**372**	**2130**
459.GemsFDTD	96	**1350**	**754**	1352	753	1350	755	96	**1350**	**754**	1352	753	1350	755
465.tonto	96	594	1590	597	1580	**595**	**1590**	96	568	1660	569	1660	**569**	**1660**
470.lbm	96	**891**	**1480**	891	1480	891	1480	96	**891**	**1480**	891	1480	891	1480
481.wrf	96	**716**	**1500**	714	1500	724	1480	96	**709**	**1510**	711	1510	707	1520
482.sphinx3	96	1350	1390	1347	1390	**1350**	**1390**	96	1350	1390	1347	1390	**1350**	**1390**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Set Power Efficiency Mode to Performance
Set Lock_step to disabled

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Hardware Availability: Feb-2014
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Platform Notes (Continued)

```
Baseboard Management Controller used to adjust the fan speed to 100%
Sysinfo program /spec/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on speccpu Sat Apr  5 06:54:07 2014

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E7-4860 v2 @ 2.60GHz
      4 "physical id"s (chips)
      96 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 12
      siblings  : 24
      physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
      physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
      physical 2: cores 0 1 2 3 4 5 8 9 10 11 12 13
      physical 3: cores 0 1 2 3 4 5 8 9 10 11 12 13
   cache size : 30720 KB

From /proc/meminfo
   MemTotal:       529101296 kB
   HugePages_Total:        0
   Hugepagesize:        2048 kB

/usr/bin/lsb_release -d
   Red Hat Enterprise Linux Server release 6.5 (Santiago)

From /etc/*release* /etc/*version*
   redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
   system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
   Linux speccpu 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013
   x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Apr 4 18:13

SPEC is set to: /spec
   Filesystem      Type   Size  Used Avail Use% Mounted on
   /dev/sda2       ext4   259G   94G  152G  39% /

Additional information from dmidecode:
  BIOS American Megatrends Inc. BLISV099 02/09/2014
  Memory:
   64x    8 GB
   32x NO DIMM NO DIMM
```

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CPU2006 license: 3175		**Test date:**	Apr-2014
Test sponsor: Huawei		**Hardware Availability:**	Feb-2014
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Platform Notes (Continued)

```
     27x Samsung M393B1K70CH0-CH9 8 GB 1333 MHz 2 rank
     37x Samsung M393B1K70DH0-CH9 8 GB 1333 MHz 2 rank

  (End of data from sysinfo program)
  Regarding the sysinfo display about the memory installed, the correct amount of
  memory is 512 GB and the dmidecode description should have two lines reading as:
     27x Samsung M393B1K70CH0-CH9 8 GB 1333 MHz 2 rank
     37x Samsung M393B1K70CH0-CH9 8 GB 1333 MHz 2 rank
```

General Notes

```
 Environment variables set by runspec before the start of the run:
 LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"

  Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
  memory using RedHat EL 6.4
  Transparent Huge Pages enabled with:
  echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
  Filesystem page cache cleared with:
  echo 1>        /proc/sys/vm/drop_caches
  runspec command invoked through numactl i.e.:
  numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
```
     icc   -m64
```

C++ benchmarks:
```
     icpc  -m64
```

Fortran benchmarks:
```
     ifort -m64
```

Benchmarks using both Fortran and C:
```
     icc   -m64 ifort -m64
```

Base Portability Flags

```
     410.bwaves: -DSPEC_CPU_LP64
     416.gamess: -DSPEC_CPU_LP64
       433.milc: -DSPEC_CPU_LP64
     434.zeusmp: -DSPEC_CPU_LP64
    435.gromacs: -DSPEC_CPU_LP64 -nofor_main
  436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
```

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Base Portability Flags (Continued)

```
   444.namd: -DSPEC_CPU_LP64
  447.dealII: -DSPEC_CPU_LP64
  450.soplex: -DSPEC_CPU_LP64
  453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
   465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
  482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3
```

Peak Compiler Invocation

C benchmarks:

```
icc   -m64
```

C++ benchmarks (except as noted below):

```
icpc   -m64
```

```
  450.soplex: icpc -m32
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc   -m64 ifort -m64
```

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Peak Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
   433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
  444.namd: -DSPEC_CPU_LP64
  447.dealII: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
  465.tonto: -DSPEC_CPU_LP64
   470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Peak Optimization Flags

C benchmarks:

```
433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
          -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
          -prof-use(pass 2) -auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes
```

C++ benchmarks:

```
444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
          -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
          -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
            -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
            -prof-use(pass 2) -unroll4 -ansi-alias
```

Fortran benchmarks:

Huawei

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SPECfp_rate_base2006 = 1430

CPU2006 license: 3175
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Tested by: Huawei

Test date: Apr-2014
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Software Availability: Nov-2013

Peak Optimization Flags (Continued)

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xAVX -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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