



SPEC® CFP2006 Result

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IBM Corporation

IBM System x3250 M5
(Intel Pentium G3220, 3.00 GHz)

SPECfp®_rate2006 = 74.5

SPECfp_rate_base2006 = 73.1

CPU2006 license: 11

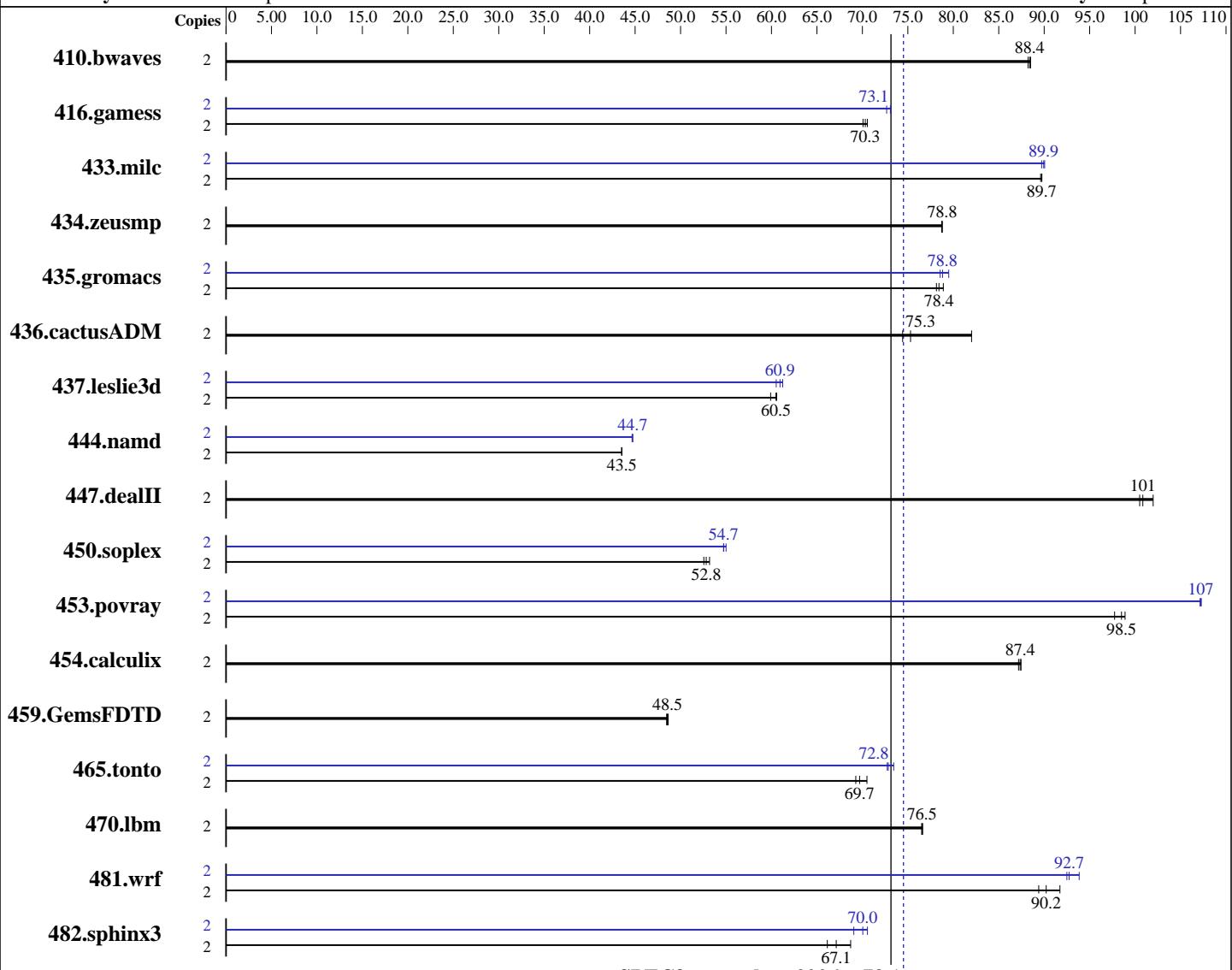
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Mar-2014

Hardware Availability: Dec-2013

Software Availability: Sep-2013



SPECfp_rate_base2006 = 73.1

SPECfp_rate2006 = 74.5

Hardware

CPU Name: Intel Pentium G3220
CPU Characteristics:
CPU MHz: 3000
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)
Compiler: 2.6.32-358.el6.x86_64
C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
Auto Parallel: No
File System: ext4

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L3 Cache: 3 MB I+D on chip per chip
Other Cache: None
Memory: 16 GB (2 x 8 GB 2Rx8 PC3-12800E-11, ECC, running at 1333 MHz)
Disk Subsystem: 1 x 1 TB SATA, 7200 RPM
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	2	307	88.5	308	88.2	<u>307</u>	<u>88.4</u>	2	307	88.5	308	88.2	<u>307</u>	<u>88.4</u>		
416.gamess	2	555	70.5	<u>557</u>	<u>70.3</u>	559	70.1	2	535	73.1	<u>536</u>	<u>73.1</u>	539	72.7		
433.milc	2	<u>205</u>	<u>89.7</u>	205	89.7	205	89.6	2	204	90.1	205	89.7	<u>204</u>	<u>89.9</u>		
434.zeusmp	2	231	78.7	231	78.8	<u>231</u>	<u>78.8</u>	2	231	78.7	231	78.8	<u>231</u>	<u>78.8</u>		
435.gromacs	2	<u>182</u>	<u>78.4</u>	181	78.9	183	78.1	2	<u>181</u>	<u>78.8</u>	182	78.6	180	79.5		
436.cactusADM	2	321	74.4	<u>317</u>	<u>75.3</u>	291	82.0	2	321	74.4	<u>317</u>	<u>75.3</u>	291	82.0		
437.leslie3d	2	314	59.9	310	60.6	<u>311</u>	<u>60.5</u>	2	<u>309</u>	<u>60.9</u>	307	61.2	311	60.5		
444.namd	2	<u>368</u>	<u>43.5</u>	369	43.5	368	43.5	2	<u>359</u>	<u>44.7</u>	359	44.7	359	44.7		
447.dealII	2	<u>227</u>	<u>101</u>	228	101	224	102	2	<u>227</u>	<u>101</u>	228	101	224	102		
450.soplex	2	317	52.6	314	53.2	<u>316</u>	<u>52.8</u>	2	<u>305</u>	<u>54.7</u>	305	54.7	303	55.0		
453.povray	2	109	97.7	<u>108</u>	<u>98.5</u>	108	98.9	2	<u>99.2</u>	<u>107</u>	99.3	107	99.2	107		
454.calculix	2	189	87.5	<u>189</u>	<u>87.4</u>	189	87.2	2	189	87.5	<u>189</u>	<u>87.4</u>	189	87.2		
459.GemsFDTD	2	438	48.5	436	48.6	<u>437</u>	<u>48.5</u>	2	438	48.5	436	48.6	<u>437</u>	<u>48.5</u>		
465.tonto	2	279	70.5	<u>282</u>	<u>69.7</u>	284	69.3	2	271	72.7	<u>270</u>	<u>72.8</u>	268	73.4		
470.lbm	2	359	76.6	<u>359</u>	<u>76.5</u>	359	76.5	2	359	76.6	<u>359</u>	<u>76.5</u>	359	76.5		
481.wrf	2	244	91.7	<u>248</u>	<u>90.2</u>	250	89.4	2	<u>241</u>	<u>92.7</u>	238	93.8	242	92.5		
482.sphinx3	2	567	68.7	<u>581</u>	<u>67.1</u>	589	66.1	2	<u>564</u>	69.1	552	70.6	<u>557</u>	<u>70.0</u>		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS setting:

Operating Mode set to Maximum Performance

Sysinfo program /home/SPECcpu-new/config/sysinfo.rev6818

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Platform Notes (Continued)

\$Rev: 6818 \$ \$Date::: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191
running on x3250M5 Thu Mar 27 17:32:40 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
    model name : Intel(R) Pentium(R) CPU G3220 @ 3.00GHz
        1 "physical id"s (chips)
        2 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 2
    siblings   : 2
    physical 0: cores 0 1
cache size : 3072 KB
```

```
From /proc/meminfo
MemTotal:      16299404 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.4 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux x3250M5 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41 EST 2013
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Mar 27 11:05
```

```
SPEC is set to: /home/SPECcpu-new
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/mapper/vg_x3250m5-lv_home
                ext4   852G   16G  793G   2%  /home
```

Additional information from dmidecode:

BIOS IBM -[JUE109OUS-1.00]- 11/20/2013

Memory:

2x Micron 18KSF1G72AZ-1G6E1 8 GB 1333 MHz 2 rank
2x Not Specified Not Specified

(End of data from sysinfo program)

"Not Specified" memory information from dmidecode indicates unused DIMM slots.



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General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/home/SPECcpu-new/libs/32:/home/SPECcpu-new/libs/64:/home/SPECcpu-new/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB

memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

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Base Optimization Flags (Continued)

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias  
-opt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias  
-opt-mem-layout-trans=3
```

Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc -m64
```

```
482.sphinx3: icc -m32
```

C++ benchmarks (except as noted below):

```
icpc -m64
```

```
450.soplex: icpc -m32
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

Peak Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64  
416.gamess: -DSPEC_CPU_LP64  
433.milc: -DSPEC_CPU_LP64  
434.zeusmp: -DSPEC_CPU_LP64  
435.gromacs: -DSPEC_CPU_LP64 -nofor_main  
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main  
437.leslie3d: -DSPEC_CPU_LP64  
444.namd: -DSPEC_CPU_LP64  
447.dealII: -DSPEC_CPU_LP64  
453.povray: -DSPEC_CPU_LP64  
454.calculix: -DSPEC_CPU_LP64 -nofor_main  
459.GemsFDTD: -DSPEC_CPU_LP64
```

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Peak Portability Flags (Continued)

465.tonto: -DSPEC_CPU_LP64

470.lbm: -DSPEC_CPU_LP64

481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -opt-mem-layout-trans=3
-unroll12

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -unroll14 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll12
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

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Peak Optimization Flags (Continued)

465.tonto: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xsse4.2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-HSW-A.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-HSW-A.xml>

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Tested with SPEC CPU2006 v1.2.

Report generated on Mon Sep 22 17:45:12 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 22 April 2014.