SPEC® CFP2006 Result

Dell Inc.

PowerEdge M820 (Intel Xeon E5-4610 v2, 2.30 GHz)

$SPECfp^{®}_rate2006 = $ 936

$SPECfp_rate_base2006 = $ 918
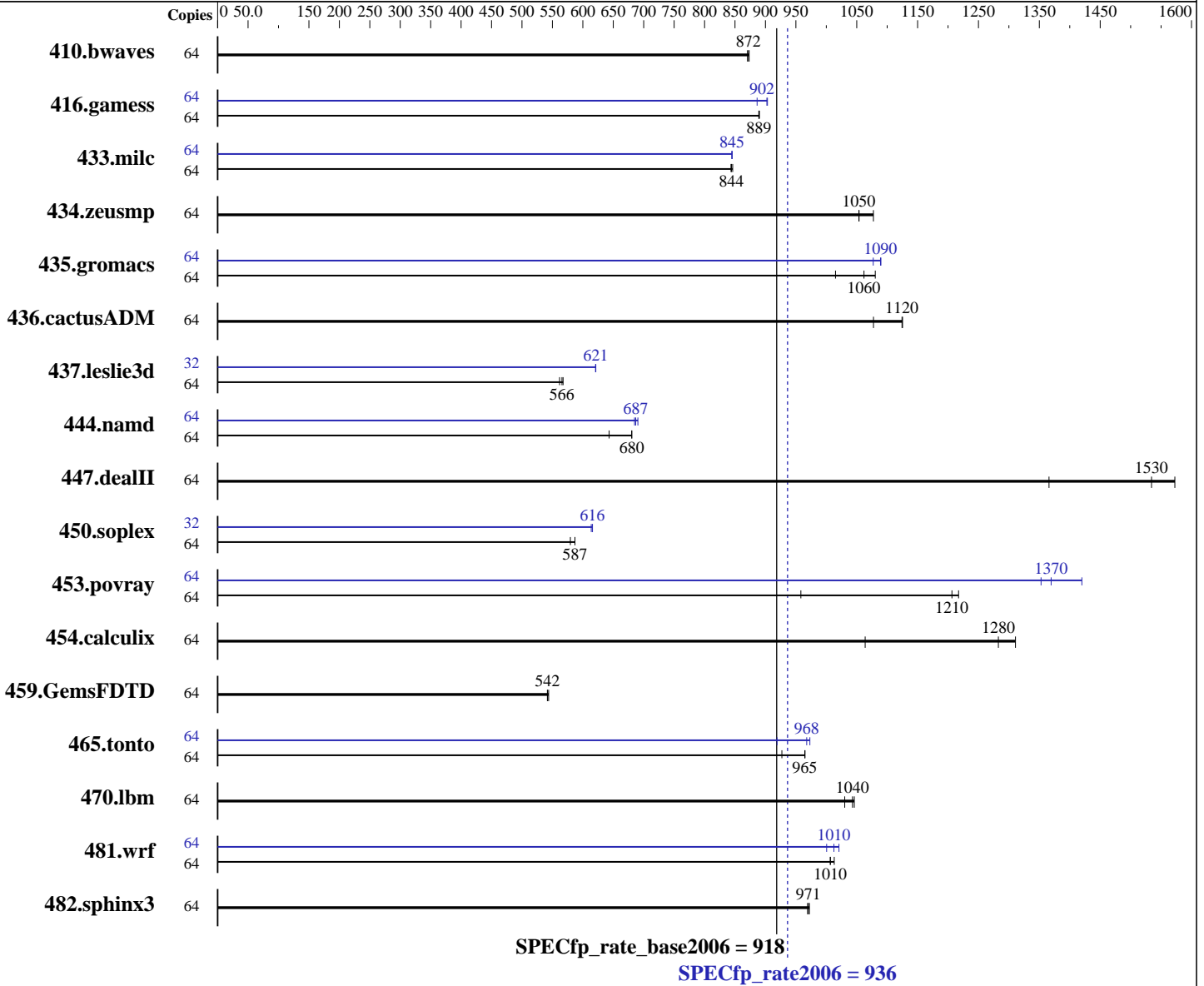
CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Feb-2014
Hardware Availability: Mar-2014
Software Availability: Aug-2013

Benchmark	Copies	Result
410.bwaves	64	872
416.gamess	64	902
416.gamess	64	889
433.milc	64	845
433.milc	64	844
434.zeusmp	64	1050
435.gromacs	64	1090
435.gromacs	64	1060
436.cactusADM	64	1120
437.leslie3d	32	621
437.leslie3d	64	566
444.namd	64	687
444.namd	64	680
447.dealII	64	1530
450.soplex	32	616
450.soplex	64	587
453.povray	64	1370
453.povray	64	1210
454.calculix	64	1280
459.GemsFDTD	64	542
465.tonto	64	968
465.tonto	64	965
470.lbm	64	1040
481.wrf	64	1010
481.wrf	64	1010
482.sphinx3	64	971

SPECfp_rate_base2006 = 918

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Hardware

CPU Name:	Intel Xeon E5-4610 v2
CPU Characteristics:	Intel Turbo Boost Technology up to 2.70 GHz
CPU MHz:	2300
FPU:	Integrated
CPU(s) enabled:	32 cores, 4 chips, 8 cores/chip, 2 threads/core
CPU(s) orderable:	4 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64)
	SUSE Linux Enterprise Server 11 (x86_64)
	3.0.76-0.11-default
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
	Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
Auto Parallel:	No
File System:	ext2

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L3 Cache:	16 MB I+D on chip per chip	System State:	Run level 3 (multi-user)
Other Cache:	None	Base Pointers:	32/64-bit
Memory:	512 GB (32 x 16 GB 2Rx4 PC3-14900R-13, ECC)	Peak Pointers:	32/64-bit
Disk Subsystem:	1 x 600 GB 10000 RPM SAS	Other Software:	None
Other Hardware:	None		

Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	64	**997**	**872**	999	870	996	873	64	**997**	**872**	999	870	996	873
416.gamess	64	**1409**	**889**	1409	889	1408	890	64	**1389**	**902**	1387	903	1414	886
433.milc	64	694	846	697	843	**696**	**844**	64	**695**	**845**	695	845	696	845
434.zeusmp	64	541	1080	**553**	**1050**	553	1050	64	541	1080	**553**	**1050**	553	1050
435.gromacs	64	423	1080	**430**	**1060**	450	1020	64	**420**	**1090**	419	1090	424	1080
436.cactusADM	64	680	1130	**680**	**1120**	710	1080	64	680	1130	**680**	**1120**	710	1080
437.leslie3d	64	1060	568	**1063**	**566**	1071	562	32	484	621	484	621	**484**	**621**
444.namd	64	754	680	**754**	**680**	798	643	64	**747**	**687**	743	690	749	685
447.dealII	64	466	1570	**477**	**1530**	536	1370	64	466	1570	**477**	**1530**	536	1370
450.soplex	64	909	587	**909**	**587**	921	580	32	433	616	**433**	**616**	435	614
453.povray	64	280	1220	**282**	**1210**	355	958	64	**249**	**1370**	252	1350	240	1420
454.calculix	64	403	1310	**412**	**1280**	496	1060	64	403	1310	**412**	**1280**	496	1060
459.GemsFDTD	64	1254	542	1249	544	**1253**	**542**	64	1254	542	1249	544	**1253**	**542**
465.tonto	64	653	965	**653**	**965**	679	927	64	**651**	**968**	685	919	647	973
470.lbm	64	841	1050	**843**	**1040**	854	1030	64	841	1050	**843**	**1040**	854	1030
481.wrf	64	**710**	**1010**	710	1010	706	1010	64	700	1020	714	1000	**706**	**1010**
482.sphinx3	64	**1285**	**971**	1287	969	1283	972	64	**1285**	**971**	1287	969	1283	972

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

Virtualization Technology disabled
Execute Disable disabled
Logical Processor enabled

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Platform Notes (Continued)

```
System Profile set to Performance
Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on linux Wed Feb 12 09:28:49 2014

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-4610 v2 @ 2.30GHz
      4 "physical id"s (chips)
      64 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 8
      siblings  : 16
      physical 0: cores 0 1 2 3 4 5 6 7
      physical 1: cores 0 1 2 3 4 5 6 7
      physical 2: cores 0 1 2 3 4 5 6 7
      physical 3: cores 0 1 2 3 4 5 6 7
   cache size : 16384 KB

From /proc/meminfo
   MemTotal:       529392220 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
   SUSE Linux Enterprise Server 11 (x86_64)

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 11 (x86_64)
      VERSION = 11
      PATCHLEVEL = 3

uname -a:
   Linux linux 3.0.76-0.11-default #1 SMP Fri Jun 14 08:21:43 UTC 2013 (ccab990)
   x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Feb 11 19:10 last=S

SPEC is set to: /root/cpu2006-1.2
   Filesystem      Type  Size  Used Avail Use% Mounted on
   /dev/sda2       ext2  451G   63G  387G  14% /

Additional information from dmidecode:
   BIOS Dell Inc. 2.0.24 01/21/2014
   Memory:
   8x 002C04B3002C 36JSF2G72PZ-1G9E1 16 GB 1600 MHz
```
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Platform Notes (Continued)

```
    21x 00CE00B300CE M393B2G70DB0-CMA 16 GB 1600 MHz
    3x 00CE04B300CE M393B2G70DB0-CMA 16 GB 1600 MHz

  (End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64:/root/cpu2006-1.2/sh"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RedHat EL 6.4
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>        /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
```
    icc    -m64
```

C++ benchmarks:
```
    icpc   -m64
```

Fortran benchmarks:
```
    ifort -m64
```

Benchmarks using both Fortran and C:
```
    icc    -m64 ifort -m64
```

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
       433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
    435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
      444.namd: -DSPEC_CPU_LP64
     447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
```

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Base Portability Flags (Continued)

```
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
  459.GemsFDTD: -DSPEC_CPU_LP64
       465.tonto: -DSPEC_CPU_LP64
        470.lbm: -DSPEC_CPU_LP64
        481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
     482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3
```

C++ benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3
```

Fortran benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:
```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3
```

Peak Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks (except as noted below):
```
icpc  -m64
```

```
  450.soplex: icpc -m32
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Peak Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
```

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Software Availability: Aug-2013

Peak Portability Flags (Continued)

```
  416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
  434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
   447.dealII: -DSPEC_CPU_LP64
   453.povray: -DSPEC_CPU_LP64
  454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
     470.lbm: -DSPEC_CPU_LP64
     481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
  482.sphinx3: -DSPEC_CPU_LP64
```

Peak Optimization Flags

C benchmarks:

```
    433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
              -prof-use(pass 2) -auto-ilp32

     470.lbm: basepeak = yes

  482.sphinx3: basepeak = yes
```

C++ benchmarks:

```
    444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
              -prof-use(pass 2) -fno-alias -auto-ilp32

   447.dealII: basepeak = yes

   450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
              -prof-use(pass 2) -opt-malloc-options=3

   453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
              -prof-use(pass 2) -unroll4 -ansi-alias
```

Fortran benchmarks:

```
   410.bwaves: basepeak = yes
```

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Peak Optimization Flags (Continued)

416.gamess: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)`
`-no-prec-div(pass 2) -prof-use(pass 2) -unroll2`
`-inline-level=0 -scalar-rep-`

434.zeusmp: `basepeak = yes`

437.leslie3d: `-xAVX -ipo -O3 -no-prec-div -opt-prefetch`

459.GemsFDTD: `basepeak = yes`

465.tonto: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)`
`-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto`
`-inline-calloc -opt-malloc-options=3`

Benchmarks using both Fortran and C:

435.gromacs: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)`
`-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)`
`-prof-use(pass 2) -opt-prefetch -auto-ilp32`

436.cactusADM: `basepeak = yes`

454.calculix: `basepeak = yes`

481.wrf: `-xAVX -ipo -O3 -no-prec-div -auto-ilp32`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revC.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revC.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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