



SPEC[®] CFP2006 Result

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Dell Inc.

SPECfp[®]_rate2006 = 1640

PowerEdge R920 (Intel Xeon E7-4880 v2, 2.50 GHz)

SPECfp_rate_base2006 = 1610

CPU2006 license: 55

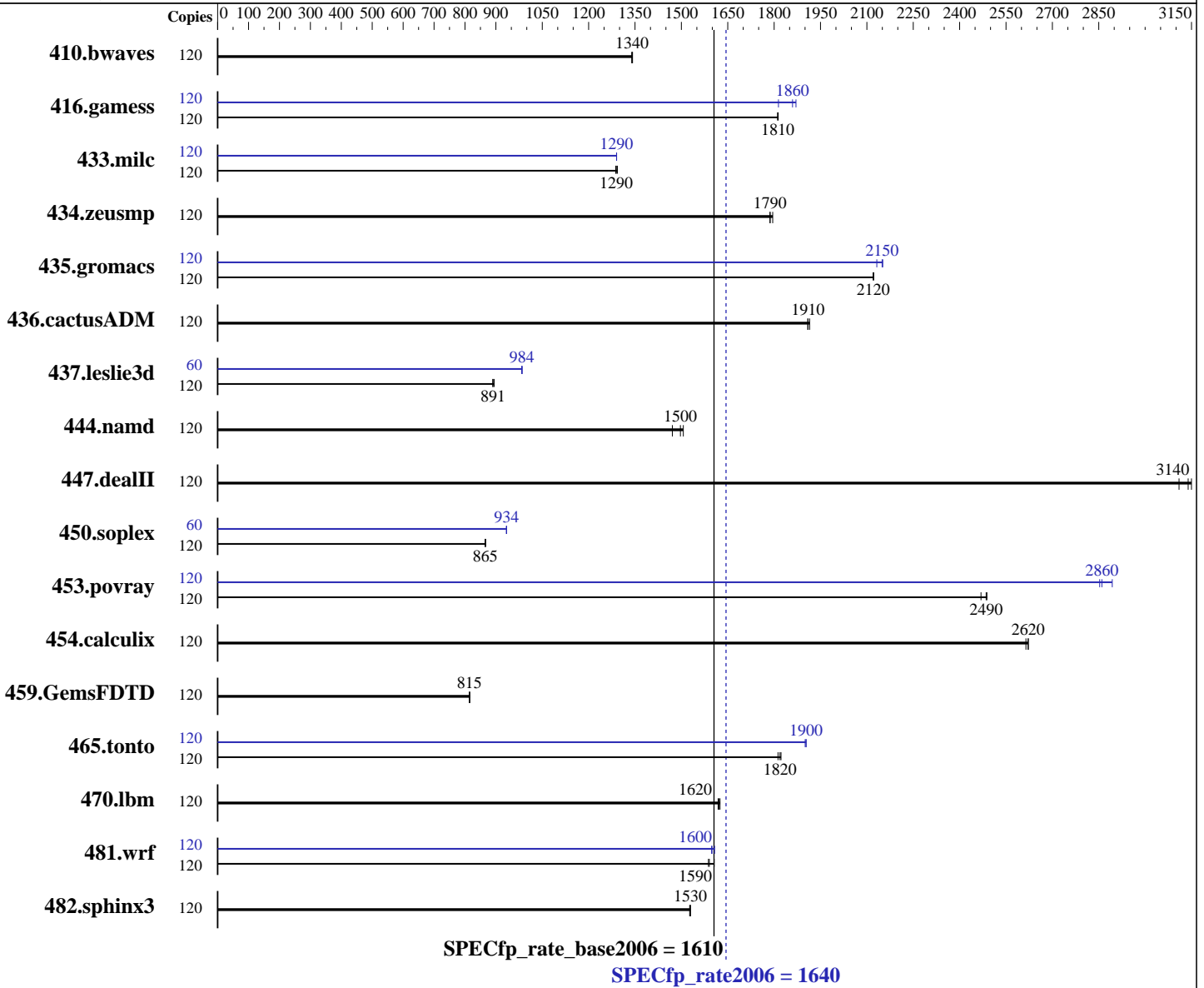
Test date: Mar-2014

Test sponsor: Dell Inc.

Hardware Availability: Mar-2014

Tested by: Dell Inc.

Software Availability: Mar-2014



Hardware

CPU Name: Intel Xeon E7-4880 v2
 CPU Characteristics: Intel Turbo Boost Technology up to 3.10 GHz
 CPU MHz: 2500
 FPU: Integrated
 CPU(s) enabled: 60 cores, 4 chips, 15 cores/chip, 2 threads/core
 CPU(s) orderable: 2,4 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64)
 3.0.76-0.11-default
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
 Auto Parallel: No
 File System: ext2
 System State: Run level 3 (multi-user)

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L3 Cache: 37.5 MB I+D on chip per chip
Other Cache: None
Memory: 1 TB (64 x 16 GB 2Rx4 PC3L-12800R-11, ECC)
Disk Subsystem: 1 x 400 GB SAS6 SSD
Other Hardware: None

Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	120	1215	1340	1218	1340	<u>1216</u>	<u>1340</u>	120	1215	1340	1218	1340	<u>1216</u>	<u>1340</u>
416.gamess	120	1297	1810	1296	1810	<u>1297</u>	<u>1810</u>	120	1295	1810	1256	1870	<u>1264</u>	<u>1860</u>
433.milc	120	<u>854</u>	<u>1290</u>	852	1290	856	1290	120	854	1290	854	1290	<u>854</u>	<u>1290</u>
434.zeusmp	120	608	1800	611	1790	<u>611</u>	<u>1790</u>	120	608	1800	611	1790	<u>611</u>	<u>1790</u>
435.gromacs	120	404	2120	<u>404</u>	<u>2120</u>	404	2120	120	398	2150	402	2130	<u>399</u>	<u>2150</u>
436.cactusADM	120	749	1910	<u>751</u>	<u>1910</u>	751	1910	120	749	1910	<u>751</u>	<u>1910</u>	751	1910
437.leslie3d	120	1268	890	<u>1266</u>	<u>891</u>	1261	894	60	573	985	<u>573</u>	<u>984</u>	574	983
444.namd	120	<u>643</u>	<u>1500</u>	639	1510	654	1470	120	<u>643</u>	<u>1500</u>	639	1510	654	1470
447.dealII	120	436	3150	<u>437</u>	<u>3140</u>	441	3110	120	436	3150	<u>437</u>	<u>3140</u>	441	3110
450.soplex	120	1154	867	<u>1156</u>	<u>865</u>	1157	865	60	535	935	<u>535</u>	<u>934</u>	536	934
453.povray	120	257	2490	<u>257</u>	<u>2490</u>	259	2470	120	<u>223</u>	<u>2860</u>	221	2890	224	2850
454.calculix	120	378	2620	379	2610	<u>378</u>	<u>2620</u>	120	378	2620	379	2610	<u>378</u>	<u>2620</u>
459.GemsFDTD	120	<u>1562</u>	<u>815</u>	1561	816	1564	814	120	<u>1562</u>	<u>815</u>	1561	816	1564	814
465.tonto	120	<u>649</u>	<u>1820</u>	648	1820	651	1810	120	620	1900	<u>620</u>	<u>1900</u>	621	1900
470.lbm	120	<u>1017</u>	<u>1620</u>	1019	1620	1015	1630	120	<u>1017</u>	<u>1620</u>	1019	1620	1015	1630
481.wrf	120	844	1590	<u>843</u>	<u>1590</u>	835	1610	120	<u>838</u>	<u>1600</u>	839	1600	834	1610
482.sphinx3	120	1532	1530	1529	1530	<u>1530</u>	<u>1530</u>	120	1532	1530	1529	1530	<u>1530</u>	<u>1530</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS settings:
Virtualization Technology disabled
Execute Disable disabled

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Platform Notes (Continued)

System Profile set to Custom
 Memory Patrol Scrub set to disabled
 Sysinfo program
 /root/Desktop/Performance/ic14.0_Oct17_2013/config/sysinfo.rev6818
 \$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191
 running on slesperf1 Sat Mar 1 21:36:26 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name      : Intel(R) Xeon(R) CPU E7-4880 v2 @ 2.50GHz
 4 "physical id"s (chips)
 120 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores    : 15
  siblings    : 30
  physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14
  physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14
  physical 2: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14
  physical 3: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14
cache size     : 38400 KB
```

```
From /proc/meminfo
MemTotal:      1058789108 kB
HugePages_Total:    0
Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 11 (x86_64)
```

```
From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 11 (x86_64)
VERSION = 11
PATCHLEVEL = 3
```

```
uname -a:
Linux slesperf1 3.0.76-0.11-default #1 SMP Fri Jun 14 08:21:43 UTC 2013
(ccab990) x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Feb 28 18:46 last=S

```
SPEC is set to: /root/Desktop/Performance/ic14.0_Oct17_2013
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       ext2  365G   31G  333G   9% /
```

Additional information from dmidecode:
 BIOS Dell Inc. 1.0.4 01/27/2014

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Platform Notes (Continued)

Memory:

32x 00CE00B300CE M393B2G70BH0-YK0 16 GB 1333 MHz
10x 00CE00B300CE M393B2G70CB0-YK0 16 GB 1333 MHz
22x 00CE04B300CE M393B2G70CB0-YK0 16 GB 1333 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = */root/Desktop/Performance/icl4.0_Oct17_2013/libs/32:/root/Desktop/Performance/icl4.0_Oct17_2013/libs/64:/root/Desktop/Performance/icl4.0_Oct17_2013/sh*

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64

416.gamess: -DSPEC_CPU_LP64

433.milc: -DSPEC_CPU_LP64

434.zeusmp: -DSPEC_CPU_LP64

435.gromacs: -DSPEC_CPU_LP64 -nofor_main

436.cactusADM: -DSPEC_CPU_LP64 -nofor_main

437.leslie3d: -DSPEC_CPU_LP64

444.namd: -DSPEC_CPU_LP64

447.dealII: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

```

450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

```

C++ benchmarks:

```

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

```

Fortran benchmarks:

```

-xAVX -ipo -O3 -no-prec-div -opt-prefetch

```

Benchmarks using both Fortran and C:

```

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

```

Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks (except as noted below):

```

icpc -m64

```

```

450.soplex: icpc -m32

```

Fortran benchmarks:

```

ifort -m64

```

Benchmarks using both Fortran and C:

```

icc -m64 ifort -m64

```



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Peak Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Peak Optimization Flags

C benchmarks:

```

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -auto-ilp32

```

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: basepeak = yes

447.dealII: basepeak = yes

```

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-malloc-options=3

```

```

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -unroll4 -ansi-alias

```

Fortran benchmarks:

410.bwaves: basepeak = yes

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Peak Optimization Flags (Continued)

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xAVX -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revC.xml>

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