



# SPEC<sup>®</sup> CFP2006 Result

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## NEC Corporation

SPECfp<sup>®</sup>\_rate2006 = 369

Express5800/R120e-1E (Intel Xeon E5-2430L v2)

SPECfp\_rate\_base2006 = 361

CPU2006 license: 9006

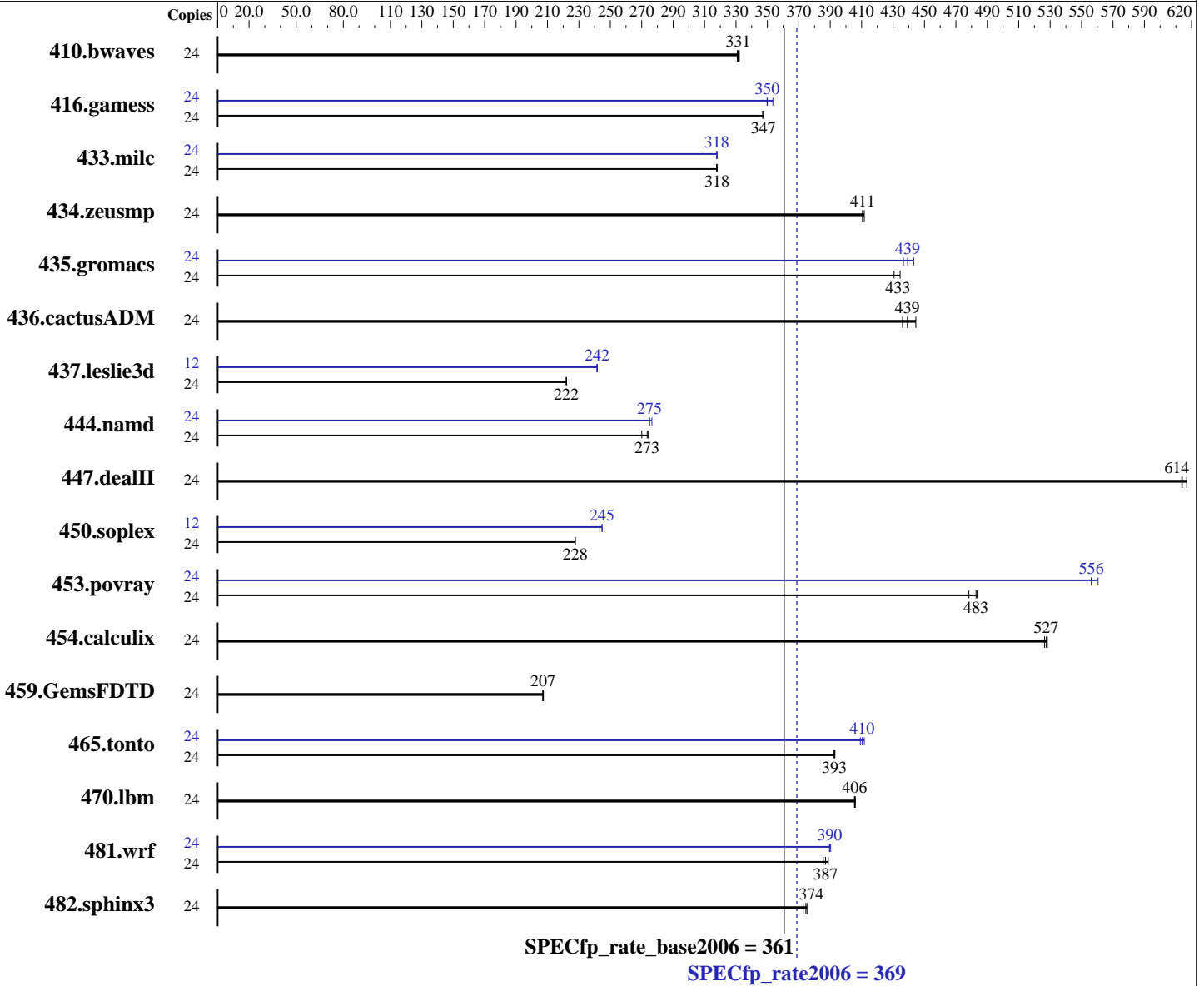
Test date: Feb-2014

Test sponsor: NEC Corporation

Hardware Availability: Jan-2014

Tested by: NEC Corporation

Software Availability: Sep-2013



### Hardware

CPU Name: Intel Xeon E5-2430L v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)  
 Kernel 2.6.32-358.18.1.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;  
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4

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L3 Cache: 15 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 192 GB (12 x 16 GB 2Rx4 PC3L-12800R-11, ECC)  
 Disk Subsystem: 1 x 250 GB SATA, 7200 RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	24	<b>984</b>	<b>331</b>	982	332	986	331	24	<b>984</b>	<b>331</b>	982	332	986	331
416.gamess	24	1352	348	<b>1352</b>	<b>347</b>	1354	347	24	<b>1343</b>	<b>350</b>	1329	353	1343	350
433.milc	24	<b>693</b>	<b>318</b>	693	318	693	318	24	<b>693</b>	<b>318</b>	693	318	693	318
434.zeusmp	24	532	411	531	412	<b>532</b>	<b>411</b>	24	532	411	531	412	<b>532</b>	<b>411</b>
435.gromacs	24	394	434	<b>396</b>	<b>433</b>	398	431	24	<b>390</b>	<b>439</b>	392	437	387	443
436.cactusADM	24	658	436	645	445	<b>653</b>	<b>439</b>	24	658	436	645	445	<b>653</b>	<b>439</b>
437.leslie3d	24	1016	222	1017	222	<b>1016</b>	<b>222</b>	12	466	242	467	241	<b>467</b>	<b>242</b>
444.namd	24	702	274	<b>704</b>	<b>273</b>	713	270	24	700	275	<b>700</b>	<b>275</b>	696	276
447.dealII	24	<b>447</b>	<b>614</b>	445	617	447	614	24	<b>447</b>	<b>614</b>	445	617	447	614
450.soplex	24	879	228	<b>879</b>	<b>228</b>	880	228	12	<b>409</b>	<b>245</b>	411	243	409	245
453.povray	24	267	478	264	483	<b>264</b>	<b>483</b>	24	228	561	230	556	<b>230</b>	<b>556</b>
454.calculix	24	375	528	<b>375</b>	<b>527</b>	376	526	24	375	528	<b>375</b>	<b>527</b>	376	526
459.GemsFDTD	24	<b>1229</b>	<b>207</b>	1228	207	1230	207	24	<b>1229</b>	<b>207</b>	1228	207	1230	207
465.tonto	24	601	393	<b>601</b>	<b>393</b>	602	392	24	574	412	<b>575</b>	<b>410</b>	577	409
470.lbm	24	<b>813</b>	<b>406</b>	812	406	813	405	24	<b>813</b>	<b>406</b>	812	406	813	405
481.wrf	24	690	389	695	386	<b>693</b>	<b>387</b>	24	687	390	688	390	<b>688</b>	<b>390</b>
482.sphinx3	24	1247	375	1255	373	<b>1249</b>	<b>374</b>	24	1247	375	1255	373	<b>1249</b>	<b>374</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Settings:  
Energy Performance: Performance  
Memory Voltage: 1.5 V



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## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

The Express5800/R120e-1E and the Express5800/R120e-2E models are electronically equivalent. The results have been measured on the Express5800/R120e-2E model.

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

Filesystem page cache cleared with:

echo 1 > /proc/sys/vm/drop\_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64



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## Base Optimization Flags

C benchmarks:

`-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias  
-opt-mem-layout-trans=3`

C++ benchmarks:

`-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias  
-opt-mem-layout-trans=3`

Fortran benchmarks:

`-xAVX -ipo -O3 -no-prec-div -opt-prefetch`

Benchmarks using both Fortran and C:

`-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias  
-opt-mem-layout-trans=3`

## Peak Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks (except as noted below):

`icpc -m64`

`450.soplex: icpc -m32`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

## Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`  
416.gamess: `-DSPEC_CPU_LP64`  
433.milc: `-DSPEC_CPU_LP64`  
434.zeusmp: `-DSPEC_CPU_LP64`  
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`  
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`  
437.leslie3d: `-DSPEC_CPU_LP64`  
444.namd: `-DSPEC_CPU_LP64`  
447.dealII: `-DSPEC_CPU_LP64`  
453.povray: `-DSPEC_CPU_LP64`  
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`  
459.GemsFDTD: `-DSPEC_CPU_LP64`

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## Peak Portability Flags (Continued)

465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Peak Optimization Flags

### C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)  
 -prof-use(pass 2) -auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

### C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)  
 -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealIII: basepeak = yes

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)  
 -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)  
 -prof-use(pass 2) -unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

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## Peak Optimization Flags (Continued)

```
465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
           -inline-calloc -opt-malloc-options=3
```

Benchmarks using both Fortran and C:

```
435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
            -prof-use(pass 2) -opt-prefetch -auto-ilp32
```

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

```
481.wrf: -xAVX -ipo -O3 -no-prec-div -auto-ilp32
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120-RevB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120-RevB.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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