



SPEC[®] CFP2006 Result

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Supermicro

Supermicro A+ Server 4022G-6F, (H8DG6-F, Opteron 6370P)
AMD Opteron 6370P

SPECfp[®]_rate2006 = 376

SPECfp_rate_base2006 = 338

CPU2006 license: 001176

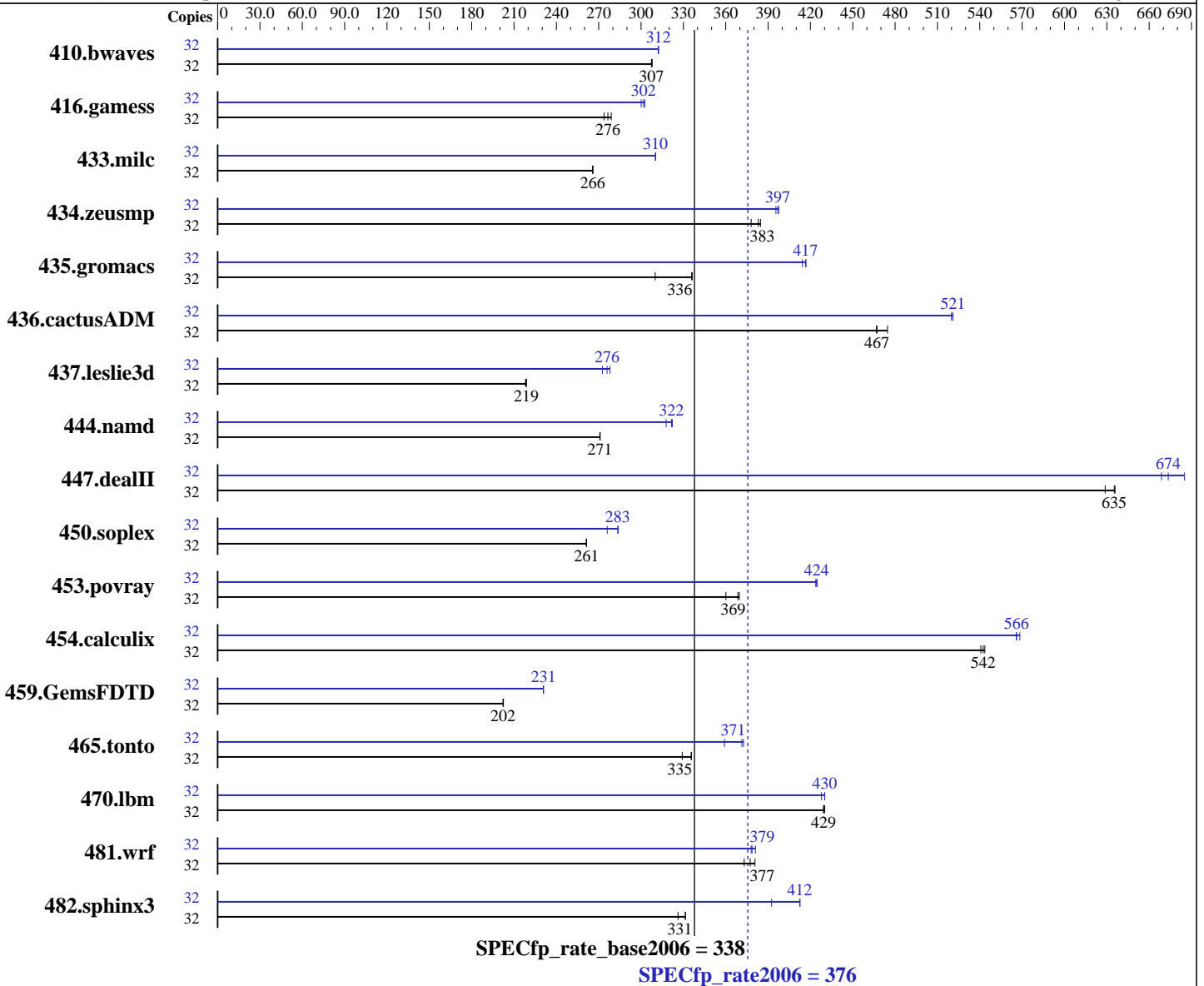
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Oct-2013

Hardware Availability: Jan-2014

Software Availability: Aug-2012



Hardware

CPU Name: AMD Opteron 6370P
 CPU Characteristics: AMD Turbo CORE technology up to 2.50 GHz
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 32 cores, 2 chips, 16 cores/chip
 CPU(s) orderable: 1,2 chips

Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server release 6.4, Kernel 2.6.32-358.el6.x86_64
 Compiler: C/C++/Fortran: Version 4.5.2 of x86 Open64 Compiler Suite (from AMD)
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (Full multiuser with network)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit

Continued on next page



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Primary Cache: 512 KB I on chip per chip,
64 KB I shared / 2 cores;
16 KB D on chip per core
Secondary Cache: 16 MB I+D on chip per chip, 2 MB shared / 2 cores
L3 Cache: 16 MB I+D on chip per chip, 8 MB shared / 8 cores
Other Cache: None
Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC)
Disk Subsystem: 1 x 500 GB SATA II, 7200 RPM
Other Hardware: None

Other Software: None

Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---------------|--------|-------------|------------|-------------|------------|-------------|------------|--------|-------------|------------|-------------|------------|-------------|------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 32 | 1413 | 308 | 1415 | 307 | 1414 | 307 | 32 | 1393 | 312 | 1393 | 312 | 1392 | 312 |
| 416.gamess | 32 | 2247 | 279 | 2289 | 274 | 2266 | 276 | 32 | 2077 | 302 | 2088 | 300 | 2070 | 303 |
| 433.milc | 32 | 1106 | 266 | 1105 | 266 | 1106 | 266 | 32 | 946 | 310 | 947 | 310 | 947 | 310 |
| 434.zeusmp | 32 | 757 | 385 | 760 | 383 | 770 | 378 | 32 | 733 | 398 | 734 | 397 | 736 | 396 |
| 435.gromacs | 32 | 680 | 336 | 679 | 336 | 737 | 310 | 32 | 551 | 414 | 548 | 417 | 548 | 417 |
| 436.cactusADM | 32 | 819 | 467 | 806 | 475 | 819 | 467 | 32 | 734 | 521 | 734 | 521 | 736 | 520 |
| 437.leslie3d | 32 | 1375 | 219 | 1375 | 219 | 1379 | 218 | 32 | 1103 | 273 | 1090 | 276 | 1082 | 278 |
| 444.namd | 32 | 947 | 271 | 948 | 271 | 947 | 271 | 32 | 808 | 318 | 797 | 322 | 798 | 322 |
| 447.dealII | 32 | 576 | 635 | 582 | 629 | 576 | 636 | 32 | 534 | 685 | 547 | 669 | 544 | 674 |
| 450.soplex | 32 | 1022 | 261 | 1021 | 261 | 1021 | 261 | 32 | 967 | 276 | 941 | 283 | 940 | 284 |
| 453.povray | 32 | 462 | 369 | 473 | 360 | 461 | 370 | 32 | 401 | 424 | 402 | 424 | 401 | 425 |
| 454.calculix | 32 | 487 | 542 | 486 | 544 | 488 | 541 | 32 | 466 | 566 | 464 | 568 | 467 | 566 |
| 459.GemsFDTD | 32 | 1678 | 202 | 1679 | 202 | 1677 | 202 | 32 | 1471 | 231 | 1471 | 231 | 1471 | 231 |
| 465.tonto | 32 | 938 | 336 | 957 | 329 | 939 | 335 | 32 | 845 | 373 | 877 | 359 | 848 | 371 |
| 470.lbm | 32 | 1024 | 429 | 1022 | 430 | 1024 | 429 | 32 | 1028 | 428 | 1022 | 430 | 1022 | 430 |
| 481.wrf | 32 | 958 | 373 | 939 | 381 | 947 | 377 | 32 | 938 | 381 | 946 | 378 | 944 | 379 |
| 482.sphinx3 | 32 | 1912 | 326 | 1882 | 331 | 1882 | 331 | 32 | 1589 | 392 | 1511 | 413 | 1512 | 412 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152' was used to set environment locked pages in memory limit

Set transparent_hugepage=never as a boot parameter in /boot/grub/menu.lst
Continued on next page



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Operating System Notes (Continued)

```
Set vm/nr_hugepages=28672 in /etc/sysctl.conf
mount -t hugetlbfs nodev /mnt/hugepages
```

Platform Notes

C State mode set to disabled under Processor & Clock Options in BIOS.
HPC Mode set to enabled under Processor & Clock Options in BIOS.
C1E Support set to disabled under Processor & Clock Options in BIOS.
Power Down Enable set to disabled in Memory Configuration in BIOS.

General Notes

Environment variables set by runspec before the start of the run:

```
HUGETLB_LIMIT = "896"
```

```
LD_LIBRARY_PATH = "/home/spec/amd1206-rate-libs-revA/32:/home/spec/amd1206-rate-libs-revA/64"
```

The x86 Open64 Compiler Suite is only available from (and supported by) AMD at <http://developer.amd.com/cpu/open64>

Binaries were compiled on a system with 2x AMD Opteron 6386SE chips + 128GB Memory using RHEL 6.3

Base Compiler Invocation

C benchmarks:

```
opencc
```

C++ benchmarks:

```
openCC
```

Fortran benchmarks:

```
openf95
```

Benchmarks using both Fortran and C:

```
opencc openf95
```

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
```

```
416.gamess: -DSPEC_CPU_LP64
```

```
433.milc: -DSPEC_CPU_LP64
```

```
434.zeusmp: -DSPEC_CPU_LP64
```

```
435.gromacs: -DSPEC_CPU_LP64
```

```
436.cactusADM: -DSPEC_CPU_LP64 -fno-second-underscore
```

```
437.leslie3d: -DSPEC_CPU_LP64
```

Continued on next page

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info@spec.org

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Page 3



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Base Portability Flags (Continued)

```

444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LINUX -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LP64
      -fno-second-underscore
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```

-Ofast -OPT:malloc_alg=1 -HP:bd=2m:heap=2m -IPA:plimit=8000
-IPA:small_pu=100 -mso -march=bdver1

```

C++ benchmarks:

```

-Ofast -static -CG:load_exe=0 -OPT:malloc_alg=1 -INLINE:aggressive=on
-HP:bd=2m:heap=2m -D__OPEN64_FAST_SET -march=bdver1

```

Fortran benchmarks:

```

-Ofast -LNO:blocking=off -LNO:simd_peel_align=on -OPT:rsqrt=2
-OPT:unroll_size=256 -HP:bd=2m:heap=2m -mso -march=bdver1

```

Benchmarks using both Fortran and C:

```

-Ofast -OPT:malloc_alg=1 -HP:bd=2m:heap=2m -IPA:plimit=8000
-IPA:small_pu=100 -mso -march=bdver1 -LNO:blocking=off
-LNO:simd_peel_align=on -OPT:rsqrt=2 -OPT:unroll_size=256

```

Peak Compiler Invocation

C benchmarks:

opencc

C++ benchmarks:

openCC

Fortran benchmarks:

openf95

Benchmarks using both Fortran and C:

opencc openf95



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Peak Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64
436.cactusADM: -DSPEC_CPU_LP64 -fno-second-underscore
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LINUX -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LP64
-fno-second-underscore
```

Peak Optimization Flags

C benchmarks:

```
433.milc: -Ofast -CG:movnti=1 -CG:locs_best=on -HP:bdt=2m:heap=2m
-IPA:plimit=7000 -IPA:callee_limit=1200
-OPT:struct_array_copy=2 -OPT:alias=field_sensitive -mso
-march=bdver1

470.lbm: -Ofast -CG:cmp_peep=on -OPT:keep_ext=on -HP:bdt=2m:heap=2m
-IPA:plimit=8000 -IPA:small_pu=100 -march=bdver1 -mso

482.sphinx3: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-m32 -IPA:plimit=1000 -OPT:malloc_alg=2 -CG:cmp_peep=on
-CG:p2align=0 -CG:load_exe=1 -CG:dsched=on
-INLINE:aggressive=on -LNO:prefetch=2 -LNO:prefetch_ahead=4
-mso -march=bdver2
```

C++ benchmarks:

```
444.namd: -Ofast -IPA:plimit=3000 -LNO:ignore_feedback=off
-CG:local_sched_alg=0 -CG:load_exe=0 -OPT:unroll_size=256
-fno-exceptions -HP:bdt=2m:heap=2m -LNO:if_select_conv=1
-OPT:alias=disjoint -LNO:psimd_iso_unroll=ON -march=bdver1

447.dealIII: -Ofast -D__OPEN64_FAST_SET -static -INLINE:aggressive=on
-LNO:opt=1 -LNO:simd=2 -fno-emit-exceptions -m32
-OPT:unroll_times_max=8 -OPT:unroll_size=256
-OPT:unroll_level=2 -HP:bdt=2m:heap=2m -GRA:unspill=on
-CG:cmp_peep=on -CG:movext_icmp=off -TENV:frame_pointer=off
-march=bdver1
```

Continued on next page



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Peak Optimization Flags (Continued)

450.soplex: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -O3
-LNO:ignore_feedback=off -INLINE:aggressive=on -OPT:RO=1
-OPT:IEEE_arith=3 -OPT:IEEE_NaN_Inf=off
-OPT:fold_unsigned_relops=on -fno-exceptions -CG:p2align=0
-m32 -mno-fma4 -HP:bdt=2m:heap=2m -WOPT:sib=on
-march=bdver1

453.povray: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-CG:pre_local_sched=off -CG:p2align=0 -CG:p2align_split=on
-CG:dsched=on -INLINE:aggressive=on -HP:bd=2m:heap=2m
-OPT:transform=2 -OPT:alias=disjoint -WOPT:aggcm=0
-march=bdver2

Fortran benchmarks:

410.bwaves: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-OPT:Ofast -OPT:treeheight=on -LNO:blocking=off
-LNO:ignore_feedback=off -LNO:fu=4 -LNO:loop_model_simd=on
-LNO:simd_rm_unity_remainder=on -WOPT:aggstr=0
-HP:bdt=2m:heap=2m -CG:cmp_peep=on -march=bdver1

416.gamess: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-LNO:fu=6 -LNO:blocking=0 -LNO:simd=2 -OPT:ro=3
-OPT:recip=on -CG:local_sched_alg=1 -HP:bdt=2m:heap=2m
-WOPT:sib=on -march=bdver1

434.zeusmp: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-LNO:blocking=off -LNO:interchange=off -IPA:plimit=1500
-HP:bdt=2m:heap=2m -march=bdver1

437.leslie3d: -Ofast -CG:pre_minreg_level=2 -LNO:simd=0 -LNO:fusion=2
-HP:bdt=2m:heap=2m -mso -march=bdver1

459.GemsFDTD: -Ofast -IPA:plimit=1500 -OPT:unroll_size=1024
-OPT:unroll_times_max=16 -LNO:fission=2
-CG:local_sched_alg=2 -HP -march=bdver1

465.tonto: -Ofast -OPT:alias=no_f90_pointer_alias -LNO:blocking=off
-CG:load_exe=1 -CG:local_sched_alg=3 -IPA:plimit=525
-HP:bdt=2m:heap=2m -march=bdver1

Benchmarks using both Fortran and C:

435.gromacs: -Ofast -OPT:rsqrt=2 -HP:bdt=2m:heap=2m
-CG:local_sched_alg=2 -CG:load_exe=3 -GRA:unspill=on
-march=bdver1 -LNO:simd=3

436.cactusADM: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-LNO:blocking=off -LNO:prefetch=2 -LNO:pf2=0
-LNO:prefetch_ahead=4 -HP -CG:locs_shallow_depth=1
-CG:load_exe=0 -CG:dsched=on -WOPT:sib=on -march=bdver1

Continued on next page



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Peak Optimization Flags (Continued)

454.calculix: -Ofast -OPT:unroll_size=256 -OPT:alias=disjoint
-GRA:optimize_boundary=on -CG:dsched=on -HP:bdtd=2m:heap=2m
-march=bdver1

481.wrf: -Ofast -LNO:blocking=off -LANG:copyinout=off
-IPA:callee_limit=5000 -GRA:prioritize_by_density=on -HP
-WOPT:sib=on -march=bdver1

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/x86-open64-452-flags-rate-revA-III.html>

<http://www.spec.org/cpu2006/flags/amd-Supermicro-platform-rate-revC-I.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/x86-open64-452-flags-rate-revA-III.xml>

<http://www.spec.org/cpu2006/flags/amd-Supermicro-platform-rate-revC-I.xml>

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For other inquiries, please contact webmaster@spec.org.

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