



SPEC® CFP2006 Result

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Hewlett-Packard Company

SPECfp®_rate2006 = 429

ProLiant DL360p Gen8
(2.60 GHz, Intel Xeon E5-2630 v2)

SPECfp_rate_base2006 = 423

CPU2006 license: 3

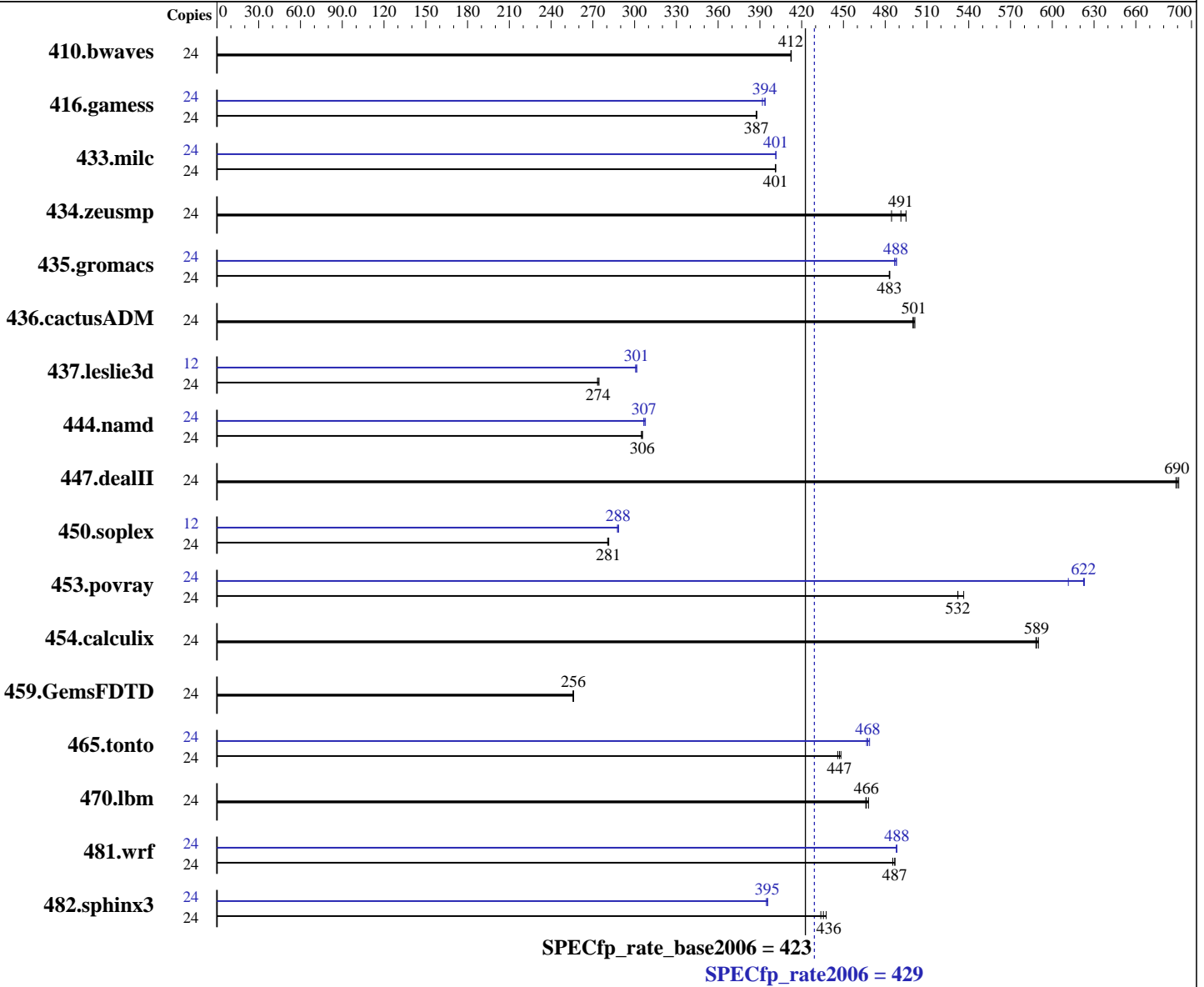
Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Oct-2013

Hardware Availability: Sep-2013

Software Availability: Sep-2013



Hardware

CPU Name: Intel Xeon E5-2630 v2
 CPU Characteristics: Intel Turbo Boost Technology up to 3.10 GHz
 CPU MHz: 2600
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)
 Kernel 2.6.32-358.el6.x86_64
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
 Auto Parallel: No
 File System: ext4

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L3 Cache: 15 MB I+D on chip per chip
Other Cache: None
Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC)
Disk Subsystem: 1 x 400 GB SAS SSD, RAID 1
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	24	<u>791</u>	<u>412</u>	791	413	791	412	24	<u>791</u>	<u>412</u>	791	413	791	412
416.gamess	24	1212	388	<u>1213</u>	<u>387</u>	1213	387	24	1199	392	1193	394	<u>1194</u>	<u>394</u>
433.milc	24	<u>549</u>	<u>401</u>	549	401	549	401	24	549	402	<u>549</u>	<u>401</u>	549	401
434.zeusmp	24	<u>445</u>	<u>491</u>	441	495	451	485	24	<u>445</u>	<u>491</u>	441	495	451	485
435.gromacs	24	355	483	355	483	<u>355</u>	<u>483</u>	24	<u>351</u>	<u>488</u>	352	487	351	488
436.cactusADM	24	<u>573</u>	<u>501</u>	574	500	572	501	24	<u>573</u>	<u>501</u>	574	500	572	501
437.leslie3d	24	822	274	<u>824</u>	<u>274</u>	825	273	12	<u>374</u>	<u>301</u>	375	301	374	302
444.namd	24	<u>630</u>	<u>306</u>	630	306	631	305	24	628	307	625	308	<u>627</u>	<u>307</u>
447.dealII	24	399	689	397	691	<u>398</u>	<u>690</u>	24	399	689	397	691	<u>398</u>	<u>690</u>
450.soplex	24	711	281	713	281	<u>712</u>	<u>281</u>	12	<u>347</u>	<u>288</u>	348	288	347	288
453.povray	24	<u>240</u>	<u>532</u>	238	536	240	532	24	209	612	205	623	<u>205</u>	<u>622</u>
454.calculix	24	336	590	<u>336</u>	<u>589</u>	337	588	24	336	590	<u>336</u>	<u>589</u>	337	588
459.GemsFDTD	24	<u>995</u>	<u>256</u>	996	256	994	256	24	<u>995</u>	<u>256</u>	996	256	994	256
465.tonto	24	530	446	527	448	<u>528</u>	<u>447</u>	24	<u>505</u>	<u>468</u>	506	467	504	469
470.lbm	24	<u>707</u>	<u>466</u>	708	466	705	468	24	<u>707</u>	<u>466</u>	708	466	705	468
481.wrf	24	<u>551</u>	<u>487</u>	550	487	552	485	24	549	488	<u>549</u>	<u>488</u>	549	488
482.sphinx3	24	1078	434	<u>1074</u>	<u>436</u>	1069	438	24	1185	395	<u>1183</u>	<u>395</u>	1182	396

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
Disabled unused Linux services through "stop_services.sh" before running.



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Platform Notes

BIOS Configuration:

HP Power Profile set to Maximum Performance
Memory Power Savings Mode set to Maximum Performance
Collaborative Power Control set to Disabled
Dynamic Power Capping Functionality set to Disabled
Thermal Configuration set to Maximum Cooling
Processor Power and Utilization Monitoring set to Disabled
Memory Refresh Rate set to 1x

Sysinfo program /cpu2006/config/sysinfo.rev6818
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191
running on ML350pCPU20062 Fri Oct 25 22:26:44 2013

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) CPU E5-2630 v2 @ 2.60GHz
 2 "physical id"s (chips)
 24 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores      : 6
  siblings       : 12
  physical 0:    cores 0 1 2 3 4 5
  physical 1:    cores 0 1 2 3 4 5
cache size      : 15360 KB
```

From /proc/meminfo

```
MemTotal:      132119284 kB
HugePages_Total: 0
Hugepagesize:   2048 kB
```

/usr/bin/lsb_release -d

Red Hat Enterprise Linux Server release 6.4 (Santiago)

From /etc/*release* /etc/*version*

```
redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

uname -a:

```
Linux ML350pCPU20062 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41 EST 2013 x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Oct 25 11:01

SPEC is set to: /cpu2006

```
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/sda3        ext4      365G  209G  139G   61% /
Continued on next page
```



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Platform Notes (Continued)

Additional information from dmidecode:

BIOS HP P71 09/08/2013

Memory:

16x HP 689911-071 8 GB 1600 MHz 2 rank

8x UNKNOWN NOT AVAILABLE

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 128 GB and the dmidecode description should have one line reading as:

16x HP 689911-071 8 GB 1600 MHz 2 rank

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/cpu2006/libs/32:/cpu2006/libs/64:/cpu2006/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64

416.gamess: -DSPEC_CPU_LP64

433.milc: -DSPEC_CPU_LP64

434.zeusmp: -DSPEC_CPU_LP64

435.gromacs: -DSPEC_CPU_LP64 -nofor_main

436.cactusADM: -DSPEC_CPU_LP64 -nofor_main

437.leslie3d: -DSPEC_CPU_LP64

444.namd: -DSPEC_CPU_LP64

447.dealII: -DSPEC_CPU_LP64

450.soplex: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

```

453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:
 -xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
 -opt-mem-layout-trans=3

C++ benchmarks:
 -xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
 -opt-mem-layout-trans=3

Fortran benchmarks:
 -xAVX -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:
 -xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
 -opt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks (except as noted below):
 icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):
 icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:
 ifort -m64

Benchmarks using both Fortran and C:
 icc -m64 ifort -m64



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Software Availability: Sep-2013

Peak Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

```

Peak Optimization Flags

C benchmarks:

```

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
         -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
         -prof-use(pass 2) -auto-ilp32

```

```

470.lbm: basepeak = yes

```

```

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -opt-mem-layout-trans=3
            -unroll2

```

C++ benchmarks:

```

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
         -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
         -prof-use(pass 2) -fno-alias -auto-ilp32

```

```

447.dealII: basepeak = yes

```

```

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
         -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
         -prof-use(pass 2) -opt-malloc-options=3

```

```

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
         -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
         -prof-use(pass 2) -unroll4 -ansi-alias

```

Fortran benchmarks:

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Peak Optimization Flags (Continued)

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xAVX -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revB.20131009.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revB.20131009.xml>

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Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 18:14:11 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 3 December 2013.