



SPEC® CFP2006 Result

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Fujitsu

SPECfp®_rate2006 = 394

PRIMERGY RX200 S8, Intel Xeon E5-2637 v2, 3.50 GHz

SPECfp_rate_base2006 = 383

CPU2006 license: 19

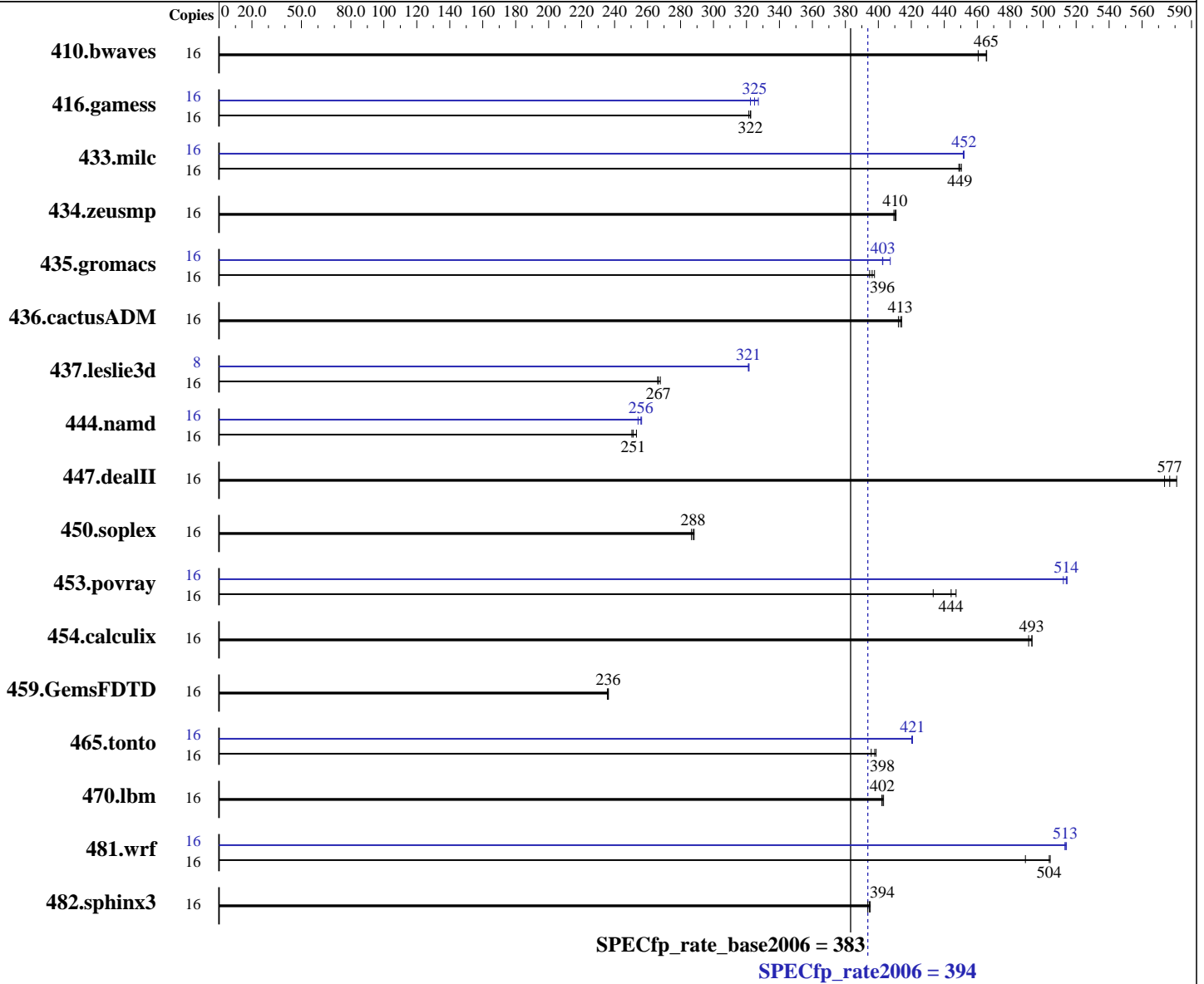
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Sep-2013

Hardware Availability: Oct-2013

Software Availability: Sep-2013



Hardware

CPU Name: Intel Xeon E5-2637 v2
 CPU Characteristics: Intel Turbo Boost Technology up to 3.80 GHz
 CPU MHz: 3500
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)
 2.6.32-358.11.1.el6.x86_64
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
 Auto Parallel: No
 File System: ext4

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L3 Cache: 15 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC)
 Disk Subsystem: 1 x SATA, 450 GB, 15000 RPM
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	472	461	<u>467</u>	<u>465</u>	467	466	16	472	461	<u>467</u>	<u>465</u>	467	466
416.gamess	16	971	323	<u>972</u>	<u>322</u>	975	321	16	957	327	972	322	<u>964</u>	<u>325</u>
433.milc	16	327	449	326	450	<u>327</u>	<u>449</u>	16	<u>325</u>	<u>452</u>	325	452	325	452
434.zeusmp	16	<u>355</u>	<u>410</u>	356	409	354	411	16	<u>355</u>	<u>410</u>	356	409	354	411
435.gromacs	16	<u>288</u>	<u>396</u>	287	398	289	395	16	284	403	281	407	<u>284</u>	<u>403</u>
436.cactusADM	16	<u>463</u>	<u>413</u>	464	412	462	414	16	<u>463</u>	<u>413</u>	464	412	462	414
437.leslie3d	16	562	268	<u>564</u>	<u>267</u>	565	266	8	234	322	<u>234</u>	<u>321</u>	234	321
444.namd	16	<u>511</u>	<u>251</u>	507	253	512	250	16	505	254	<u>501</u>	<u>256</u>	500	256
447.dealII	16	315	581	319	574	<u>317</u>	<u>577</u>	16	315	581	319	574	<u>317</u>	<u>577</u>
450.soplex	16	463	288	466	287	<u>464</u>	<u>288</u>	16	463	288	466	287	<u>464</u>	<u>288</u>
453.povray	16	<u>192</u>	<u>444</u>	196	433	190	447	16	166	512	<u>166</u>	<u>514</u>	165	515
454.calculix	16	268	493	269	491	<u>268</u>	<u>493</u>	16	268	493	269	491	<u>268</u>	<u>493</u>
459.GemsFDTD	16	718	236	721	236	<u>719</u>	<u>236</u>	16	718	236	721	236	<u>719</u>	<u>236</u>
465.tonto	16	395	399	<u>396</u>	<u>398</u>	398	396	16	374	421	375	420	<u>374</u>	<u>421</u>
470.lbm	16	<u>546</u>	<u>402</u>	545	403	547	402	16	<u>546</u>	<u>402</u>	545	403	547	402
481.wrf	16	365	489	354	504	<u>355</u>	<u>504</u>	16	348	514	<u>348</u>	<u>513</u>	348	513
482.sphinx3	16	789	395	792	394	<u>790</u>	<u>394</u>	16	789	395	792	394	<u>790</u>	<u>394</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Energy Performance = Performance



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General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64:/SPECcpu2006/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

For information about Fujitsu please visit: <http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64



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Base Optimization Flags

C benchmarks:

`-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3`

C++ benchmarks:

`-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3`

Fortran benchmarks:

`-xAVX -ipo -O3 -no-prec-div -opt-prefetch`

Benchmarks using both Fortran and C:

`-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3`

Peak Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

`433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -auto-ilp32`

`470.lbm: basepeak = yes`

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Peak Optimization Flags (Continued)

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xAVX -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20131009.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20131009.xml>

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