



# SPEC® CFP2006 Result

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## IBM Corporation

SPECfp®\_rate2006 = 689

IBM System x iDataPlex dx360 M4  
(Intel Xeon E5-2697 v2, 2.70 GHz)

SPECfp\_rate\_base2006 = 669

CPU2006 license: 11

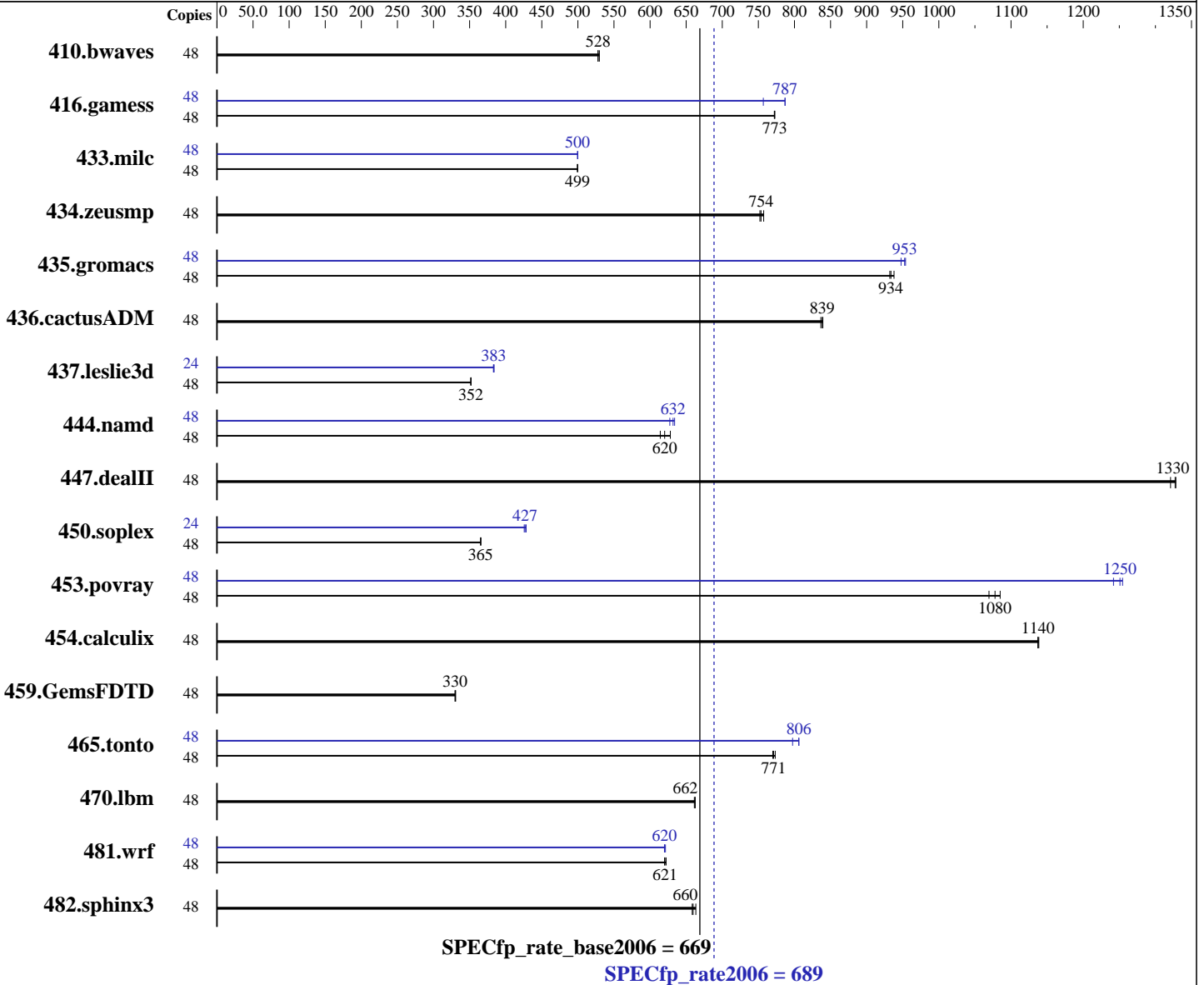
Test date: Aug-2013

Test sponsor: IBM Corporation

Hardware Availability: Dec-2013

Tested by: IBM Corporation

Software Availability: Sep-2013



### Hardware

CPU Name: Intel Xeon E5-2697 v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz  
 CPU MHz: 2700  
 FPU: Integrated  
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)  
 2.6.32-358.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;  
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4

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L3 Cache: 30 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC)  
Disk Subsystem: 1 x 500 GB SATA, 7200 RPM  
Other Hardware: None

System State: Run level 3 (multi-user)  
Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	48	<u>1235</u>	<u>528</u>	1231	530	1236	528	48	<u>1235</u>	<u>528</u>	1231	530	1236	528
416.gamess	48	1217	772	1216	773	<u>1216</u>	<u>773</u>	48	1242	757	<u>1195</u>	<u>787</u>	1194	787
433.milc	48	<u>882</u>	<u>499</u>	882	500	882	499	48	882	500	882	500	<u>882</u>	<u>500</u>
434.zeusmp	48	<u>580</u>	<u>754</u>	577	757	581	752	48	<u>580</u>	<u>754</u>	577	757	581	752
435.gromacs	48	365	938	368	932	<u>367</u>	<u>934</u>	48	362	948	359	954	<u>360</u>	<u>953</u>
436.cactusADM	48	683	839	<u>684</u>	<u>839</u>	686	837	48	683	839	<u>684</u>	<u>839</u>	686	837
437.leslie3d	48	1282	352	1283	352	<u>1283</u>	<u>352</u>	24	589	383	<u>588</u>	<u>383</u>	588	384
444.namd	48	627	614	613	628	<u>621</u>	<u>620</u>	48	607	634	614	627	<u>609</u>	<u>632</u>
447.dealII	48	<u>414</u>	<u>1330</u>	413	1330	416	1320	48	<u>414</u>	<u>1330</u>	413	1330	416	1320
450.soplex	48	<u>1096</u>	<u>365</u>	1095	365	1096	365	24	467	428	470	426	<u>469</u>	<u>427</u>
453.povray	48	239	1070	235	1080	<u>237</u>	<u>1080</u>	48	204	1250	<u>204</u>	<u>1250</u>	206	1240
454.calculix	48	<u>348</u>	<u>1140</u>	348	1140	348	1140	48	<u>348</u>	<u>1140</u>	348	1140	348	1140
459.GemsFDTD	48	1540	331	<u>1543</u>	<u>330</u>	1544	330	48	1540	331	<u>1543</u>	<u>330</u>	1544	330
465.tonto	48	611	773	613	770	<u>613</u>	<u>771</u>	48	592	797	586	806	<u>586</u>	<u>806</u>
470.lbm	48	<u>996</u>	<u>662</u>	997	661	995	663	48	<u>996</u>	<u>662</u>	997	661	995	663
481.wrf	48	865	620	<u>864</u>	<u>621</u>	862	622	48	<u>865</u>	<u>620</u>	865	620	863	621
482.sphinx3	48	1410	663	1421	658	<u>1418</u>	<u>660</u>	48	1410	663	1421	658	<u>1418</u>	<u>660</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Zone reclaim mode enabled with:  
echo 1 > /proc/sys/vm/zone\_reclaim\_mode



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**Test date:** Aug-2013  
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### Platform Notes

BIOS setting:  
Operating Mode set to Maximum Performance  
Sysinfo program /home/SPECcpu-new/config/sysinfo.rev6818  
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191  
running on td-2 Fri Aug 30 12:44:18 2013

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name      : Intel(R) Xeon(R) CPU E5-2697 v2 @ 2.70GHz
 2 "physical id"s (chips)
 48 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores      : 12
  siblings       : 24
  physical 0:    cores 0 1 2 3 4 5 8 9 10 11 12 13
  physical 1:    cores 0 1 2 3 4 5 8 9 10 11 12 13
cache size      : 30720 KB
```

```
From /proc/meminfo
MemTotal:        264648596 kB
HugePages_Total: 0
Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.4 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux td-2 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41 EST 2013 x86_64
x86_64 x86_64 GNU/Linux
```

```
run-level 3 Aug 29 22:38
```

```
SPEC is set to: /home/SPECcpu-new
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/mapper/vg_td2-lv_home
ext4             380G    200G  161G  56% /home
```

```
Additional information from dmidecode:
BIOS IBM      -[TDE133HT2-1.30]- 08/27/2013
Memory:
16x Micron 36JSF2G72PZ-1G9E1 16 GB 1867 MHz 2 rank
```

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## Platform Notes (Continued)

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/home/SPECcpu-new/libs/32:/home/SPECcpu-new/libs/64:/home/SPECcpu-new/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB  
memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop\_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64

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## Base Portability Flags (Continued)

470.ibm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias  
-opt-mem-layout-trans=3

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias  
-opt-mem-layout-trans=3

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias  
-opt-mem-layout-trans=3

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

```

435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Peak Optimization Flags

C benchmarks:

```

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -auto-ilp32

```

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

```

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -fno-alias -auto-ilp32

```

447.dealII: basepeak = yes

```

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-malloc-options=3

```

```

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -unroll4 -ansi-alias

```

Fortran benchmarks:

410.bwaves: basepeak = yes

```

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

```

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## Peak Optimization Flags (Continued)

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto  
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xAVX -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>  
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>  
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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