



SPEC[®] CFP2006 Result

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Hewlett-Packard Company

SPECfp[®]_rate2006 = 76.9

ProLiant ML10 Gen8
(3.20 GHz, Intel Pentium G2130)

SPECfp_rate_base2006 = 75.4

CPU2006 license: 3

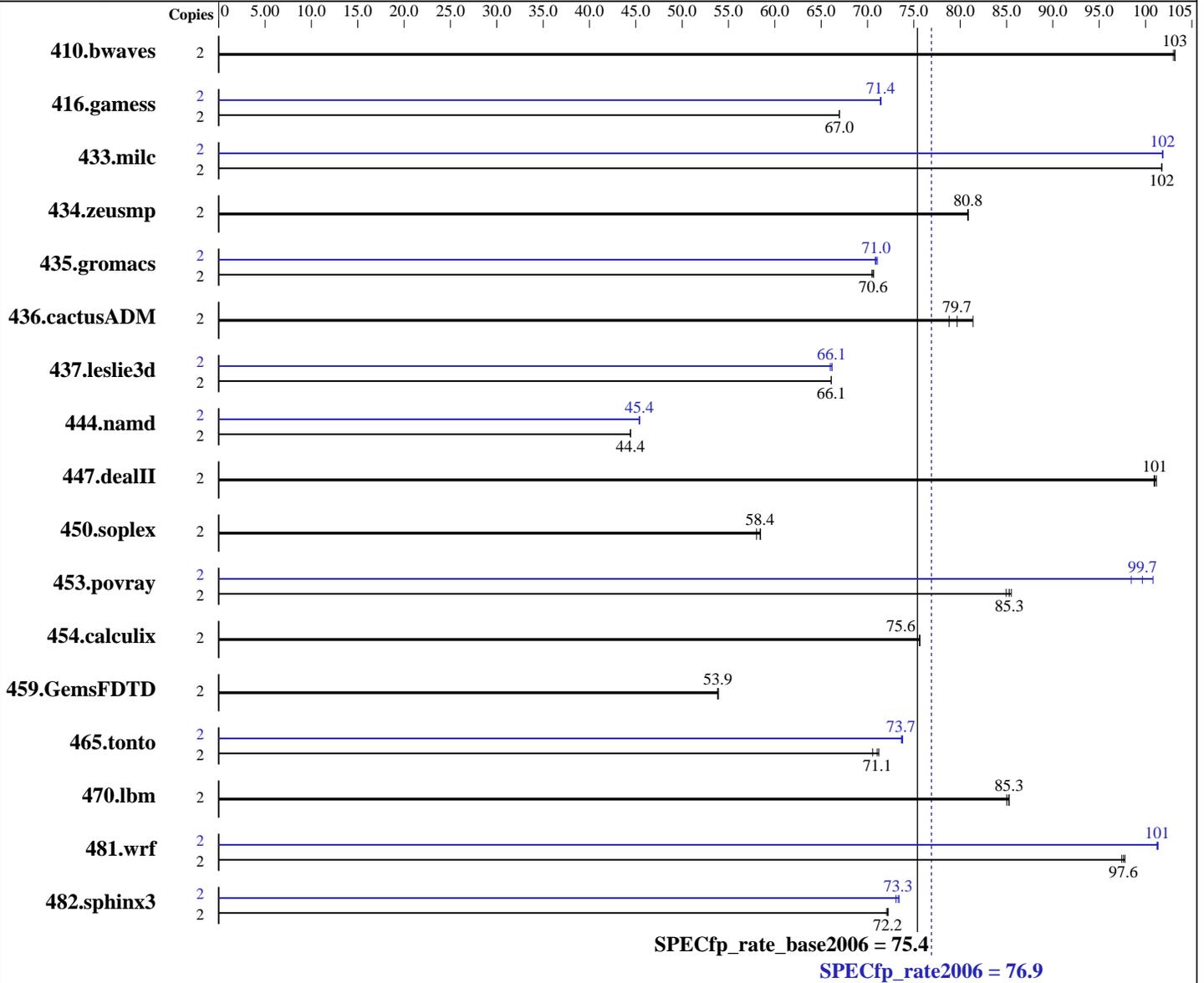
Test date: Jul-2013

Test sponsor: Hewlett-Packard Company

Hardware Availability: Aug-2013

Tested by: Hewlett-Packard Company

Software Availability: Feb-2013



Hardware

CPU Name: Intel Pentium G2130
 CPU Characteristics:
 CPU MHz: 3200
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)
 2.6.32-358.el6.x86_64
 Compiler: C/C++: Version 13.1.1.163 of Intel C++ Studio XE for Linux;
 Fortran: Version 13.1.1.163 of Intel Fortran Studio XE for Linux
 Auto Parallel: No
 File System: ext4

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L3 Cache: 3 MB I+D on chip per chip
Other Cache: None
Memory: 16 GB (2 x 8 GB 2Rx8 PC3-12800E-11, ECC)
Disk Subsystem: 1 x 250 GB 7.2 K SATA
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	2	<u>263</u>	<u>103</u>	264	103	263	103	2	<u>263</u>	<u>103</u>	264	103	263	103
416.gamess	2	<u>585</u>	<u>67.0</u>	585	67.0	585	67.0	2	548	71.4	548	71.5	<u>548</u>	<u>71.4</u>
433.milc	2	<u>180</u>	<u>102</u>	180	102	180	102	2	180	102	<u>180</u>	<u>102</u>	180	102
434.zeusmp	2	<u>225</u>	<u>80.8</u>	225	80.8	225	80.9	2	<u>225</u>	<u>80.8</u>	225	80.8	225	80.9
435.gromacs	2	202	70.7	203	70.5	<u>202</u>	<u>70.6</u>	2	<u>201</u>	<u>71.0</u>	201	71.1	202	70.8
436.cactusADM	2	303	78.8	<u>300</u>	<u>79.7</u>	294	81.4	2	303	78.8	<u>300</u>	<u>79.7</u>	294	81.4
437.leslie3d	2	285	66.1	284	66.1	<u>284</u>	<u>66.1</u>	2	<u>284</u>	<u>66.1</u>	284	66.2	285	66.0
444.namd	2	361	44.5	<u>361</u>	<u>44.4</u>	361	44.4	2	<u>353</u>	<u>45.4</u>	353	45.4	353	45.4
447.dealII	2	226	101	227	101	<u>227</u>	<u>101</u>	2	226	101	227	101	<u>227</u>	<u>101</u>
450.soplex	2	<u>286</u>	<u>58.4</u>	287	58.0	285	58.5	2	<u>286</u>	<u>58.4</u>	287	58.0	285	58.5
453.povray	2	<u>125</u>	<u>85.3</u>	124	85.5	125	85.0	2	106	101	<u>107</u>	<u>99.7</u>	108	98.5
454.calculix	2	218	75.6	218	75.7	<u>218</u>	<u>75.6</u>	2	218	75.6	218	75.7	<u>218</u>	<u>75.6</u>
459.GemsFDTD	2	394	53.9	<u>394</u>	<u>53.9</u>	394	53.8	2	394	53.9	<u>394</u>	<u>53.9</u>	394	53.8
465.tonto	2	279	70.5	<u>277</u>	<u>71.1</u>	276	71.2	2	267	73.8	<u>267</u>	<u>73.7</u>	267	73.7
470.lbm	2	<u>322</u>	<u>85.3</u>	323	85.0	322	85.3	2	<u>322</u>	<u>85.3</u>	323	85.0	322	85.3
481.wrf	2	228	97.8	229	97.4	<u>229</u>	<u>97.6</u>	2	220	101	<u>221</u>	<u>101</u>	221	101
482.sphinx3	2	<u>540</u>	<u>72.2</u>	540	72.2	541	72.1	2	533	73.1	531	73.4	<u>532</u>	<u>73.3</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>



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Platform Notes

BIOS Configuration:

HP Power Profile set to Maximum Performance
Collaborative Power Control set to Disabled
Thermal Configuration set to Increased Cooling
Processor Power and Utilization Monitoring set to Disabled
Memory Refresh Rate set to 1x Refresh

Sysinfo program /cpu2006/config/sysinfo.rev6818
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191
running on ml10 Wed Jun 26 06:44:03 2013

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
  model name : Intel(R) Pentium(R) CPU G2130 @ 3.20GHz
    1 "physical id"s (chips)
    2 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The
  following excerpts from /proc/cpuinfo might not be reliable. Use with
  caution.)
    cpu cores : 2
    siblings  : 2
    physical 0: cores 0 1
  cache size : 3072 KB
```

```
From /proc/meminfo
MemTotal:      16414924 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.4 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux ml10 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41 EST 2013 x86_64
x86_64 x86_64 GNU/Linux
```

```
run-level 3 Jun 26 00:09
```

```
SPEC is set to: /cpu2006
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/sda3        ext4      228G  12G  204G   6% /
```

Additional information from dmidecode:
BIOS HP P88 06/26/2013

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Platform Notes (Continued)

Memory:
2x Not Specified Not Specified
2x Not Specified Not Specified 8 GB 1600 MHz 2 rank

(End of data from sysinfo program)
Regarding the sysinfo display about the memory installed, the correct amount of memory is 16 GB and the dmidecode description should have one line reading as:
2x Not Specified Not Specified 8 GB 1600 MHz 2 rank

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/cpu2006/libs/32:/cpu2006/libs/64:/cpu2006/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main

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Base Portability Flags (Continued)

459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

```

433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

```

Peak Optimization Flags

C benchmarks:

```

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -static -auto-ilp32

```

470.lbm: basepeak = yes

```

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -opt-mem-layout-trans=3
-unroll2

```

C++ benchmarks:

```

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -fno-alias -auto-ilp32

```

447.dealII: basepeak = yes

450.soplex: basepeak = yes

```

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -unroll4 -ansi-alias

```

Fortran benchmarks:

410.bwaves: basepeak = yes

```

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

```

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Peak Optimization Flags (Continued)

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -static -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic13-official-linux64.20130702.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-A.20130813.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic13-official-linux64.20130702.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-A.20130813.xml>

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