



SPEC® CINT2006 Result

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Supermicro

SuperServer 1018D-73MTF
(X10SL7-F, Intel Xeon E3-1220 v3)

SPECint®2006 = 57.2

SPECint_base2006 = 55.0

CPU2006 license: 001176

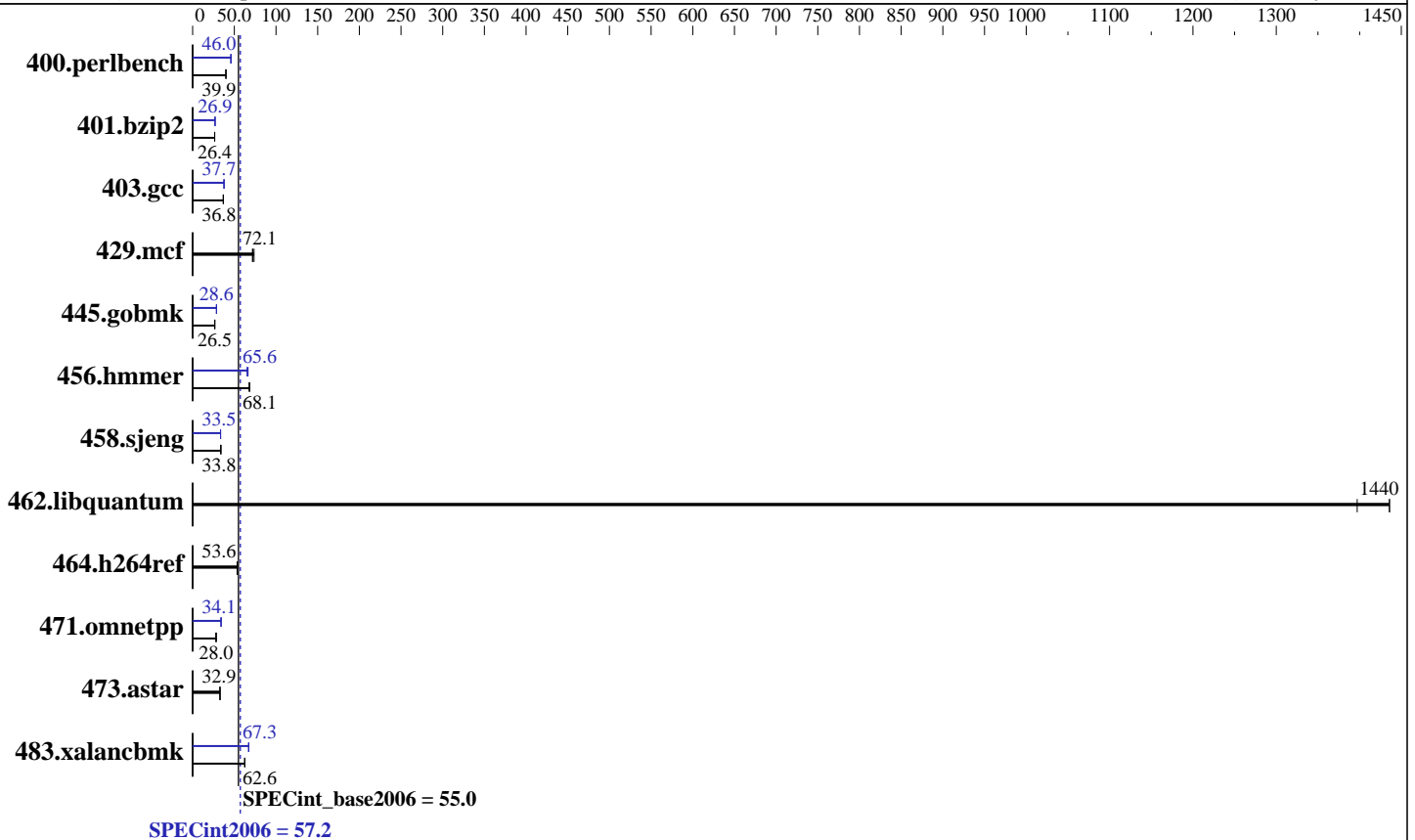
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Jun-2013

Hardware Availability: Jun-2013

Software Availability: May-2013



Hardware

CPU Name: Intel Xeon E3-1220 v3
 CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz
 CPU MHz: 3100
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 16 GB (2 x 8 GB 2Rx8 PC3-12800E-11, ECC)
 Disk Subsystem: 2 x 600 GB SAS 6Gbps, RAID 1, 10000 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)
 Kernel 2.6.32-358.el6.x86_64
 Compiler: C/C++: Version 13.1.1.163 of Intel C++ Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.0



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	244	40.0	<u>245</u>	<u>39.9</u>	245	39.9	<u>213</u>	<u>46.0</u>	213	46.0	213	45.9
401.bzip2	365	26.5	365	26.4	<u>365</u>	<u>26.4</u>	359	26.9	359	26.9	<u>359</u>	<u>26.9</u>
403.gcc	218	36.9	219	36.7	<u>219</u>	<u>36.8</u>	<u>214</u>	<u>37.7</u>	214	37.6	214	37.7
429.mcf	127	71.8	<u>127</u>	<u>72.1</u>	124	73.7	127	71.8	<u>127</u>	<u>72.1</u>	124	73.7
445.gobmk	396	26.5	<u>395</u>	<u>26.5</u>	394	26.6	367	28.6	367	28.6	<u>367</u>	<u>28.6</u>
456.hammer	137	67.9	<u>137</u>	<u>68.1</u>	136	68.4	<u>142</u>	<u>65.6</u>	143	65.4	140	66.5
458.sjeng	<u>358</u>	<u>33.8</u>	357	33.9	358	33.8	<u>361</u>	<u>33.5</u>	361	33.5	361	33.5
462.libquantum	14.4	1440	14.8	1400	<u>14.4</u>	<u>1440</u>	14.4	1440	14.8	1400	<u>14.4</u>	<u>1440</u>
464.h264ref	412	53.7	415	53.3	<u>413</u>	<u>53.6</u>	412	53.7	415	53.3	<u>413</u>	<u>53.6</u>
471.omnetpp	<u>223</u>	<u>28.0</u>	224	27.9	219	28.5	183	34.1	184	34.0	<u>183</u>	<u>34.1</u>
473.astar	213	32.9	215	32.7	<u>214</u>	<u>32.9</u>	213	32.9	215	32.7	<u>214</u>	<u>32.9</u>
483.xalancbmk	110	62.7	111	62.4	<u>110</u>	<u>62.6</u>	<u>103</u>	<u>67.3</u>	103	67.1	102	67.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/home/cpu2006_IC13.1/libs/32:/home/cpu2006_IC13.1/libs/64:/home/cpu2006_IC13.1/sh"
OMP_NUM_THREADS = "4"
```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

C++ benchmarks (except as noted below):
icpc -m32

473.astar: icpc -m64



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Peak Portability Flags

```

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

```

Peak Optimization Flags

C benchmarks:

```

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -opt-prefetch -ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
           -opt-prefetch -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
         -opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
          -ansi-alias

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
          -ansi-alias

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
           -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

```

C++ benchmarks:

```

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
             -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
             -opt-ra-region-strategy=block -ansi-alias
             -Wl,-z,muldefs -L/sh -lsmartheap

```

Continued on next page



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Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic13-official-linux64.20130702.html>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revB.20130719.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic13-official-linux64.20130702.xml>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revB.20130719.xml>

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For other inquiries, please contact webmaster@spec.org.

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