



# SPEC® CINT2006 Result

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Itaotec

SPECint®\_rate2006 = 224

Servidor Itaotec MX215 (Intel Xeon E5-2609)

SPECint\_rate\_base2006 = 215

CPU2006 license: 9001

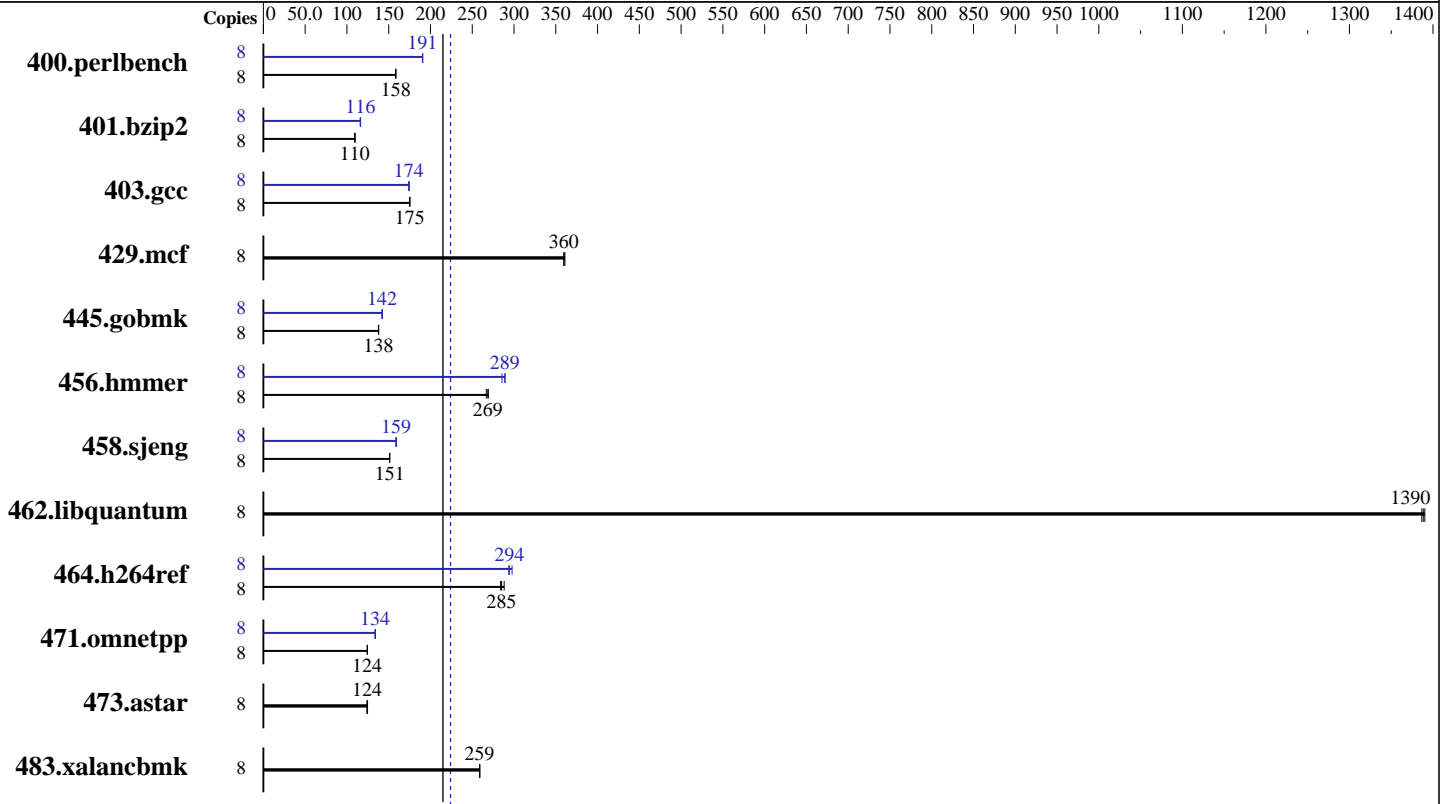
Test sponsor: Itaotec

Tested by: Itaotec

Test date: Nov-2012

Hardware Availability: Jun-2012

Software Availability: Jun-2012



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## Hardware

CPU Name: Intel Xeon E5-2609  
 CPU Characteristics:  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 10 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 64 GB (8 x 8 GB 2Rx8 PC3-10600R-9, ECC, running at 1066 MHz and CL7)  
 Disk Subsystem: 1 TB, SATA-2, 7200 RPM  
 Other Hardware: None

## Software

Operating System: Red Hat Enterprise Linux Server release 6.3, 2.6.32-279.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0 of Intel Compiler XE Build 20111011  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1



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## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
400.perlbench	8	494	158	493	159	<b>493</b>	<b>158</b>	8	<b>410</b>	<b>191</b>	410	191	410	191		
401.bzip2	8	704	110	<b>705</b>	<b>110</b>	706	109	8	666	116	663	116	<b>665</b>	<b>116</b>		
403.gcc	8	368	175	<b>367</b>	<b>175</b>	367	175	8	369	175	370	174	<b>370</b>	<b>174</b>		
429.mcf	8	203	360	202	361	<b>203</b>	<b>360</b>	8	203	360	202	361	<b>203</b>	<b>360</b>		
445.gobmk	8	610	138	608	138	<b>609</b>	<b>138</b>	8	<b>591</b>	<b>142</b>	591	142	589	142		
456.hammer	8	277	269	<b>278</b>	<b>269</b>	280	267	8	258	289	261	286	<b>258</b>	<b>289</b>		
458.sjeng	8	641	151	<b>640</b>	<b>151</b>	640	151	8	608	159	610	159	<b>608</b>	<b>159</b>		
462.libquantum	8	120	1390	119	1390	<b>119</b>	<b>1390</b>	8	120	1390	119	1390	<b>119</b>	<b>1390</b>		
464.h264ref	8	614	288	624	284	<b>621</b>	<b>285</b>	8	603	294	<b>601</b>	<b>294</b>	595	298		
471.omnetpp	8	<b>402</b>	<b>124</b>	402	124	402	124	8	373	134	<b>374</b>	<b>134</b>	374	134		
473.astar	8	453	124	450	125	<b>451</b>	<b>124</b>	8	453	124	450	125	<b>451</b>	<b>124</b>		
483.xalancbmk	8	213	259	<b>213</b>	<b>259</b>	213	259	8	213	259	<b>213</b>	<b>259</b>	213	259		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.  
Large pages were not enabled for this run

## Platform Notes

Sysinfo program /home/rcaneca/cpu2006/Docs/sysinfo  
\$Rev: 6775 \$ \$Date:: 2011-08-16 #\$ 8787f7622badcf24e01c368b1db4377c  
running on mx225sp Tue Nov 29 20:20:09 2011

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) CPU E5-2609 0 @ 2.40GHz  
2 "physical id"s (chips)  
8 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 4  
siblings : 4

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## Platform Notes (Continued)

```
physical 0: cores 0 1 2 3
physical 1: cores 0 1 2 3
cache size : 10240 KB
```

```
From /proc/meminfo
MemTotal:      65881720 kB
HugePages_Total: 0
Hugepagesize:  2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.3 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.3 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.3 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux mx225sp 2.6.32-279.el6.x86_64 #1 SMP Wed Jun 13 18:24:36 EDT 2012
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Nov 29 19:30
```

```
SPEC is set to: /home/rcaneca/cpu2006
Filesystem      Type      Size Used Avail Use% Mounted on
/dev/mapper/vg_mx225sp-lv_home
ext4            836G  2.1G  791G   1% /home
```

(End of data from sysinfo program)

## General Notes

This result was measured on the Servidor Itautec MX225.  
The Servidor Itautec MX225 and the Servidor Itautec MX215  
are electronically equivalent.

## Base Compiler Invocation

C benchmarks:  
icc -m32

C++ benchmarks:  
icpc -m32



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
C++ benchmarks:  
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
-Wl,-z,muldefs -L/home/rcaneca/sh/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32  
400.perlbench: icc -m64  
401.bzip2: icc -m64  
456.hmmer: icc -m64  
458.sjeng: icc -m64  
C++ benchmarks:  
icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32  
401.bzp2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch  
-auto-ilp32 -ansi-alias  
403.gcc: -xAVX -ipo -O3 -no-prec-div  
429.mcf: basepeak = yes  
445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -opt-mem-layout-trans=3  
456.hmmcr: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4  
-auto-ilp32  
462.libquantum: basepeak = yes  
464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias  
-opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/home/rcaneca/sh/SmartHeap\_8.1/lib -lsmartheap  
473.astar: basepeak = yes  
483.xalancbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

[http://www.spec.org/cpu2006/flags/Itaotec-Servidor\\_Itaotec-Intel-Linux-Platform.html](http://www.spec.org/cpu2006/flags/Itaotec-Servidor_Itaotec-Intel-Linux-Platform.html)  
<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

You can also download the XML flags sources by saving the following links:

[http://www.spec.org/cpu2006/flags/Itaotec-Servidor\\_Itaotec-Intel-Linux-Platform.xml](http://www.spec.org/cpu2006/flags/Itaotec-Servidor_Itaotec-Intel-Linux-Platform.xml)  
<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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