



# SPEC<sup>®</sup> CFP2006 Result

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## IBM Corporation

IBM System x3250 M4  
(Intel Core i3-3240, 3.40 GHz)

SPECfp<sup>®</sup>2006 = 55.4

SPECfp\_base2006 = 54.2

CPU2006 license: 11

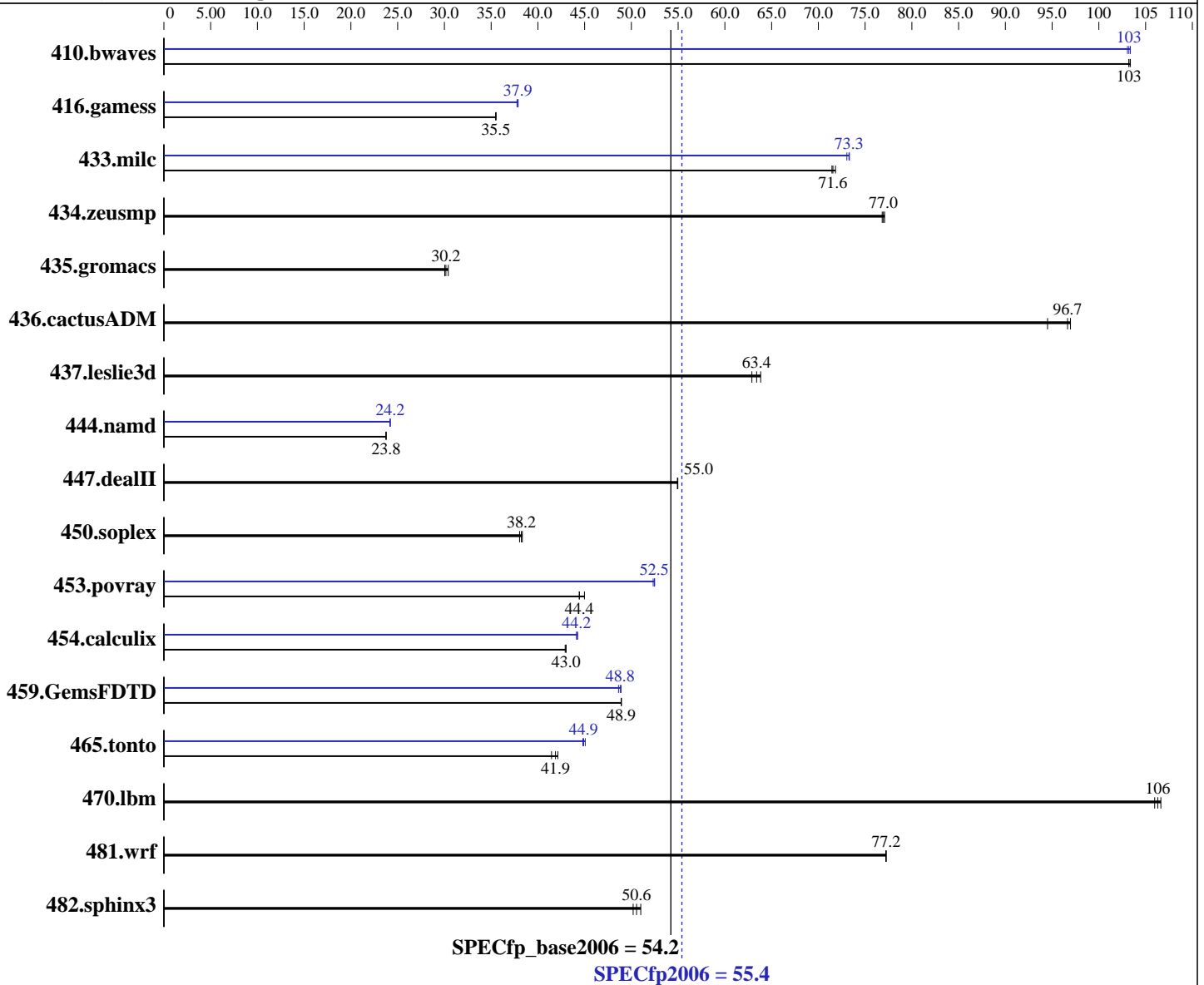
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Oct-2012

Hardware Availability: Sep-2012

Software Availability: Dec-2011



### Hardware

CPU Name: Intel Core i3-3240  
 CPU Characteristics:  
 CPU MHz: 3400  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)  
 2.6.32-220.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;  
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4

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L3 Cache: 3 MB I+D on chip per chip  
Other Cache: None  
Memory: 16 GB (2 x 8 GB 2Rx8 PC3-12800E-11, ECC)  
Disk Subsystem: 1 x 146 GB SAS, 15000 RPM  
Other Hardware: None

System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	131	103	132	103	<u>132</u>	<u>103</u>	131	103	<u>132</u>	<u>103</u>	132	103
416.gamess	552	35.5	551	35.5	<u>552</u>	<u>35.5</u>	518	37.8	<u>517</u>	<u>37.9</u>	517	37.9
433.milc	128	71.5	<u>128</u>	<u>71.6</u>	128	71.8	125	73.3	<u>125</u>	<u>73.3</u>	126	73.0
434.zeusmp	118	77.1	118	76.8	<u>118</u>	<u>77.0</u>	118	77.1	118	76.8	<u>118</u>	<u>77.0</u>
435.gromacs	<u>237</u>	<u>30.2</u>	238	30.0	235	30.4	<u>237</u>	<u>30.2</u>	238	30.0	235	30.4
436.cactusADM	123	97.0	126	94.5	<u>124</u>	<u>96.7</u>	123	97.0	126	94.5	<u>124</u>	<u>96.7</u>
437.leslie3d	147	63.8	<u>148</u>	<u>63.4</u>	149	62.9	147	63.8	<u>148</u>	<u>63.4</u>	149	62.9
444.namd	337	23.8	<u>338</u>	<u>23.8</u>	338	23.8	332	24.2	<u>331</u>	<u>24.2</u>	331	24.2
447.dealII	208	54.9	208	55.0	<u>208</u>	<u>55.0</u>	208	54.9	208	55.0	<u>208</u>	<u>55.0</u>
450.soplex	219	38.0	<u>218</u>	<u>38.2</u>	218	38.3	219	38.0	<u>218</u>	<u>38.2</u>	218	38.3
453.povray	<u>120</u>	<u>44.4</u>	118	45.0	120	44.4	101	52.5	<u>101</u>	<u>52.5</u>	102	52.3
454.calculix	<u>192</u>	<u>43.0</u>	192	42.9	192	43.0	186	44.3	<u>187</u>	<u>44.2</u>	187	44.1
459.GemsFDTD	<u>217</u>	<u>48.9</u>	217	48.9	217	48.9	218	48.6	217	48.9	<u>217</u>	<u>48.8</u>
465.tonto	237	41.5	234	42.1	<u>235</u>	<u>41.9</u>	218	45.1	220	44.8	<u>219</u>	<u>44.9</u>
470.lbm	130	106	<u>129</u>	<u>106</u>	129	107	130	106	<u>129</u>	<u>106</u>	129	107
481.wrf	145	77.2	<u>145</u>	<u>77.2</u>	145	77.2	145	77.2	<u>145</u>	<u>77.2</u>	145	77.2
482.sphinx3	388	50.2	<u>386</u>	<u>50.6</u>	382	51.0	388	50.2	<u>386</u>	<u>50.6</u>	382	51.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Settings:  
Turbo Mode enabled in BIOS  
C-State enabled in BIOS  
Sysinfo program /root/SPECcpu1.2/config/sysinfo.rev6800  
\$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3  
running on localhost.localdomain Sun Oct 14 06:30:51 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

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## Platform Notes (Continued)

```

From /proc/cpuinfo
model name : Intel(R) Core(TM) i3-3240 CPU @ 3.40GHz
 1 "physical id"s (chips)
 4 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 2
  siblings  : 4
  physical 0: cores 0 1
  cache size : 3072 KB

From /proc/meminfo
MemTotal:      16322724 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.2 (Santiago)

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
Linux localhost.localdomain 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13
EST 2011 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Oct 11 15:51

SPEC is set to: /root/SPECcpul.2
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/mapper/VolGroup-lv_root
                ext4      50G   32G   15G   69% /

Additional information from dmidecode:
Memory:
  2x Micron 18JSF1G72AZ-1G6D1 8 GB 1600 MHz 2 rank

(End of data from sysinfo program)

```

## General Notes

Environment variables set by runspec before the start of the run:

```

KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/root/SPECcpul.2/libs/32:/root/SPECcpul.2/libs/64"
OMP_NUM_THREADS = "2"

```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB

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## General Notes (Continued)

memory using RHEL5.5  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:  
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

C++ benchmarks:  
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

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## Base Optimization Flags (Continued)

Fortran benchmarks:  
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:  
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:  
433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:  
444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

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## Peak Optimization Flags (Continued)

447.dealll: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel  
-static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

### Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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