



# SPEC® CFP2006 Result

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## IBM Corporation

IBM System x3100 M4  
(Intel Core i3-3220T, 2.80 GHz)

SPECfp®2006 = 48.2

SPECfp\_base2006 = 47.2

CPU2006 license: 11

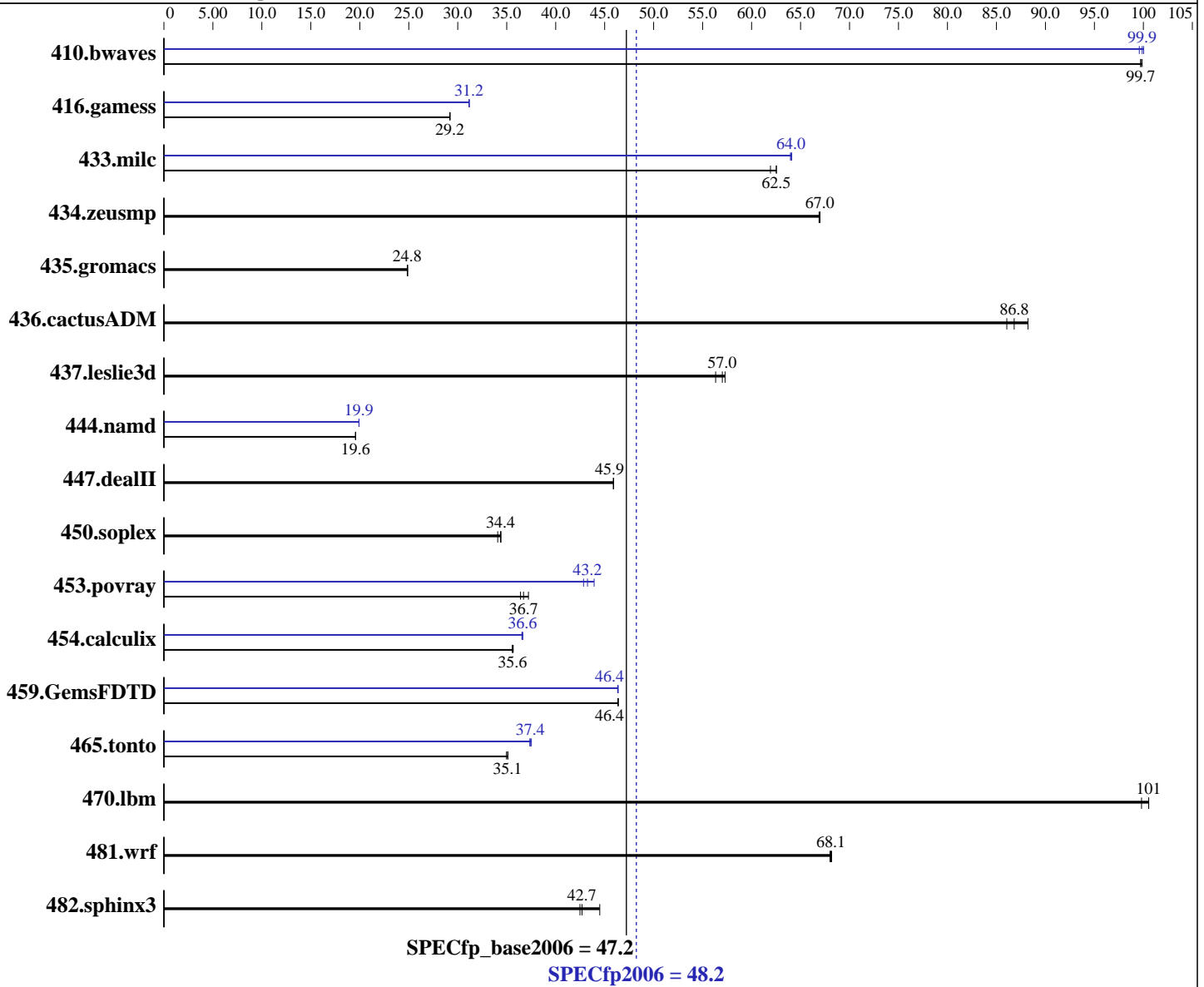
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Oct-2012

Hardware Availability: Sep-2012

Software Availability: Dec-2011



### Hardware

CPU Name: Intel Core i3-3220T  
 CPU Characteristics:  
 CPU MHz: 2800  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)  
 2.6.32-220.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;  
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4

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L3 Cache: 3 MB I+D on chip per chip  
Other Cache: None  
Memory: 16 GB (2 x 8 GB 2Rx8 PC3-12800E-11, ECC)  
Disk Subsystem: 1 x 250 GB SATA, 7200 RPM  
Other Hardware: None

System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	136	99.7	136	99.9	<b><u>136</u></b>	<b><u>99.7</u></b>	136	100	136	99.6	<b><u>136</u></b>	<b><u>99.9</u></b>
416.gamess	671	29.2	670	29.2	<b><u>670</u></b>	<b><u>29.2</u></b>	<b><u>628</u></b>	<b><u>31.2</u></b>	628	31.2	629	31.2
433.milc	<b><u>147</u></b>	<b><u>62.5</u></b>	148	61.9	147	62.5	144	64.0	<b><u>143</u></b>	<b><u>64.0</u></b>	143	64.1
434.zeusmp	136	66.9	136	67.0	<b><u>136</u></b>	<b><u>67.0</u></b>	136	66.9	136	67.0	<b><u>136</u></b>	<b><u>67.0</u></b>
435.gromacs	<b><u>287</u></b>	<b><u>24.8</u></b>	287	24.9	287	24.8	<b><u>287</u></b>	<b><u>24.8</u></b>	287	24.9	287	24.8
436.cactusADM	135	88.2	<b><u>138</u></b>	<b><u>86.8</u></b>	139	86.1	135	88.2	<b><u>138</u></b>	<b><u>86.8</u></b>	139	86.1
437.leslie3d	167	56.3	164	57.3	<b><u>165</u></b>	<b><u>57.0</u></b>	167	56.3	164	57.3	<b><u>165</u></b>	<b><u>57.0</u></b>
444.namd	410	19.6	410	19.6	<b><u>410</u></b>	<b><u>19.6</u></b>	<b><u>403</u></b>	<b><u>19.9</u></b>	403	19.9	403	19.9
447.dealII	249	45.9	249	45.9	<b><u>249</u></b>	<b><u>45.9</u></b>	249	45.9	249	45.9	<b><u>249</u></b>	<b><u>45.9</u></b>
450.soplex	<b><u>243</u></b>	<b><u>34.4</u></b>	245	34.1	242	34.4	<b><u>243</u></b>	<b><u>34.4</u></b>	245	34.1	242	34.4
453.povray	146	36.4	<b><u>145</u></b>	<b><u>36.7</u></b>	143	37.2	124	42.8	121	43.9	<b><u>123</u></b>	<b><u>43.2</u></b>
454.calculix	<b><u>232</u></b>	<b><u>35.6</u></b>	231	35.7	232	35.6	226	36.5	225	36.6	<b><u>225</u></b>	<b><u>36.6</u></b>
459.GemsFDTD	229	46.3	229	46.4	<b><u>229</u></b>	<b><u>46.4</u></b>	<b><u>229</u></b>	<b><u>46.4</u></b>	229	46.4	229	46.3
465.tonto	281	35.0	280	35.1	<b><u>280</u></b>	<b><u>35.1</u></b>	263	37.4	262	37.5	<b><u>263</u></b>	<b><u>37.4</u></b>
470.lbm	138	99.8	137	101	<b><u>137</u></b>	<b><u>101</u></b>	138	99.8	137	101	<b><u>137</u></b>	<b><u>101</u></b>
481.wrf	164	68.0	<b><u>164</u></b>	<b><u>68.1</u></b>	164	68.2	164	68.0	<b><u>164</u></b>	<b><u>68.1</u></b>	164	68.2
482.sphinx3	<b><u>457</u></b>	<b><u>42.7</u></b>	459	42.5	438	44.5	<b><u>457</u></b>	<b><u>42.7</u></b>	459	42.5	438	44.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Settings:

Turbo Mode enabled in BIOS

C-State enabled in BIOS

Sysinfo program /root/SPECcpu1.2/config/sysinfo.rev6800

\$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3

running on localhost.localdomain Wed Oct 24 15:09:21 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

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<http://www.spec.org/>

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**IBM Corporation**

**SPECfp2006 = 48.2**

IBM System x3100 M4  
(Intel Core i3-3220T, 2.80 GHz)

**SPECfp\_base2006 = 47.2**

**CPU2006 license:** 11  
**Test sponsor:** IBM Corporation  
**Tested by:** IBM Corporation

**Test date:** Oct-2012  
**Hardware Availability:** Sep-2012  
**Software Availability:** Dec-2011

## Platform Notes (Continued)

```

From /proc/cpuinfo
model name : Intel(R) Core(TM) i3-3220T CPU @ 2.80GHz
 1 "physical id"s (chips)
 4 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 2
  siblings  : 4
  physical 0: cores 0 1
cache size : 3072 KB

From /proc/meminfo
MemTotal:      16322724 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.2 (Santiago)

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
Linux localhost.localdomain 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13
EST 2011 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Oct 22 14:07

SPEC is set to: /root/SPECcpul.2
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/mapper/VolGroup-lv_root
                ext4      50G   38G   9.5G  80% /

Additional information from dmidecode:
Memory:
 2x Micron 18JSF1G72AZ-1G6D1 8 GB 1600 MHz 2 rank

(End of data from sysinfo program)

```

## General Notes

Environment variables set by runspec before the start of the run:

```

KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/root/SPECcpul.2/libs/32:/root/SPECcpul.2/libs/64"
OMP_NUM_THREADS = "2"

```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB

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## General Notes (Continued)

memory using RHEL5.5  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:  
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

C++ benchmarks:  
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

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## Base Optimization Flags (Continued)

Fortran benchmarks:

`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:

`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias`

## Peak Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias`

470.lbm: `basepeak = yes`

482.sphinx3: `basepeak = yes`

C++ benchmarks:

444.namd: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32`

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## Peak Optimization Flags (Continued)

447.deallI: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel  
-static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

### Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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