



# SPEC<sup>®</sup> CINT2006 Result

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## NEC Corporation

SPECint<sup>®</sup>\_rate2006 = 286

### Express5800/B120d (Intel Xeon E5-2450)

SPECint\_rate\_base2006 = 275

CPU2006 license: 9006

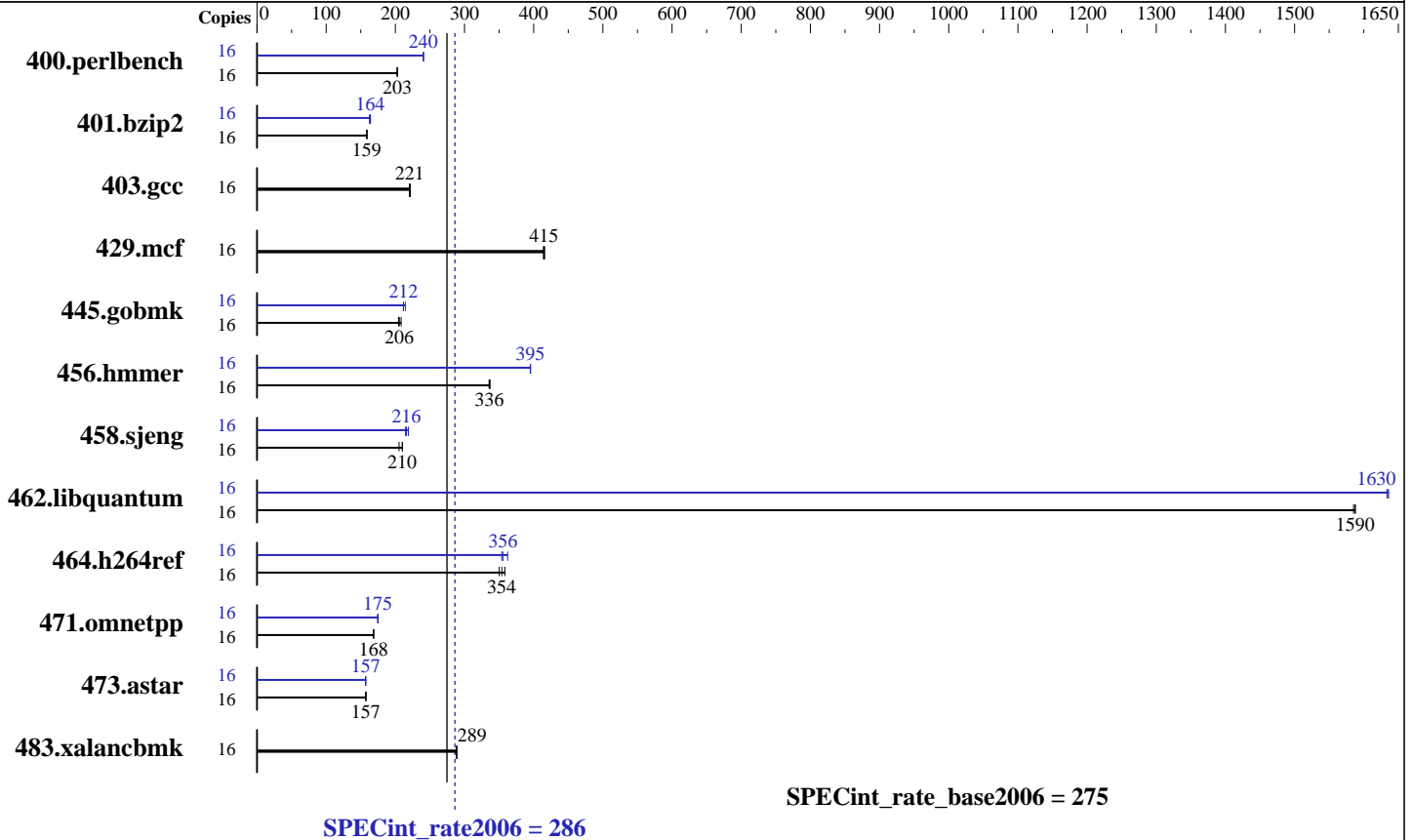
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Oct-2012

Hardware Availability: Oct-2012

Software Availability: Feb-2012



### Hardware

CPU Name: Intel Xeon E5-2450  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.90 GHz  
 CPU MHz: 2100  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 1 chip, 8 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 20 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (6 x 8 GB 2Rx4 PC3L-12800R-11, ECC)  
 Disk Subsystem: 1 x 500 GB SATA, 7200 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)  
 Kernel 2.6.32-220.el6.x86\_64  
 Compiler: C/C++: Version 12.1.3.293 of Intel C++ Studio XE for Linux;  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1



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### Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	770	203	<u>771</u>	<u>203</u>	772	203	16	651	240	649	241	<u>650</u>	<u>240</u>
401.bzip2	16	970	159	973	159	<u>972</u>	<u>159</u>	16	942	164	<u>942</u>	<u>164</u>	950	163
403.gcc	16	581	222	585	220	<u>583</u>	<u>221</u>	16	581	222	585	220	<u>583</u>	<u>221</u>
429.mcf	16	351	416	<u>352</u>	<u>415</u>	352	414	16	351	416	<u>352</u>	<u>415</u>	352	414
445.gobmk	16	805	208	820	205	<u>816</u>	<u>206</u>	16	793	212	783	214	<u>792</u>	<u>212</u>
456.hammer	16	<u>444</u>	<u>336</u>	445	336	443	337	16	378	395	<u>378</u>	<u>395</u>	377	395
458.sjeng	16	943	205	921	210	<u>922</u>	<u>210</u>	16	902	215	885	219	<u>896</u>	<u>216</u>
462.libquantum	16	209	1590	<u>209</u>	<u>1590</u>	209	1590	16	<u>203</u>	<u>1630</u>	203	1630	203	1640
464.h264ref	16	988	358	<u>1000</u>	<u>354</u>	1011	350	16	1001	354	977	362	<u>996</u>	<u>356</u>
471.omnetpp	16	593	169	594	168	<u>593</u>	<u>168</u>	16	<u>572</u>	<u>175</u>	573	175	572	175
473.astar	16	<u>714</u>	<u>157</u>	716	157	712	158	16	716	157	715	157	<u>715</u>	<u>157</u>
483.xalancbmk	16	382	289	<u>382</u>	<u>289</u>	383	289	16	382	289	<u>382</u>	<u>289</u>	383	289

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Platform Notes

BIOS Settings:  
Energy Performance: Performance  
Memory Voltage: 1.5 V

### General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64"

Added glibc-static-2.12-1.47.el6.x86\_64.rpm  
to enable static linking

Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

Filesystem page cache cleared with:  
echo 1> /proc/sys/vm/drop\_caches

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## General Notes (Continued)

runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:  
icc -m32

C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
-Wl,-z,muldefs -L/opt/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

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## Peak Compiler Invocation (Continued)

456.hmmr: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

456.hmmr: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -opt-mem-layout-trans=3

456.hmmr: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32

462.libquantum: -xAVX -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

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## Peak Optimization Flags (Continued)

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/opt/SmartHeap\_8.1/lib -lsmarheap

473.astar: -xAVX -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3 -Wl,-z,muldefs  
-L/opt/SmartHeap\_8.1/lib -lsmarheap

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html>  
<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120d-RevA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml>  
<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120d-RevA.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.  
Report generated on Thu Jul 24 14:05:05 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 20 November 2012.