SPEC® CFP2006 Result

Hewlett-Packard Company

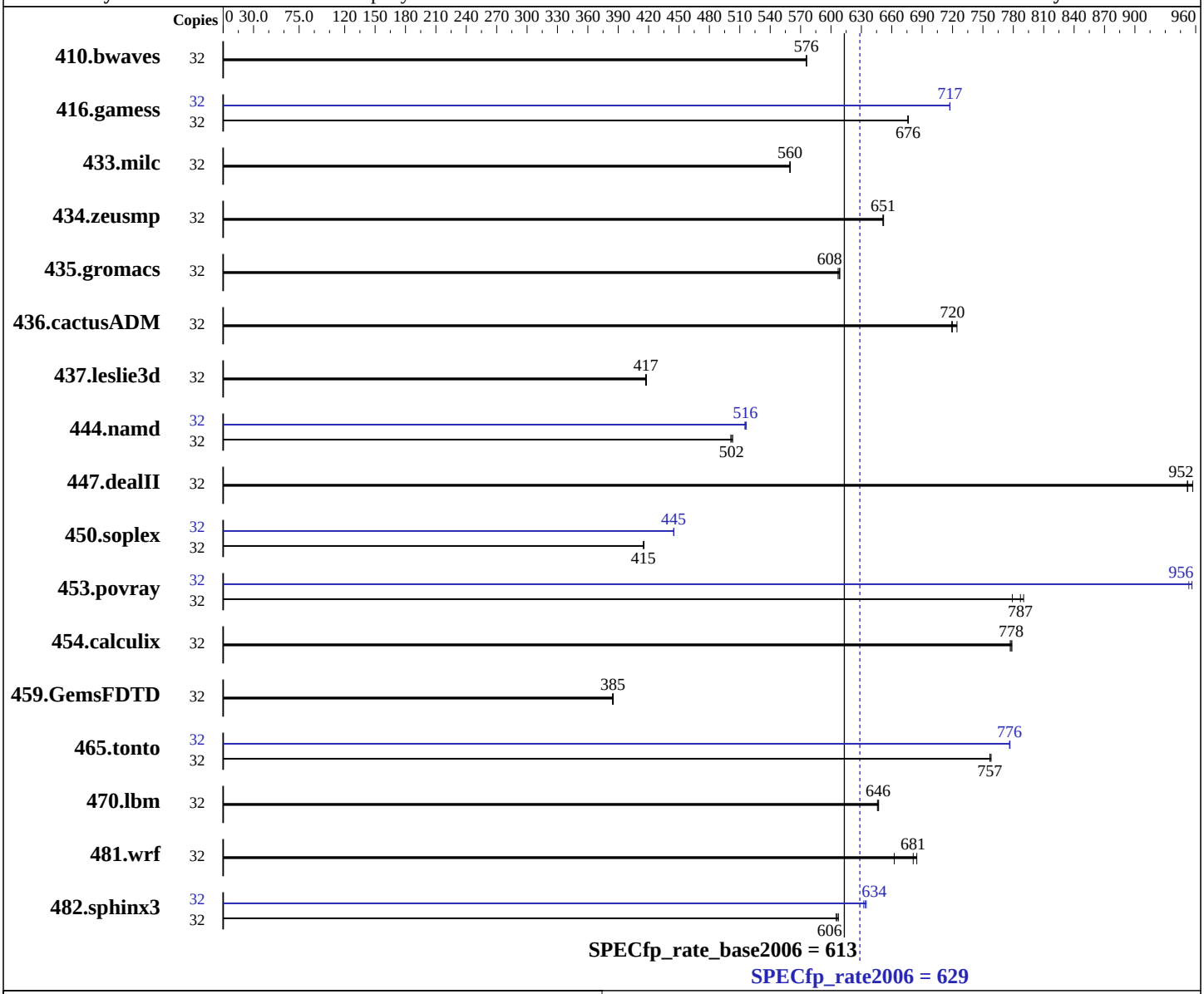
ProLiant DL580 G7
(2.66 GHz, Intel Xeon E7-8837)

SPECfp®_rate2006 = 629

SPECfp_rate_base2006 = 613

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Aug-2012
Hardware Availability: Dec-2011
Software Availability: Oct-2011



Hardware

CPU Name:	Intel Xeon E7-8837
CPU Characteristics:	Intel Turbo Boost Technology up to 2.80 GHz
CPU MHz:	2667
FPU:	Integrated
CPU(s) enabled:	32 cores, 4 chips, 8 cores/chip
CPU(s) orderable:	2,4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	SUSE Linux Enterprise Server 11 SP1 (x86_64) 2.6.32.12-0.7-default
Compiler:	C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux; Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)

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L3 Cache: 24 MB I+D on chip per chip
Other Cache: None
Memory: 1 TB (64 x 16 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem: 5 x 900 GB 6G SAS 10K, RAID 5
Other Hardware: None

Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	32	**755**	**576**	755	576	755	576	32	**755**	**576**	755	576	755	576
416.gamess	32	926	676	**926**	**676**	927	676	32	**873**	**717**	874	717	873	718
433.milc	32	525	560	**525**	**560**	525	559	32	525	560	**525**	**560**	525	559
434.zeusmp	32	447	652	447	651	**447**	**651**	32	447	652	447	651	**447**	**651**
435.gromacs	32	376	607	**376**	**608**	375	609	32	376	607	**376**	**608**	375	609
436.cactusADM	32	**531**	**720**	528	724	532	719	32	**531**	**720**	528	724	532	719
437.leslie3d	32	720	418	721	417	**721**	**417**	32	720	418	721	417	**721**	**417**
444.namd	32	512	501	510	503	**511**	**502**	32	**498**	**516**	497	516	498	515
447.dealII	32	385	952	382	957	**384**	**952**	32	385	952	382	957	**384**	**952**
450.soplex	32	**643**	**415**	642	415	643	415	32	**600**	**445**	600	445	600	445
453.povray	32	215	790	**216**	**787**	219	779	32	**178**	**956**	179	953	178	956
454.calculix	32	339	779	340	777	**339**	**778**	32	339	779	340	777	**339**	**778**
459.GemsFDTD	32	882	385	883	384	**882**	**385**	32	882	385	883	384	**882**	**385**
465.tonto	32	416	757	415	758	**416**	**757**	32	405	777	406	776	**406**	**776**
470.lbm	32	680	647	681	646	**680**	**646**	32	680	647	681	646	**680**	**646**
481.wrf	32	522	685	**525**	**681**	540	662	32	522	685	**525**	**681**	540	662
482.sphinx3	32	1031	605	**1029**	**606**	1027	607	32	982	635	**984**	**634**	986	632

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Filesystem page cache cleared with:
echo 1>       /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```

Platform Notes

```
 BIOS Configuration:
   HP Power Profile set to Maximum Performance
   Thermal Configuration set to Increased Cooling
   Collaborative Power Control set to Disabled
  Sysinfo program /cpu2006/config/sysinfo.rev6800
  $Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdff5032aaa42e583f96b07f99d3
  running on dl580g7-r88 Fri Aug 10 23:21:17 2012

  This section contains SUT (System Under Test) info as seen by
  some common utilities.  To remove or add to this section, see:
    http://www.spec.org/cpu2006/Docs/config.html#sysinfo

  From /proc/cpuinfo
     model name : Intel(R) Xeon(R) CPU E7- 8837 @ 2.67GHz
        4 "physical id"s (chips)
        32 "processors"
     cores, siblings (Caution: counting these is hw and system dependent.  The
     following excerpts from /proc/cpuinfo might not be reliable.  Use with
     caution.)
        cpu cores : 8
        siblings  : 8
        physical 0: cores 0 1 2 9 16 18 24 25
        physical 1: cores 0 1 2 8 17 18 24 25
        physical 2: cores 0 1 2 8 17 18 24 25
        physical 3: cores 0 1 8 9 16 17 24 25
     cache size : 24576 KB

  From /proc/meminfo
     MemTotal:       1058831952 kB
     HugePages_Total:       0
     Hugepagesize:       2048 kB

  From /etc/*release* /etc/*version*
     SuSE-release:
        SUSE Linux Enterprise Server 11 (x86_64)
        VERSION = 11
        PATCHLEVEL = 1

  uname -a:
     Linux dl580g7-r88 2.6.32.12-0.7-default #1 SMP 2010-05-20 11:14:20 +0200
     x86_64 x86_64 x86_64 GNU/Linux

  run-level 3 Aug 10 13:58 last=S

  SPEC is set to: /cpu2006
     Filesystem     Type    Size  Used Avail Use% Mounted on
     /dev/cciss/c0d0p2
                    ext3    3.3T   42G  3.1T   2% /

  Additional information from dmidecode:
    BIOS HP P65 12/01/2011
```

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Platform Notes (Continued)

```
(End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/cpu2006/libs/32:/cpu2006/libs/64"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RHEL5.5
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32 -ansi-alias -opt-mem-layout-trans=3`

C++ benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32 -ansi-alias -opt-mem-layout-trans=3`

Fortran benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch`

Benchmarks using both Fortran and C:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32 -ansi-alias -opt-mem-layout-trans=3`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m64`

482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):
`icpc -m64`

450.soplex: `icpc -m32`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`

Continued on next page

Peak Portability Flags (Continued)

454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -opt-mem-layout-trans=3 -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

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Peak Optimization Flags (Continued)

465.tonto: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-unroll4 -auto -inline-calloc -opt-malloc-options=3`

Benchmarks using both Fortran and C:

435.gromacs: `basepeak = yes`

436.cactusADM: `basepeak = yes`

454.calculix: `basepeak = yes`

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html`
`http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-A.20120829.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml`
`http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-A.20120829.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 13:40:18 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 23 October 2012.